RoHS

COMPLIANT

Vishay Semiconductors





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DESIGN SUPPORT TOOLS *(click logo to get started)*



PRIMARY CHARACTERISTICS						
I _{F(AV)}	3 A					
V _R	600 V					
V _F at I _F	0.99 V					
t _{rr}	50 ns					
T _J max.	175 °C					
Package	SlimSMA (DO-221AC)					
Circuit configuration	Single					

FEATURES

- Ultrafast recovery time, reduced Q_{rr}, and soft recovery
- 175 °C maximum operating junction temperature
- Low forward voltage drop
- Low leakage current
- Meets MSL level 1, per J-STD-020, LF maximum *FREE* peak of 260 °C
- Material categorization: for definitions of compliance please see <u>www.vishav.com/doc?99912</u>

DESCRIPTION / APPLICATIONS

State of the art ultrafast recovery rectifiers designed with optimized performance of forward voltage drop and ultrafast recovery time, and fast recovery.

The planar structure and the platinum doped life time control guarantee the best overall performance, ruggedness and reliability characteristics.

These devices are intended for use in snubber, output operation, inverters or as freewheeling diodes.

The extremely optimized stored charge and low recovery current minimize the switching losses and reduce power dissipation in the switching element.

ABSOLUTE MAXIMUM RATINGS						
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS		
Peak repetitive reverse voltage	V _{RRM}		600	V		
Average rectified forward current	I _{F(AV)}	T _C = 117 °C ⁽¹⁾	3	٨		
Non-repetitive peak surge current	I _{FSM}	T _J = 25 °C	43	A		
Operating junction and storage temperatures	T _J , T _{Stg}		-55 to +175	°C		

Note

⁽¹⁾ Device on PCB with 8 mm x 16 mm soldering lands

ELECTRICAL SPECIFICATIONS (T _J = 25 $^{\circ}$ C unless otherwise specified)						
PARAMETER	SYMBOL	YMBOL TEST CONDITIONS		TYP.	MAX.	UNITS
Breakdown voltage, blocking voltage	V _{BR} , V _R	I _R = 100 μA	600	-	-	
Forward voltage	V _F	I _F = 3 A	-	1.15	1.35	V
		I _F = 3 A, T _J = 150 °C	-	0.99	1.2	
	1	$V_{R} = V_{R}$ rated	-	-	3	
Reverse leakage current	I _R	T_J = 150 °C, V_R = V_R rated	-	-	100	μA
Junction capacitance	CT	V _R = 600 V	-	6.2	-	pF

Revision: 11-Sep-2018 1 Document Number: 96067 For technical questions within your region: <u>DiodesAmericas@vishay.com</u>, <u>DiodesAsia@vishay.com</u>, <u>DiodesEurope@vishay.com</u> THIS DOCUMENT IS SUBJECT TO CHANGE WITHOUT NOTICE. THE PRODUCTS DESCRIBED HEREIN AND THIS DOCUMENT ARE SUBJECT TO SPECIFIC DISCLAIMERS, SET FORTH AT <u>www.vishay.com/doc?91000</u>



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DYNAMIC RECOVERY CHARACTERISTICS ($T_J = 25$ °C unless otherwise specified)								
PARAMETER	SYMBOL	TEST CON	MIN.	TYP.	MAX.	UNITS		
		$I_F = 1.0 \text{ A}, \text{ d}I_F/\text{d}t = 50 \text{ A}/\mu\text{s}, \text{ V}_R = 30 \text{ V}$		-	45	-		
Reverse recovery time	+	$I_F = 0.5 \text{ A}, I_R = 1 \text{ A}, I_{rr} = 0.25 \text{ A}$		-	-	50		
Reverse recovery time t _{rr}	T _J = 25 °C		-	52	-	ns		
		T _J = 125 °C		-	82	-		
Peak recovery current	I _{BBM}	T _J = 25 °C	$I_F = 3 A$	-	7.3	-	А	
		T _J = 125 °C	dl _F /dt = 500 A/µs V _R = 400 V	-	10	-	A	
Reverse recovery charge	Q _{rr}	$T_J = 25 \ ^{\circ}C$	-	210	-	nC		
		T _J = 125 °C		-	400	-	no	

THERMAL - MECHANICAL SPECIFICATIONS							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS	
Maximum junction and storage temperature range	T _J , T _{Stg}		-55	-	175	°C	
Thermal resistance, junction to lead	R _{thJL}	R _{thJL} Device mounted on PCB with 8 mm x 16 mm soldering lands		16	-	°C/W	
Thermal resistance, junction to ambient	R _{thJA}	Device mounted on PCB with 3 mm x 3 mm soldering lands	-	115	-	C/W	
Approximate Weight				0.03		g	
Approximate Weight			0.0011			oz.	
Marking device		Case style SlimSMA (DO-221AC)		31	J6		

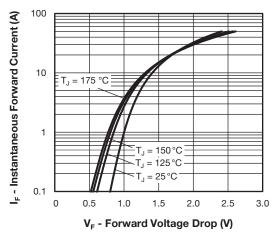
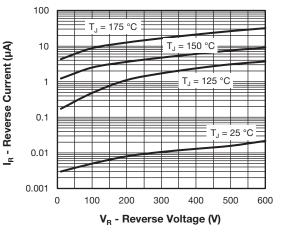
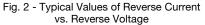
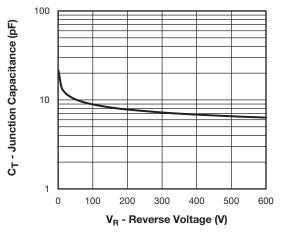


Fig. 1 - Typical Forward Voltage Drop Characteristics





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Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

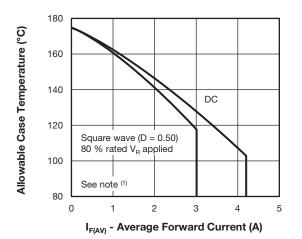


Fig. 4 - Maximum Allowable Case Temperature vs. Average Forward Current

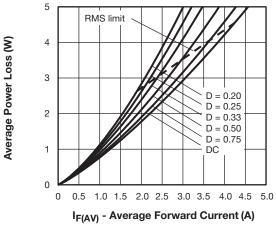


Fig. 5 - Forward Power Loss Characteristic

Note

⁽¹⁾ Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$;

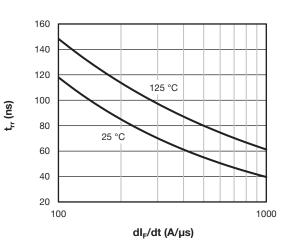
Pd = forward power loss = $I_{F(AV)} \times V_{FM}$ at ($I_{F(AV)}/D$) (see Fig. 6); Pd_{REV} = inverse power loss = $V_{R1} \times I_R$ (1 - D); I_R at V_{R1} = rated V_R

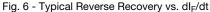
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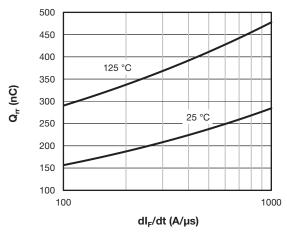


Fig. 7 - Typical Stored Charge vs. dl_F/dt

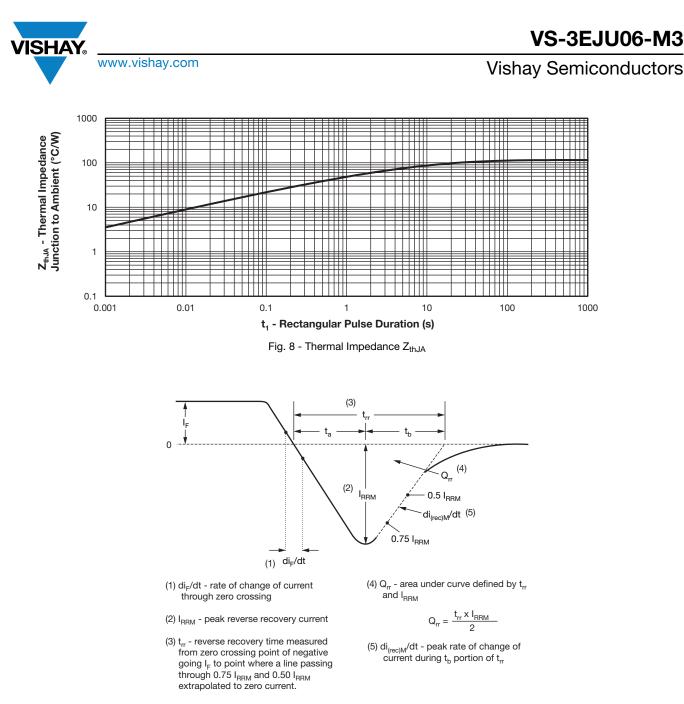


Fig. 9 - Reverse Recovery Waveform and Definitions

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Device code	VS-	3	E	J	U	06	-M3
		2	3	4	5	6	7
	Ľ.		•	niconduo	•	oduct	
				ng (3 = : iguratior			
	_		single c				
	브			A packa	ige		
	5		cess typ ultrafas	e, t recove	ery		
	6	- Voli	tage coo	le (06 =	600 V)		
	7	M3	s = halog	gen-free	, RoHS-	complia	ant, and

ORDERING INFORMATION (Example)							
PREFERRED P/N	QUANTITY PER REEL	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION				
VS-3EJU06-M3/6A	3500	3500	7" diameter plastic tape and reel				
VS-3EJU06-M3/6B	14 000	14 000	13" diameter plastic tape and reel				

LINKS TO RELATED DOCUMENTS						
Dimensions	www.vishay.com/doc?95771					
Part marking information	www.vishay.com/doc?95562					
Packaging information	www.vishay.com/doc?88869					





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