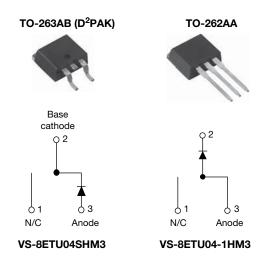
VS-8ETU04SHM3, VS-8ETU04-1HM3



Vishay Semiconductors

Ultrafast Rectifier, 8 A FRED Pt[®]



PRODUCT SUMMARY							
Package	TO-263AB (D ² PAK), TO-262AA						
I _{F(AV)}	8 A						
V _R	400 V						
V _F at I _F	0.94 V						
t _{rr} typ.	35 ns						
T _J max.	175 °C						
Diode variation	Single die						

FEATURES

- · Ultrafast recovery time
- · Low forward voltage drop
- Low leakage current
- 175 °C operating junction temperature
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C



FREE

- AEC-Q101 qualified, meets JESD 201, class 1 whisker test
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

DESCRIPTION / APPLICATIONS

Vishay Semiconductors FRED Pt® series are the state of the art ultrafast recovery rectifiers specifically designed with optimized performance of forward voltage drop and ultrafast recovery time.

The planar structure and the platinum doped life time control, guarantee the best overall performance, ruggedness and reliability characteristics.

These devices are intended for use in the output rectification stage of SMPS, UPS, DC/DC converters as well as freewheeling diode in low voltage inverters and chopper motor drives.

Their extremely optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element and snubbers.

ABSOLUTE MAXIMUM RATINGS								
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS				
Repetitive peak reverse voltage	V _{RRM}		400	V				
Average rectified forward current	I _{F(AV)}	T _C = 155 °C	8					
Non-repetitive peak surge current	I _{FSM}	T _C = 25 °C	100	А				
Repetitive peak forward current	I _{FRM}		16					
Operating junction and storage temperatures	T _J , T _{Stg}		-55 to +175	°C				

ELECTRICAL SPECIFICATIONS ($T_J = 25 \ ^{\circ}C$ unless otherwise specified)								
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS		
Breakdown voltage, blocking voltage	V _{BR} , V _R	I _R = 100 μA	400	-	-			
Conversional scales on a	V _F	I _F = 8 A	-	1.19	1.3	V		
Forward voltage	۷F	I _F = 8 A, T _J = 150 °C	-	0.94	1.0			
Deveree leekege eurrent		$V_{R} = V_{R}$ rated	-	0.2	10			
Reverse leakage current	I _R	$T_J = 150 \text{ °C}, V_R = V_R \text{ rated}$	-	20	500	μA		
Junction capacitance	CT	V _R = 400 V	-	14	-	pF		
Series inductance	Ls	Measured lead to lead 5 mm from package body	-	8.0	-	nH		



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DYNAMIC RECOVERY CHARACTERISTICS ($T_J = 25$ °C unless otherwise specified)									
PARAMETER	SYMBOL	TEST CO	NDITIONS	MIN.	TYP.	MAX.	UNITS		
		I _F = 1.0 A, dI _F /dt =	50 A/μs, V _R = 30 V	-	35	60			
Reverse recovery time	t _{rr}	T _J = 25 °C		-	43	-	ns		
		T _J = 125 °C		-	67	-			
Doole recovery ourrept	I _{RRM}	T _J = 25 °C	I _F = 8 A dI _F /dt = 200 A/μs V _B = 200 V	-	2.8	-	А		
Peak recovery current		T _J = 125 °C		-	6.3	-	A		
	Q _{rr}	T _J = 25 °C		-	60	-	nC		
Reverse recovery charge		T _J = 125 °C		-	210	-			

THERMAL - MECHANICAL SPECIFICATIONS								
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS		
Maximum junction and storage temperature range	T _J , T _{Stg}		-55	-	175	°C		
Thermal resistance, junction to case	R _{thJC}		-	1.8	2.0			
Thermal resistance, junction to ambient	R _{thJA}	Typical socket mount	-	-	50	°C/W		
Thermal resistance, case to heatsink	R _{thCS}	Mounting surface, flat, smooth and greased	-	0.5	-	0,11		
W/aiaht			-	2.0	-	g		
Weight			-	0.07	-	oz.		
Mounting torque			6.0 (5.0)	-	12 (10)	kgf · cm (lbf · in)		
Marking davias		Case style TO-263AB (D ² PAK)		8ETU	04SH	•		
Marking device		Case style TO-262		8ETU04-1H				

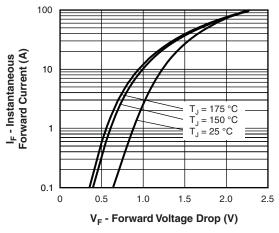
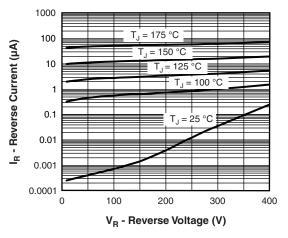


Fig. 1 - Typical Forward Voltage Drop Characteristics







VS-8ETU04SHM3, VS-8ETU04-1HM3

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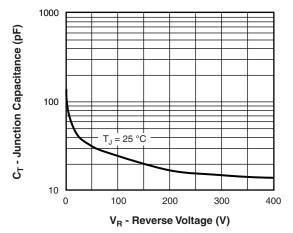


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

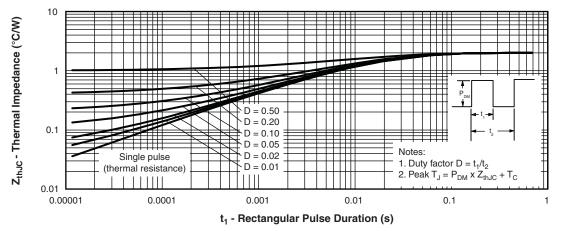


Fig. 4 - Maximum Thermal Impedance ZthJC Characteristics

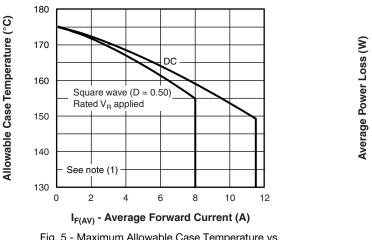
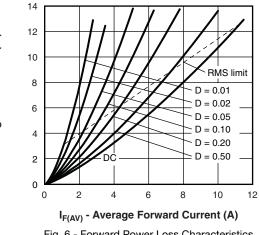


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current



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VS-8ETU04SHM3, VS-8ETU04-1HM3

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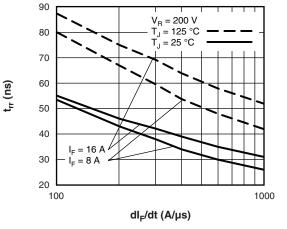
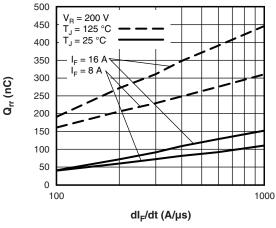
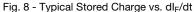


Fig. 7 - Typical Reverse Recovery Time vs. dl_F/dt





Note

- ⁽¹⁾ Formula used: $T_C = T_J (Pd + Pd_{REV}) \times R_{thJC}$;
 - $\begin{array}{l} \mbox{Pd} = \mbox{Forward power loss} = \mbox{I}_{F(AV)} \times \mbox{V}_{FM} \mbox{ at } (\mbox{I}_{F(AV)}/\mbox{D}) \mbox{ (see fig. 6);} \\ \mbox{Pd}_{REV} = \mbox{Inverse power loss} = \mbox{V}_{R1} \times \mbox{I}_{R} \mbox{ (1 D); I}_{R} \mbox{ at } \mbox{V}_{R1} = \mbox{Rated } \mbox{V}_{R} \end{array}$

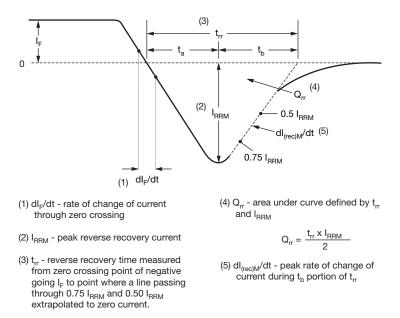


Fig. 9 - Reverse Recovery Waveform and Definitions



Vishay Semiconductors

ORDERING INFORMATION TABLE

Device code	VS-	8	Е	т	U	04	S	TRL	н	М3
		2	(3)	(4)	(5)	(6)			(9)	(10)
		\bigcirc	\bigcirc	\bigcirc	0	\bigcirc	\bigcirc	\bigcirc	\bigcirc	\bigcirc
	<u> </u>	· Visł	nay Sen	niconduo	ctors pro	oduct				
	2 -	Cur	rent rati	ng (8 A)	1					
	3 -	E =	single c	liode						
	4 -	Т=	то-220	, D ² PAł	<					
	5 -	- U =	ultrafas	t recove	ery					
	6 -	- Volt	age rati	ng (04 =	= 400 V)					
	7 -	• S	= D ² PA	K						
		• -1	= TO-2	62						
	8 -									
			• None = tube (50 pieces)							
			• TRL = tape and reel (left oriented, for D ² PAK package)							
			 TRR = tape and reel (right oriented, for D²PAK package) 							
	9	- H=	AEC-Q	101 qua	alified					
	10	- M3	= halog	en-free,	RoHS-	complia	nt, and	termina	tions le	ad (Pb)-

ORDERING INFORMATION (Example)								
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION					
VS-8ETU04SHM3	50	1000	Antistatic plastic tube					
VS-8ETU04STRRHM3	800	800	13"diameter reel					
VS-8ETU04STRLHM3	800	800	13"diameter plastic reel					
VS-8ETU04-1HM3	50	1000	Antistatic plastic tube					

LINKS TO RELATED DOCUMENTS							
Dimensions	TO-263AB (D ² PAK)	www.vishay.com/doc?95046					
Dimensions	TO-262AA	www.vishay.com/doc?95419					
Part marking information	TO-263AB (D ² PAK)	www.vishay.com/doc?95444					
Fan marking mormation	TO-262AA	www.vishay.com/doc?95443					
Packaging information	TO-263AB (D ² PAK)	www.vishay.com/doc?95032					

Outline Dimensions



D²PAK

DIMENSIONS in millimeters and inches

www.vishay.com

SHA



SYMBOL	MILLIM	IETERS	INC	HES	NOTES	SYMBOL	MILLIM	IETERS	INC	HES	NOTES
STMBOL	MIN.	MAX.	MIN.	MAX.	NOTES	STWDUL	MIN.	MAX.	MIN.	MAX.	NOTES
А	4.06	4.83	0.160	0.190		D1	6.86	8.00	0.270	0.315	3
A1	0.00	0.254	0.000	0.010		E	9.65	10.67	0.380	0.420	2, 3
b	0.51	0.99	0.020	0.039		E1	7.90	8.80	0.311	0.346	3
b1	0.51	0.89	0.020	0.035	4	е	2.54	BSC	0.100	BSC	
b2	1.14	1.78	0.045	0.070		Н	14.61	15.88	0.575	0.625	
b3	1.14	1.73	0.045	0.068	4	L	1.78	2.79	0.070	0.110	
С	0.38	0.74	0.015	0.029		L1	-	1.65	-	0.066	3
c1	0.38	0.58	0.015	0.023	4	L2	1.27	1.78	0.050	0.070	
c2	1.14	1.65	0.045	0.065		L3	0.25	BSC	0.010	BSC	
D	8.51	9.65	0.335	0.380	2	L4	4.78	5.28	0.188	0.208	

Notes

⁽¹⁾ Dimensioning and tolerancing per ASME Y14.5 M-1994

⁽²⁾ Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body

⁽³⁾ Thermal pad contour optional within dimension E, L1, D1 and E1

⁽⁴⁾ Dimension b1 and c1 apply to base metal only

⁽⁵⁾ Datum A and B to be determined at datum plane H

⁽⁶⁾ Controlling dimension: inch

⁽⁷⁾ Outline conforms to JEDEC[®] outline TO-263AB

Revision: 08-Jul-15

1

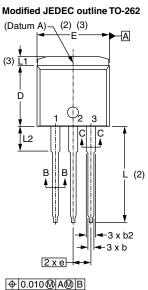


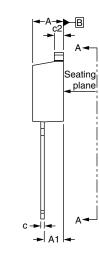
Outline Dimensions

Vishay Semiconductors

TO-262

DIMENSIONS in millimeters and inches

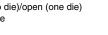


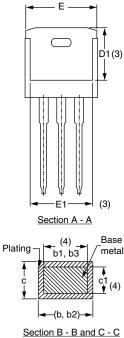


Lead assignments



Diodes 1. - Anode (two die)/open (one die) 2., 4. - Cathode 3. - Anode





Scale: None

MILLIMETERS		INC	INCHES			
SYMBOL	MIN.	MAX.	MIN.	MAX.	NOTES	
А	4.06	4.83	0.160	0.190		
A1	2.03	3.02	0.080	0.119		
b	0.51	0.99	0.020	0.039		
b1	0.51	0.89	0.020	0.035	4	
b2	1.14	1.78	0.045	0.070		
b3	1.14	1.73	0.045	0.068	4	
С	0.38	0.74	0.015	0.029		
c1	0.38	0.58	0.015	0.023	4	
c2	1.14	1.65	0.045	0.065		
D	8.51	9.65	0.335	0.380	2	
D1	6.86	8.00	0.270	0.315	3	
E	9.65	10.67	0.380	0.420	2, 3	
E1	7.90	8.80	0.311	0.346	3	
е	2.54	BSC	0.10	0 BSC		
L	13.46	14.10	0.530	0.555		
L1	-	1.65	-	0.065	3	
L2	3.56	3.71	0.140	0.146		

Notes

Revision: 04-Oct-10

⁽¹⁾ Dimensioning and tolerancing as per ASME Y14.5M-1994

⁽²⁾ Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body

⁽⁴⁾ Dimension b1 and c1 apply to base metal only

(5) Controlling dimension: inches

(6) Outline conform to JEDEC TO-262 except A1 (maximum), b (minimum) and D1 (minimum) where dimensions derived the actual package outline

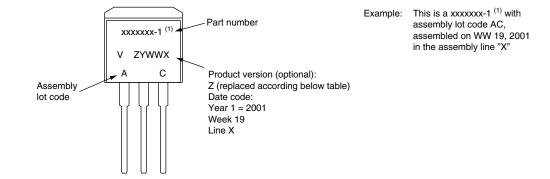
⁽³⁾ Thermal pad contour optional within dimension E, L1, D1 and E1

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Vishay Semiconductors

TO-262



Note

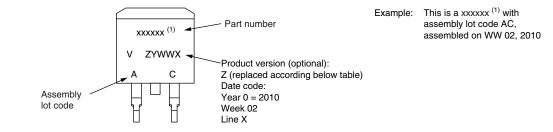
⁽¹⁾ If part number contain "H" as last digit, product is AEC-Q101 qualified

ENVIRONMENTAL NAMING CODE (Z)	PRODUCT DEFINITION			
A	Termination lead (Pb)-free			
В	Totally lead (Pb)-free			
E	RoHS-compliant and termination lead (Pb)-free			
F	RoHS-compliant and totally lead (Pb)-free			
М	Halogen-free, RoHS-compliant and termination lead (Pb)-free			
Ν	Halogen-free, RoHS-compliant and totally lead (Pb)-free			
G Green				



Vishay Semiconductors

D²PAK



Note

⁽¹⁾ If part number contain "H" as last digit, product is AEC-Q101 qualified

ENVIRONMENTAL NAMING CODE (Z)	PRODUCT DEFINITION			
A	Termination lead (Pb)-free			
В	Totally lead (Pb)-free			
E	RoHS-compliant and termination lead (Pb)-free			
F	RoHS-compliant and totally lead (Pb)-free			
М	Halogen-free, RoHS-compliant, and termination lead (Pb)-free			
N	Halogen-free, RoHS-compliant, and totally lead (Pb)-free			
G Green				



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