# Application

- · Motor drive
- · Inverter, Converter
- · Photovoltaics, wind power generation.
- · Induction heating equipment.

### Features

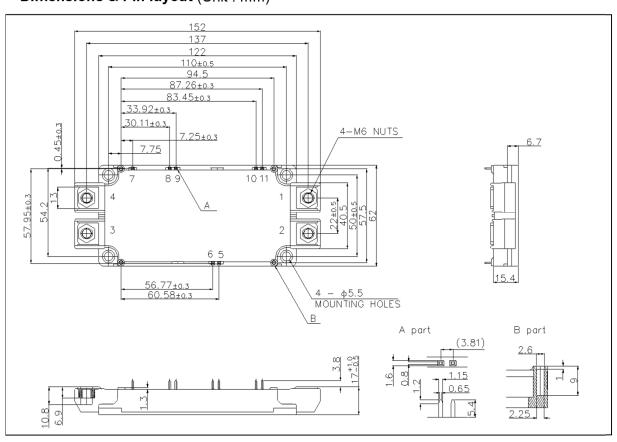
- 1) Low surge, low switching loss.
- 2) High-speed switching possible.
- 3) Reduced temperature dependence.

# PCircuit diagram 7 9 8 10 NTC 11 NTC

### Construction

This product is a half bridge module consisting of SiC-DMOSFET and SiC-SBD from ROHM.

# ●Dimensions & Pin layout (Unit: mm)

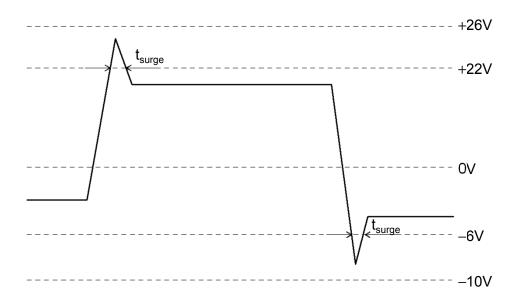


# ●Absolute maximum ratings (T<sub>j</sub> = 25°C)

Parameter	Symbol	Conditions	Limit	Unit	
Drain-source voltage	$V_{DSS}$	G-S short	1200		
Gate-source voltage(+)	$V_{GSS}$	D-S short	22	V	
Gate-source voltage(-)	V GSS	D-3 SHOIL	<b>–6</b>	\ \ \	
G - S Voltage (t <sub>surge</sub> <300nsec)	$V_{GSS\_surge}$	D-S short	-10 to 26	1	
Drain current *1	$I_D$	DC (T <sub>c</sub> =60°C)	300	- A	
	I <sub>DRM</sub>	Pulse (T <sub>c</sub> =60°C) 1ms *2	600		
Source current *1	Is	DC (T <sub>c</sub> =60°C)	300 A		
	I <sub>SRM</sub>	Pulse (Tc=60°C) 1ms *2	600		
Total power disspation *3	Ptot	T <sub>c</sub> =25°C	1875	W	
Max Junction Temperature	T <sub>jmax</sub>		175		
Operating junction temperature	$T_jop$		-40 to150	°C	
Storage temperature	T <sub>stg</sub>		-40 to125		
Isolation voltage	Visol	Terminals to baseplate, f=60Hz AC 1min.	2500	Vrms	
Mounting torque		Main Terminals : M6 screw	4.5	N · m	
	_	Mounting to heat shink: M5 screw	3.5		

<sup>(\*1)</sup> Case temperature (T<sub>c</sub>) is defined on the surface of base plate just under the chips.

# Example of acceptable $V_{\text{GS}}$ waveform



<sup>(\*2)</sup> Repetition rate should be kept within the range where temperature rise if die should not exceed  $T_{j \text{ max.}}$ 

<sup>(\*3)</sup> T<sub>i</sub> is less than 175°C

# ●Electrical characteristics (T<sub>i</sub>=25°C)

Parameter	Symbol	Conditions		Min.	Тур.	Max.	Unit
Static drain-source on-state voltage	$V_{DS(on)}$	I <sub>D</sub> =300A, V <sub>GS</sub> =18V	T <sub>j</sub> =25°C	-	2.2	2.9	V
			T <sub>j</sub> =125°C	-	3.0	-	
			T <sub>j</sub> =150°C	-	3.4	4.5	
Drain cutoff current	I <sub>DSS</sub>	V <sub>DS</sub> =1200V, V <sub>GS</sub> =0V	-	-	3.2	mA	
Source-drain voltage	$V_{SD}$	V <sub>GS</sub> =0V, I <sub>S</sub> =300A	T <sub>j</sub> =25°C	-	1.6	2.1	V
			T <sub>j</sub> =125°C		2.2	-	
			T <sub>j</sub> =150°C	ı	2.4	3.2	
		V <sub>GS</sub> =18V, I <sub>S</sub> =300A	T <sub>j</sub> =25°C	-	1.4	-	
			T <sub>j</sub> =125°C		1.6	-	
			T <sub>j</sub> =150°C	ı	1.7	-	
Gate-source threshold voltage	$V_{GS(th)}$	$V_{DS}$ =10V, $I_{D}$ =68mA	V <sub>DS</sub> =10V, I <sub>D</sub> =68mA		2.7	4.0	V
Gate-source leakage current	I <sub>GSS</sub>	V <sub>GS</sub> =22V, V <sub>DS</sub> =0V		ı	-	0.5	μΑ
		$V_{GS}$ = -6V, $V_{DS}$ =0V		-0.5	-	-	
Switching characteristics	t <sub>d(on)</sub>	$V_{GS(on)}$ =18V, $V_{GS(off)}$ =0V		1	80	-	ns
	t <sub>r</sub>	V <sub>DS</sub> =600V	-	70	-		
	t <sub>rr</sub>	I <sub>D</sub> =300A	-	50	-		
	$t_{d(off)}$	$R_G$ =0.2 $\Omega$		1	250	-	
	t <sub>f</sub>	inductive load		-	65	-	
Input capacitance	Ciss	V <sub>DS</sub> =10V, V <sub>GS</sub> =0V,100	ı	32	-	nF	
Gate Registance	$R_{Gint}$	T <sub>j</sub> =25°C	1	1.6	-	Ω	
NTC Rated Resistance	R25				5.0		kΩ
NTC B Value	B50/25						K
Stray Inductance	Ls				13	-	nΗ
Creepage Distance	1	Terminal to heat sink			14.5	-	mm
		Terminal to terminal			15.0	-	mm
Clearance Distance	-	Terminal to heat sink			12.0	-	mm
		Terminal to terminal			9.0	-	mm
Junction-to-case thermal resistance		DMOS (1/2 module) *4		ı	-	0.08	K/W
		SBD (1/2 module) *4		ı	-	0.11	
Case-to-heat sink		Case to heat sink, per 1 module,			0.035	-	
Thermal resistance	· vtn(O i)	Thermal grease applie	*5				

- (\*4) Measurement of Tc is to be done at the point just under the chip.
- (\*5) Typical value is measured by using thermally conductive grease of λ=0.9W/(m K).
- (\*6) If the Product is used beyond absolute maximum ratings defined in the Specifications, as its internal structure may be dameged, please replace such Product with a new one.

<Wavelength for Switching Test>

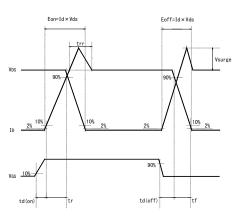
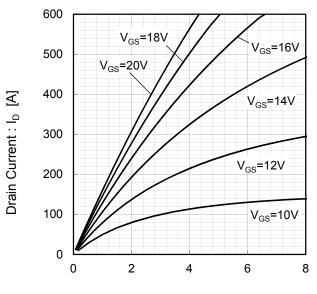
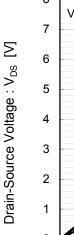
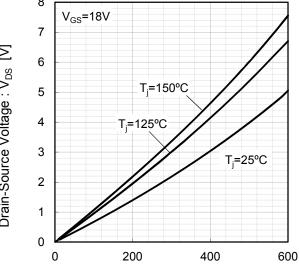


Fig.1 Typical Output Characteristics [ $T_i$ =25°C] Fig.2 Drain-Source Voltage vs. Drain Current



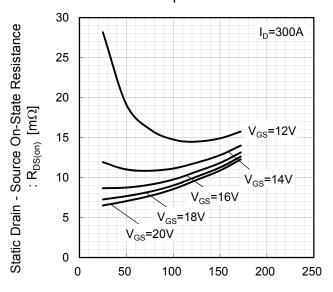




Drain-Source Voltage : V<sub>DS</sub> [V] Drain Current : I<sub>D</sub> [A]

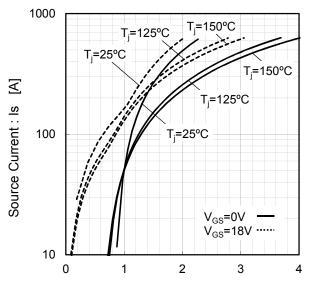
Fig.3 Drain-Source Voltage vs. Gate-Source Voltage [T<sub>i</sub>=25°C] 8 T<sub>i</sub>=25°C 7 Drain-Source Voltage: V<sub>DS</sub> [V] 6 5 4 3 I<sub>D</sub>=400A 2 I<sub>D</sub>=300A I<sub>D</sub>=200A 1 I<sub>D</sub>=150A 0 12 16 18 20 22 24 26 14 Gate-Source Voltage : V<sub>GS</sub> [V]

Fig.4 Static Drain - Source On-State Resistance vs. Junction Temperature



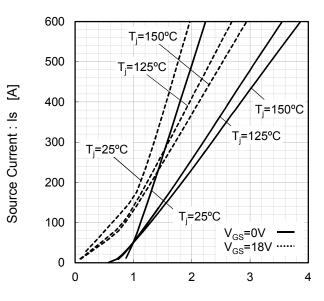
Junction Temperature : T<sub>i</sub> [°C]

Fig.5 Forward characteristic of Diode



Source-Drain Voltage :  $V_{SD}$  [V]

Fig.6 Forward characteristic of Diode



Source-Drain Voltage : V<sub>SD</sub> [V]

Fig.7 Drain Current vs. Gate-Source Voltage

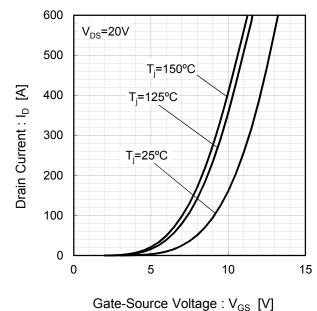
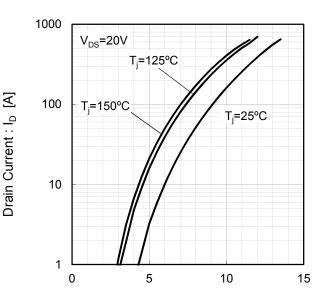


Fig.8 Drain Current vs. Gate-Source Voltage



Gate-Source Voltage : V<sub>GS</sub> [V]

Fig.9 Switching Characteristics [T<sub>i</sub>=25°C]

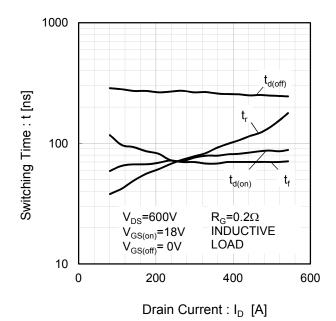
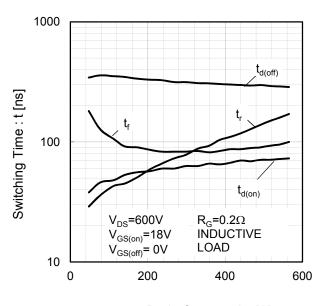


Fig.10 Switching Characteristics [T<sub>i</sub>=150°C]



Drain Current : I<sub>D</sub> [A]

Fig.11 Switching Loss vs. Drain Current [T=25°C1]

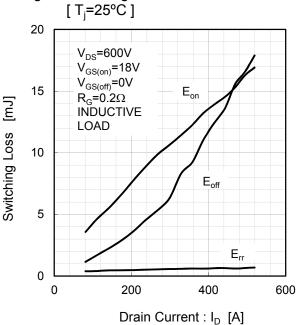
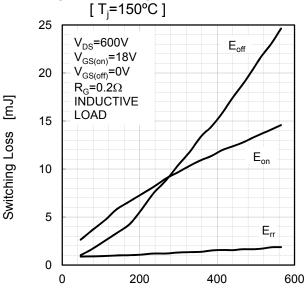
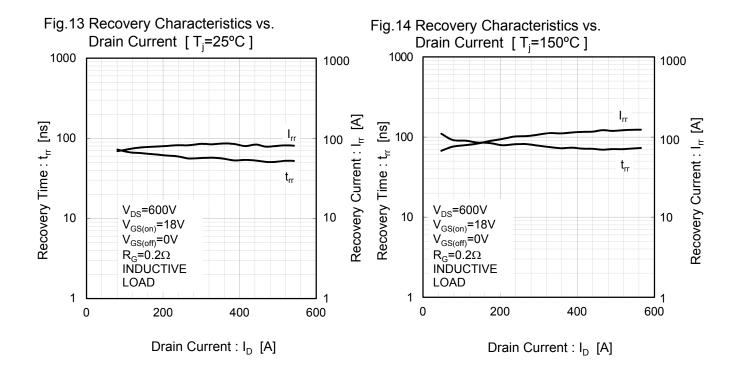
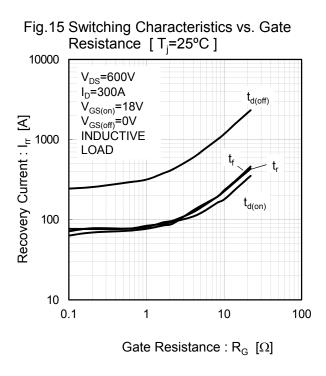


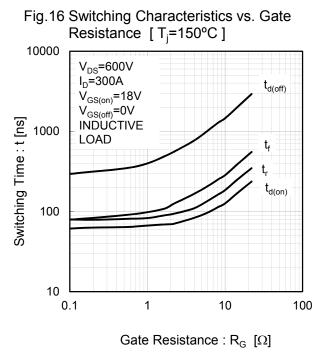
Fig.12 Switching Loss vs. Drain Current



Drain Current : I<sub>D</sub> [A]







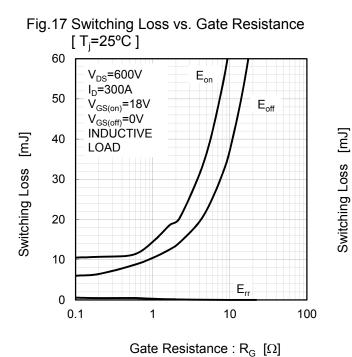
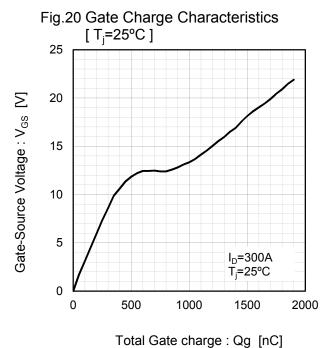
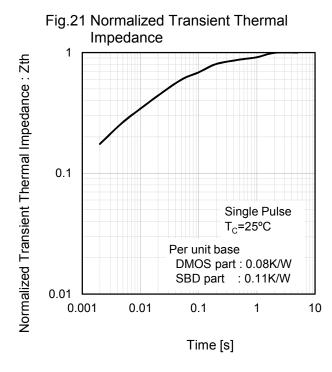


Fig.18 Switching Loss vs. Gate Resistance  $[T_i=150^{\circ}C]$ 60 V<sub>DS</sub>=600V I<sub>D</sub>=300A 50  $V_{GS(on)}$ =18V  $V_{GS(off)}$ =0V INDUCTIVE 40 LOAD 30  $E_{off}$ 20 10  $E_{rr}$ 0 0.1 10 100 Gate Resistance :  $R_G$  [ $\Omega$ ]

Fig.19 Typical Capacitance vs. Drain-Source Voltage 1.E-07 Ciss 1.E-08 Capasitance: C [F] Coss 1.E-09 T;=25°C Crss  $t_{GS} = 0V$ 1.E-10 0.01 0.1 1 10 100 1000 Drain-Source Voltage :  $V_{DS}$  [V]







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