## **Vishay Semiconductors**



## **FEATURES**

- Ultrafast recovery time, reduced Q<sub>rr</sub> and soft recovery
- 175 °C maximum operating junction temperature
- For PFC CRM/CCM, snubber operation
- Low forward voltage drop
- Low leakage current
- · Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

#### **DESCRIPTION / APPLICATIONS**

State of the art ultrafast recovery rectifiers designed with optimized performance of forward voltage drop, ultrafast recovery time, and fast recovery.

The planar structure and the platinum doped life time control guarantee the best overall performance, ruggedness and reliability characteristics.

These devices are intended for use in PFC Boost stage in the AC/DC section of SMPS, inverters or as freewheeling diodes.

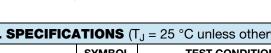
Their extremely optimized stored charge and low recovery current minimize the switching losses and reduce power dissipation in the switching element and snubbers.

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Peak repetitive reverse voltage	V <sub>RRM</sub>		600	V	
Average rectified forward current	I <sub>F(AV)</sub>	$T_{L} = 110 \ ^{\circ}C \ ^{(1)}$	3	٨	
Non-repetitive peak surge current per leg	I <sub>FSM</sub>	$T_J = 25 \text{ °C}, 6 \text{ ms}$ square pulse	55	A	
Operating junction and storage temperatures	T <sub>J</sub> , T <sub>Stg</sub>		-55 to +175	°C	

Note

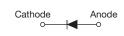
<sup>(1)</sup> Mounted on PCB with minimum pad size

<b>ELECTRICAL SPECIFICATIONS</b> ( $T_J$ = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS	
Breakdown voltage, blocking voltage	V <sub>BR</sub> , V <sub>R</sub>	I <sub>R</sub> = 100 μA	600	-	-		
Forward voltage V <sub>F</sub>	I <sub>F</sub> = 3 A	-	1.15	1.35	V		
	I <sub>F</sub> = 3 A, T <sub>J</sub> = 150 °C	-	0.99	1.2			
		$V_{R} = V_{R}$ rated	-	-	3		
Reverse leakage current I <sub>R</sub>	IR	$T_J = 150 \text{ °C}, V_R = V_R \text{ rated}$	-	-	100	μΑ	
Junction capacitance	CT	V <sub>R</sub> = 600 V	-	3.9	-	pF	





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SMB (DO-214AA)

PRIMARY CHARACTERISTICS				
I <sub>F(AV)</sub>	3 A			
V <sub>R</sub>	600 V			
V <sub>F</sub> at I <sub>F</sub>	0.99 V			
t <sub>rr</sub> typ.	41 ns			
T <sub>J</sub> max.	175 °C			
Package	SMB (DO-214AA)			
Circuit configuration	Single			

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FREE



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<b>DYNAMIC RECOVERY CHARACTERISTICS</b> ( $T_J = 25$ °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CO	MIN.	TYP.	MAX.	UNITS	
	$I_F$ = 1.0 A, $dI_F\!/dt$ = 100 A/µs, $V_R$ = 30 V		-	41	-		
		$I_F = 1.0 \text{ A}, \text{ d}I_F/\text{d}t = 50 \text{ A}/\mu\text{s}, \text{ V}_R = 30 \text{ V}$		-	52	-	ns
Reverse recovery time t <sub>rr</sub>	t <sub>rr</sub>	I <sub>F</sub> = 0.5 A, I <sub>R</sub> = 1 A, I <sub>rr</sub> = 0.25 A		-	-	65	
		T <sub>J</sub> = 25 °C		-	38	-	
		T <sub>J</sub> = 125 °C		-	52	-	
Dook rooovery ourrent		T <sub>J</sub> = 25 °C	$I_{\rm F} = 3  {\rm A}$	-	5.6	-	А
Peak recovery current I <sub>RRM</sub>		dl <sub>F</sub> /dt = 200 A/µs V <sub>R</sub> = 390 V	-	7.3	-	A	
	T <sub>J</sub> = 25 °C		-	108	-	nC	
Reverse recovery charge	Q <sub>rr</sub>	T <sub>J</sub> = 125 °C		-	193	-	

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Maximum junction and storage temperature range	T <sub>J</sub> , T <sub>Stg</sub>		-55	-	175	°C
Thermal resistance, junction to case	R <sub>thJC</sub> <sup>(1)</sup>		-	-	18	°C/W
Thermal resistance, junction to ambient	R <sub>thJA</sub> <sup>(1)</sup>		-	-	90	0/10
Annewimete Weight				0.1		g
Approximate Weight			0.003		oz.	
Marking device		Case style SMB (DO-214AA)		31	J6	

#### Note

<sup>(1)</sup> Mounted on PCB with minimum pad size

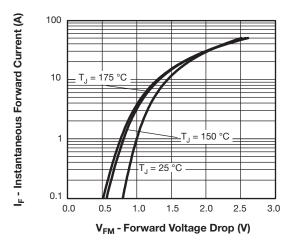


Fig. 1 - Typical Forward Voltage Drop Characteristics

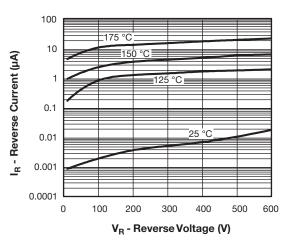


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

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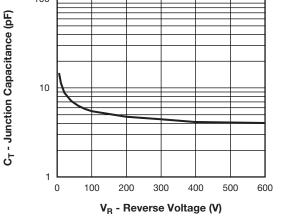


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

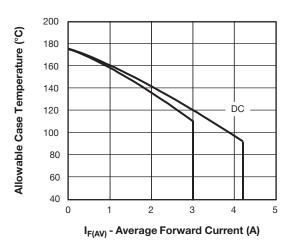


Fig. 4 - Maximum Allowable Case Temperature vs. Average Forward Current

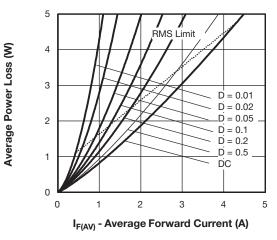


Fig. 5 - Forward Power Loss Characteristics

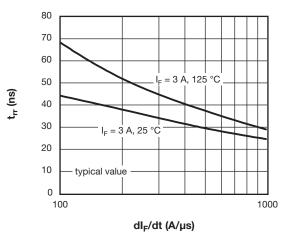
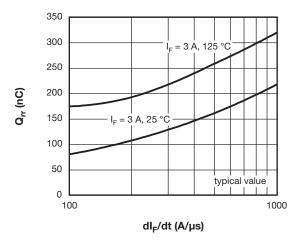


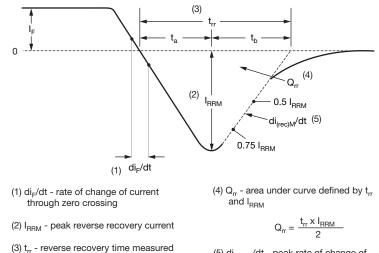
Fig. 6 - Typical Reverse Recovery Time vs. dI<sub>F</sub>/dt





# VS-3EGU06-M3

## **Vishay Semiconductors**

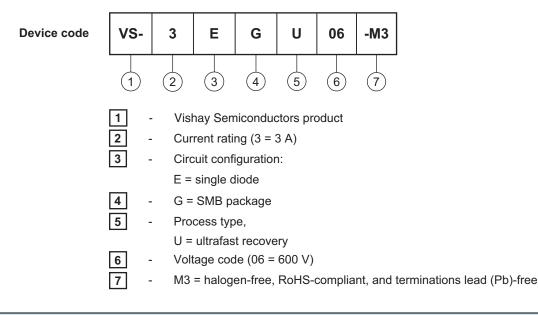


from zero crossing point of negative going I<sub>F</sub> to point where a line passing through 0.75 I<sub>RRM</sub> and 0.50 I<sub>RRM</sub> extrapolated to zero current. (5) di\_{(rec)M}/dt - peak rate of change of current during  $t_{\rm b}$  portion of  $t_{\rm rr}$ 

#### Fig. 8 - Reverse Recovery Waveform and Definitions

### **ORDERING INFORMATION TABLE**

www.vishay.com



ORDERING INFORMATION (Example)					
PREFERRED P/N	QUANTITY PER TUBE	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION		
VS-3EGU06-M3/5BT	5BT	3200	13"diameter plastic tape and reel		

LINKS TO RELATED DOCUMENTS				
Dimensions	www.vishay.com/doc?95401			
Part marking information	www.vishay.com/doc?95472			
Packaging information	www.vishay.com/doc?95404			

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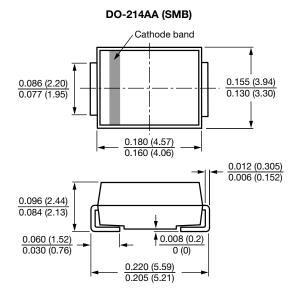


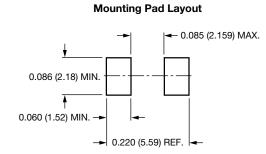
# **Outline Dimensions**

**Vishay Semiconductors** 

**SMB** 

## **DIMENSIONS** in inches (millimeters)







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