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January 2015



J109 / MMBFJ108 N-Channel Switch

Features

- This device is designed for digital switching applications where very low on resistance is mandatory.
- Sourced from process 58

Ordering Information



Figure 1. J109 Device Package

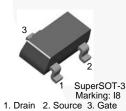


Figure 2. MMBFJ108 Device Package

Part Number	Part Number Top Mark		Packing Method	
J109	J109	TO-92 3L	Bulk	
J109_D26Z	J109	TO-92 3L	Tape and Reel	
MMBFJ108	18	SSOT 3L	Tape and Reel	

Absolute Maximum Ratings^{(1), (2)}

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at $T_A = 25^{\circ}$ C unless otherwise noted.

Symbol	Parameter	Value	Unit
V _{DG}	Drain-Gate Voltage	25	V
V _{GS}	Gate-Source Voltage	-25	V
I _{GF}	Forward Gate Current	10	mA
T _J , T _{STG} Operating and Storage Junction Temperature Range		-55 to 150	°C

Notes:

- 1. These ratings are based on a maximum junction temperature of 150°C.
- 2. These are steady-state limits. Fairchild Semiconductor should be consulted on applications involving pulsed or low-duty-cycle operations.

Thermal Characteristics

Values are at $T_A = 25^{\circ}C$ unless otherwise noted.

Symbol	Parameter	Ма	Unit	
	Falanetei	J109 ⁽³⁾	MMBFJ108 ⁽⁴⁾	Onit
в	Total Device Dissipation	625	350	mW
PD	Derate Above 25°C	5.0	2.8	mW/°C
R _{θJC}	Thermal Resistance, Junction-to-Case	125		°C/W
$R_{ extsf{ heta}JA}$	Thermal Resistance, Junction-to-Ambient	200	357	°C/W

Notes:

3. PCB size: FR-4, 76 mm x 114 mm x 1.57 mm (3.0 inch x 4.5 inch x 0.062 inch) with minimum land pattern size.

4. Device mounted on FR-4 PCB 36mm × 18mm × 1.5mm; mounting pad for the collector lead minimum 6cm².

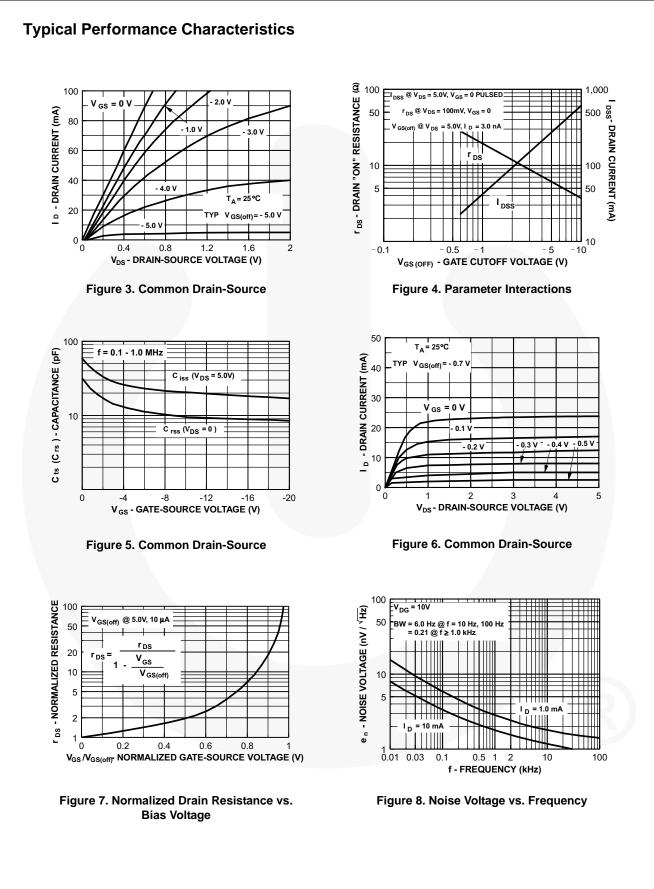
Electrical Characteristics

Values are at $T_A = 25^{\circ}C$ unless otherwise noted.

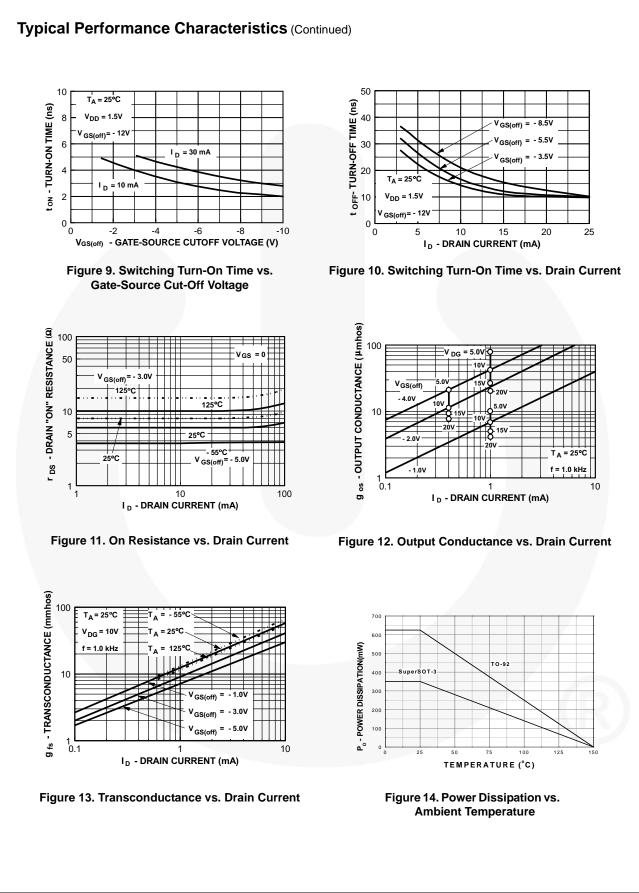
Symbol	Parameter	Condition	s	Min.	Max.	Unit
Off Charac	teristics					
V _{(BR)GSS}	Gate-Source Breakdown Voltage	$I_G = -10 \ \mu A, \ V_{DS} = 0$		-25		V
I _{GSS} Gate	Gate Reverse Current	$V_{GS} = -15 \text{ V}, \text{ V}_{DS} = 0$			-3.0	nA
		V _{GS} = -15 V, V _{DS} = 0, T _A = 100°C			-200	
V _{GS} (off)	Gate-Source Cut-Off Voltage	V _{DS} = 15 V, I _D = 10 nA	MMBFJ108	-3.0	-10.0	- V
			J109	-2.0	-6.0	
On Charac	teristics					
	Zero-Gate Voltage Drain Current ⁽⁵⁾	$V_{DS} = 15 V, V_{GS} = 0$	MMBFJ108	80		mA
I _{DSS} Ze			J109	40		
	Drain-Source On Resistance	$V \leq 0.1 V V = 0$	MMBFJ108		8.0	Ω
r _{DS} (on)		$V_{DS} \le 0.1 \text{ V}, \text{ V}_{GS} = 0$	J109		12	52
Small Sigr	al Characteristics	·				
C _{dg} (on) C _{sg} (off)	Drain-Gate &Source-Gate On Capacitance	$V_{DS} = 0, V_{GS} = 0, f = 1.$.0 MHz		85	pF
C _{dg} (off)	Drain-Gate Off Capacitance	V _{DS} = 0, V _{GS} = -10 V, f = 1.0 MHz			15	pF
C _{sg} (off)	Source-Gate Off Capacitance	V _{DS} = 0, V _{GS} = -10 V, f	= 1.0 MHz		15	pF

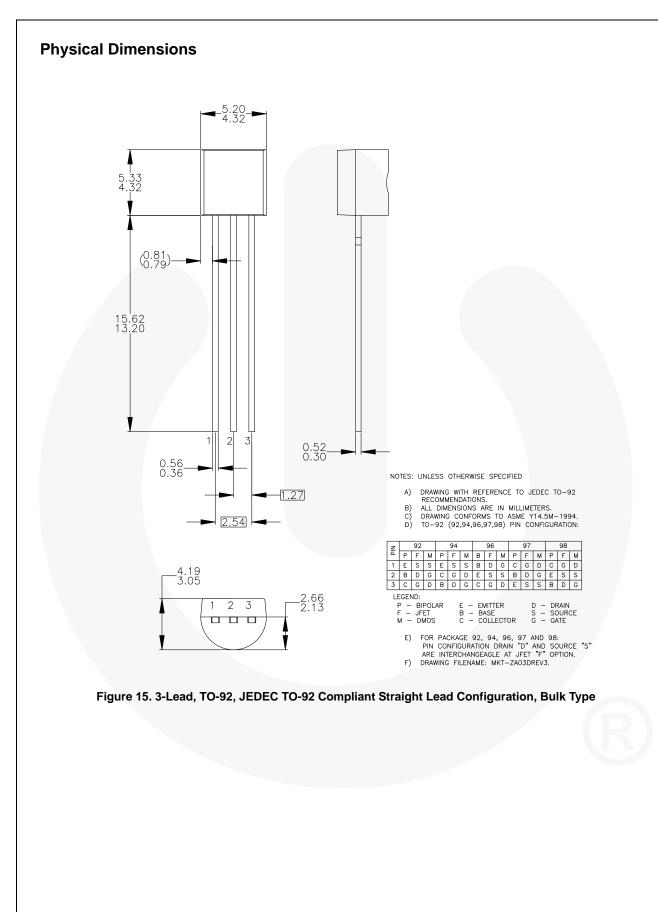
Note:

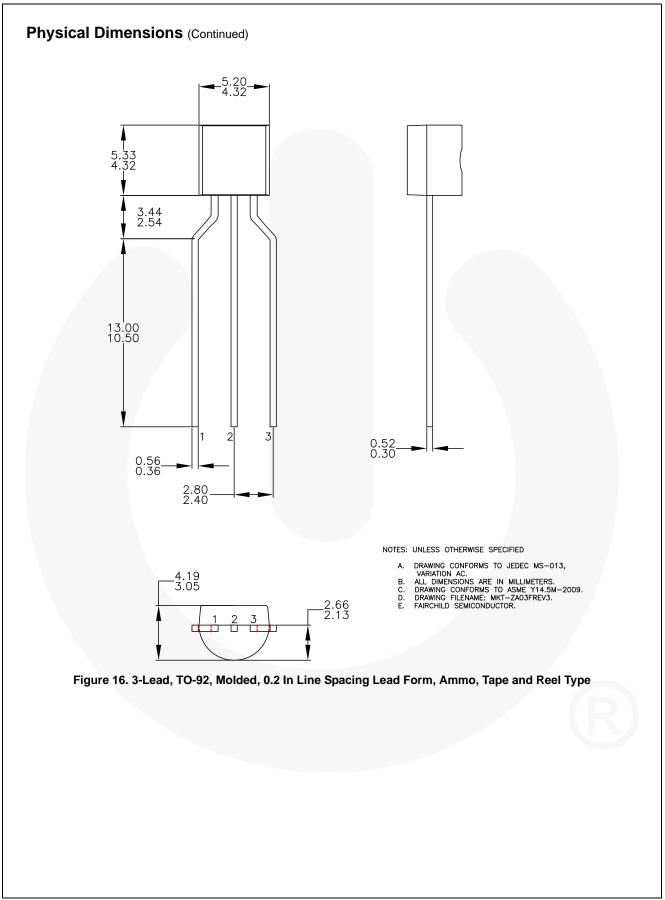
5. Pulse test: pulse width \leq 300 µs, duty cycle \leq 2%.

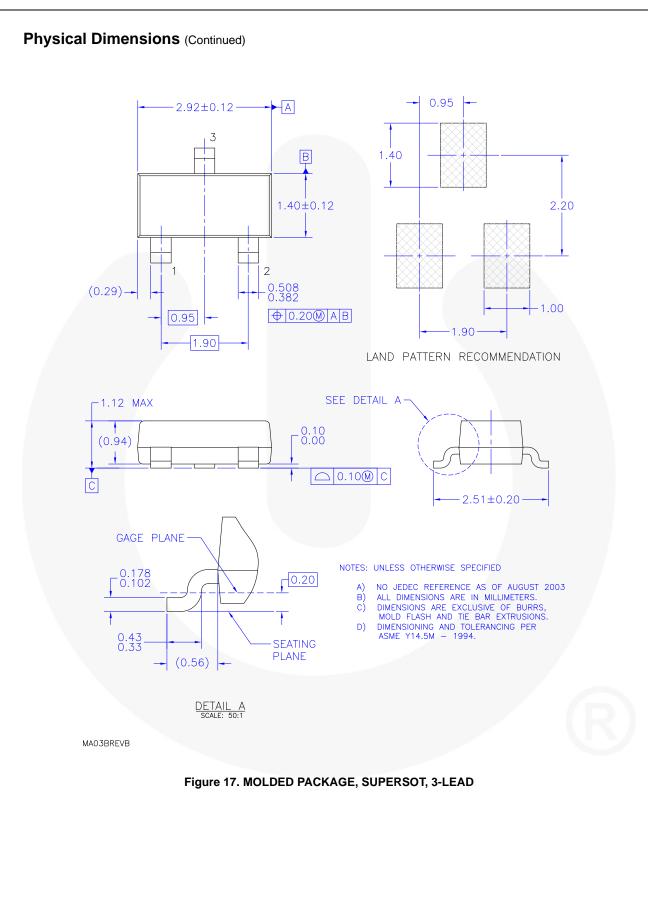


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