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January 2015



## J109 / MMBFJ108 N-Channel Switch

## Features

- This device is designed for digital switching applications where very low on resistance is mandatory.
- Sourced from process 58

**Ordering Information** 



Figure 1. J109 Device Package

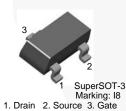


Figure 2. MMBFJ108 Device Package

| Part Number | Part Number Top Mark |          | Packing Method |  |
|-------------|----------------------|----------|----------------|--|
| J109        | J109                 | TO-92 3L | Bulk           |  |
| J109_D26Z   | J109                 | TO-92 3L | Tape and Reel  |  |
| MMBFJ108    | 18                   | SSOT 3L  | Tape and Reel  |  |

## Absolute Maximum Ratings<sup>(1), (2)</sup>

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at  $T_A = 25^{\circ}$ C unless otherwise noted.

| Symbol   | Parameter            | Value      | Unit |
|--|----------------------|------------|------|
| V <sub>DG</sub>  | Drain-Gate Voltage   | 25         | V    |
| V <sub>GS</sub>  | Gate-Source Voltage  | -25        | V    |
| I <sub>GF</sub>  | Forward Gate Current | 10         | mA   |
| T <sub>J</sub> , T <sub>STG</sub> Operating and Storage Junction Temperature Range |                      | -55 to 150 | °C   |

## Notes:

- 1. These ratings are based on a maximum junction temperature of 150°C.
- 2. These are steady-state limits. Fairchild Semiconductor should be consulted on applications involving pulsed or low-duty-cycle operations.

## **Thermal Characteristics**

Values are at  $T_A = 25^{\circ}C$  unless otherwise noted.

| Symbol                | Parameter                               | Ма                  | Unit                    |       |
|-----------------------|---|---------------------|-------------------------|-------|
|                       | Falanetei                               | J109 <sup>(3)</sup> | MMBFJ108 <sup>(4)</sup> | Onit  |
| в                     | Total Device Dissipation                | 625                 | 350                     | mW    |
| PD                    | Derate Above 25°C                       | 5.0                 | 2.8                     | mW/°C |
| R <sub>θJC</sub>      | Thermal Resistance, Junction-to-Case    | 125                 |                         | °C/W  |
| $R_{	extsf{	heta}JA}$ | Thermal Resistance, Junction-to-Ambient | 200                 | 357                     | °C/W  |

## Notes:

3. PCB size: FR-4, 76 mm x 114 mm x 1.57 mm (3.0 inch x 4.5 inch x 0.062 inch) with minimum land pattern size.

4. Device mounted on FR-4 PCB 36mm × 18mm × 1.5mm; mounting pad for the collector lead minimum 6cm<sup>2</sup>.

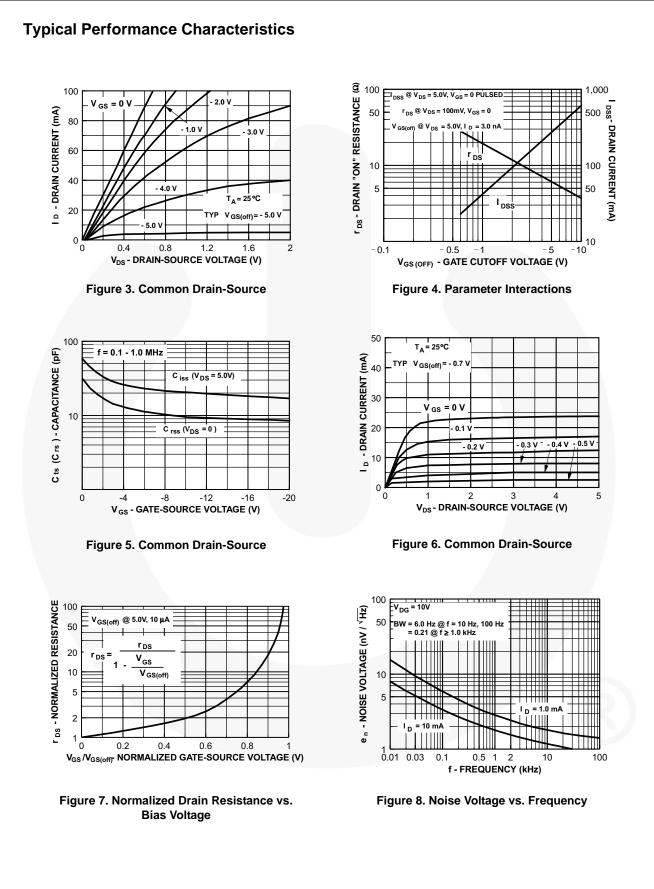
## **Electrical Characteristics**

Values are at  $T_A = 25^{\circ}C$  unless otherwise noted.

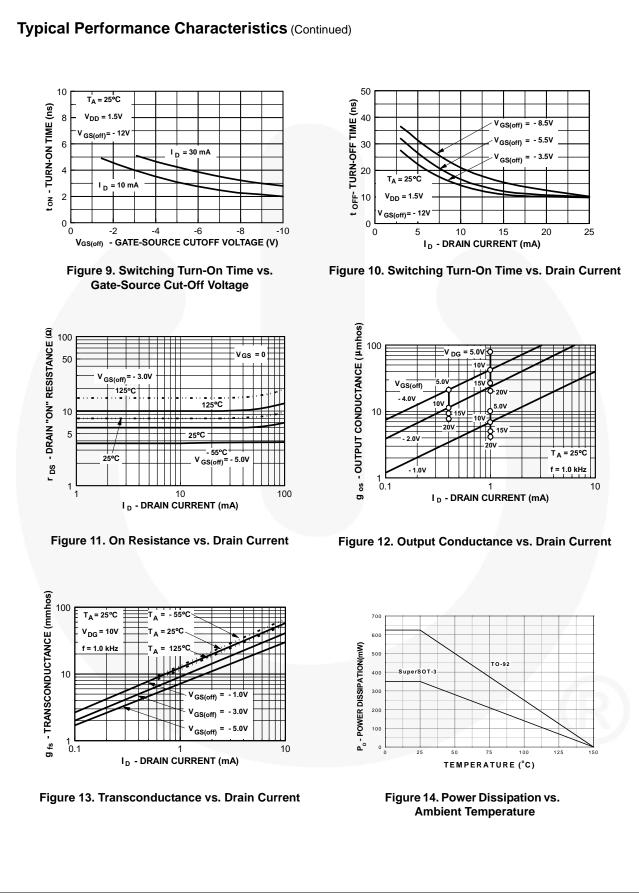
| Symbol  | Parameter                                      | Condition  | s         | Min. | Max.  | Unit |
|---|--|--|-----------|------|-------|------|
| Off Charac                                    | teristics                                      |  |           |      |       |      |
| V <sub>(BR)GSS</sub>                          | Gate-Source Breakdown Voltage                  | $I_G = -10 \ \mu A, \ V_{DS} = 0$                                    |           | -25  |       | V    |
| I <sub>GSS</sub> Gate                         | Gate Reverse Current                           | $V_{GS} = -15 \text{ V}, \text{ V}_{DS} = 0$                         |           |      | -3.0  | nA   |
|   |  | V <sub>GS</sub> = -15 V, V <sub>DS</sub> = 0, T <sub>A</sub> = 100°C |           |      | -200  |      |
| V <sub>GS</sub> (off)                         | Gate-Source Cut-Off Voltage                    | V <sub>DS</sub> = 15 V, I <sub>D</sub> = 10 nA                       | MMBFJ108  | -3.0 | -10.0 | - V  |
|   |  |  | J109      | -2.0 | -6.0  |      |
| On Charac                                     | teristics                                      |  |           |      |       |      |
|   | Zero-Gate Voltage Drain Current <sup>(5)</sup> | $V_{DS} = 15 V, V_{GS} = 0$  | MMBFJ108  | 80   |       | mA   |
| I <sub>DSS</sub> Ze                           |  |  | J109      | 40   |       |      |
|   | Drain-Source On Resistance                     | $V \leq 0.1 V V = 0$   | MMBFJ108  |      | 8.0   | Ω    |
| r <sub>DS</sub> (on)                          |  | $V_{DS} \le 0.1 \text{ V}, \text{ V}_{GS} = 0$                       | J109      |      | 12    | 52   |
| Small Sigr                                    | al Characteristics                             | ·  |           |      |       |      |
| C <sub>dg</sub> (on)<br>C <sub>sg</sub> (off) | Drain-Gate &Source-Gate On<br>Capacitance      | $V_{DS} = 0, V_{GS} = 0, f = 1.$                                     | .0 MHz    |      | 85    | pF   |
| C <sub>dg</sub> (off)                         | Drain-Gate Off Capacitance                     | V <sub>DS</sub> = 0, V <sub>GS</sub> = -10 V, f = 1.0 MHz            |           |      | 15    | pF   |
| C <sub>sg</sub> (off)                         | Source-Gate Off Capacitance                    | V <sub>DS</sub> = 0, V <sub>GS</sub> = -10 V, f                      | = 1.0 MHz |      | 15    | pF   |

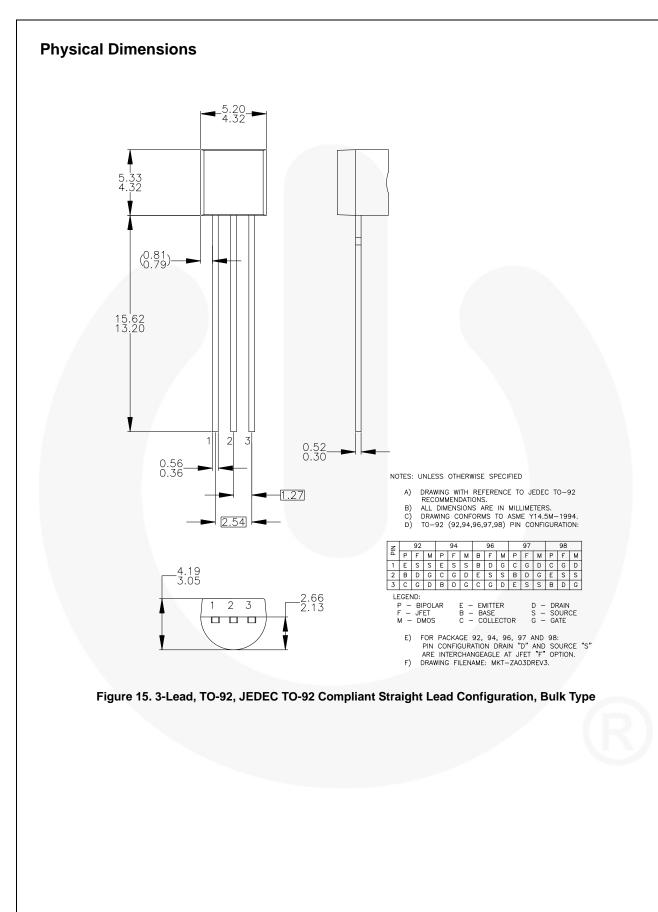
Note:

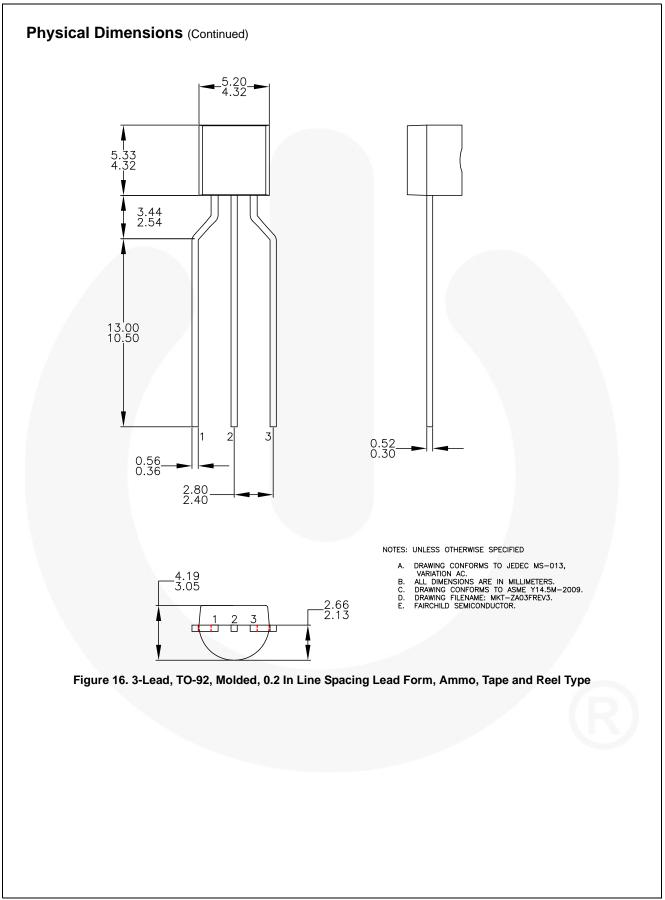
5. Pulse test: pulse width  $\leq$  300 µs, duty cycle  $\leq$  2%.

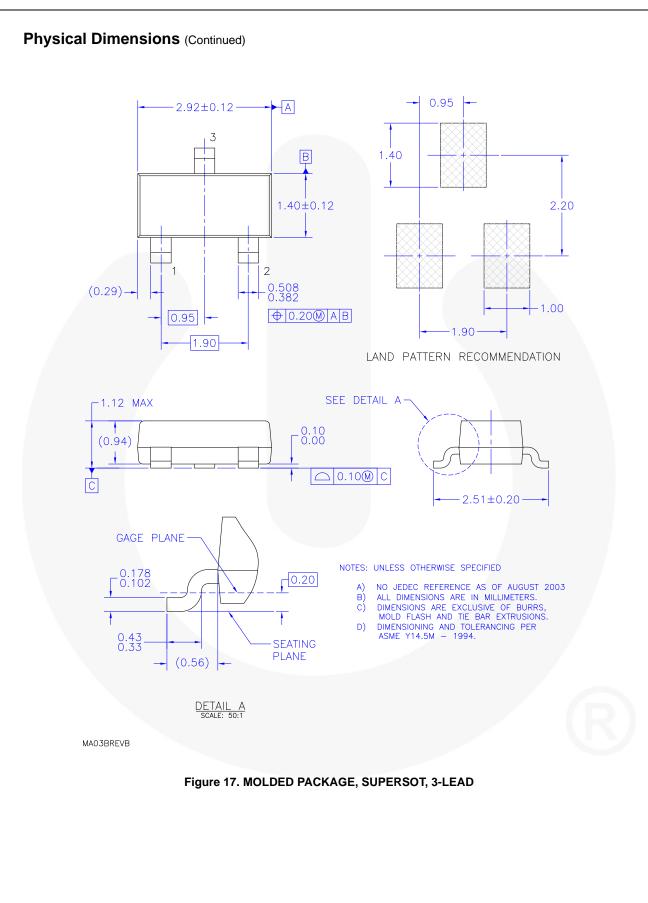


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