



BIPOLAR ANALOG INTEGRATED CIRCUIT $\mu PC8178TK$

SILICON MMIC LOW CURRENT AMPLIFIER FOR MOBILE COMMUNICATIONS

DESCRIPTION

The μ PC8178TK is a silicon monolithic integrated circuit designed as amplifier for mobile communications. This IC can realize low current consumption with external chip inductor which can not be realized on internal 50 Ω wide band matched IC. μ PC8178TK adopts 6-pin lead-less minimold package using same chip as the conventional μ PC8178TB in 6-pin super minimold.

TK suffix IC which is smaller package than TB suffix IC contributes to reduce mounting space by 50 %. This IC is manufactured using our 30 GHz fmax UHS0 (<u>U</u>Itra <u>High Speed Process</u>) silicon bipolar process.

FEATURES

•	Low current consumption	: lcc = 1.9 mA TYP. @ Vcc = 3.0 V
•	Supply voltage	: Vcc = 2.4 to 3.3 V
•	Excellent isolation	: ISL = 40 dB TYP. @ f = 1.0 GHz
		ISL = 41 dB TYP. @ f = 1.9 GHz
		ISL = 42 dB TYP. @ f = 2.4 GHz
•	Power gain	: G _P = 11.0 dB TYP. @ f = 1.0 GHz
		G _P = 11.0 dB TYP. @ f = 1.9 GHz
		G _P = 11.0 dB TYP. @ f = 2.4 GHz
•	Gain 1 dB compression output power	: Po (1 dB) = -5.5 dBm TYP. @ f = 1.0 GHz
		Po (1 dB) = -8.0 dBm TYP. @ f = 1.9 GHz
		Po (1 dB) = -8.0 dBm TYP. @ f = 2.4 GHz
•	Operating frequency	: 0.1 to 2.4 GHz (Output port LC matching)
•	High-density surface mounting	: 6-pin lead-less minimold package ($1.5 \times 1.3 \times 0.55$ mm)
•	Light weight	: 3 mg (Standard value)

APPLICAION

• Buffer amplifiers on 0.1 to 2.4 GHz mobile communications system

Caution Observe precautions when handling because these devices are sensitive to electrostatic discharge.

★ ORDERING INFORMATION

Part Number	Order Number	Package	Marking	Supplying Form
μΡC8178TK-E2	μΡC8178TK-E2-A	6-pin lead-less minimold (1511) (Pb-Free) ^{Note}	6B	 Embossed tape 8 mm wide Pin 1, 2, 3 face the perforation side of the tape Qty 5 kpcs/reel

Note With regards to terminal solder (the solder contains lead) plated products (conventionally plated), contact your nearby sales office.

RemarkTo order evaluation samples, contact your nearby sales office.Part number for sample order: μ PC8178TK-A

PRODUCT LINE-UP (TA = +25°C, Vcc = Vout = 3.0 V, Zs = ZL = 50 Ω)

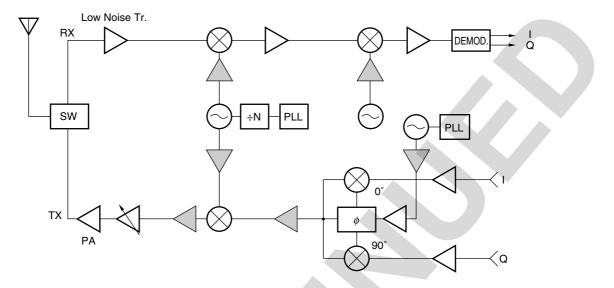
Parameter		1.0 GHz output port matching frequency			1.66 GHz output port matching frequency		1.9 GHz output port matching frequency			2.4 GHz output port matching frequency		Marking		
Part No.	lcc (mA)	G⊧ (dB)	ISL (dB)	P _{O(1dB)} (dBm)	G⊦ (dB)	ISL (dB)	Po(1dB) (dBm)	G₽ (dB)	ISL (dB)	Po(1dB) (dBm)	G⊦ (dB)	ISL (dB)	Po(1dB) (dBm)	
μPC8178TB	1.9	11.0	39.0	-4.0	_	-	-	11.5	40.0	-7.0	11.5	38.0	-7.5	C3B
<i>µ</i> РС8178ТК	1.9	11.0	40.0	-5.5	_	-		11.0	41.0	-8.0	11.0	42.0	-8.0	6B
<i>µ</i> РС8179ТВ	4.0	13.5	44.0	+3.0	_	-	1	15.5	42.0	+1.5	15.5	41.0	+1.0	C3C
<i>µ</i> РС8128ТВ	2.8	12.5	39.0	-4.0	13.0	39.0	-4.0	13.0	37.0	-4.0	-	I	_	C2P
<i>µ</i> РС8151ТВ	4.2	12.5	38.0	+2.5	15.0	36.0	+1.5	15.0	34.0	+0.5	_	_	_	C2U
μPC8152TB	5.6	23.0	40.0	-4.5	19.5	38.0	-8.5	17.5	35.0	-8.5	_	-	_	C2V

Remarks 1. Typical performance. Please refer to ELECTRICAL CHARACTERISTICS in detail.

2. To know the associated product, please refer to each latest data sheet.

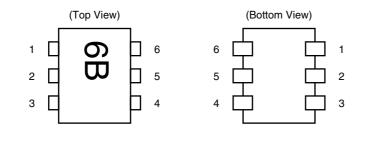
SYSTEM APPLICATION EXAMPLE

Location examples in digital cellular



These ICs can be added to your system around \triangle parts, when you need more isolation or gain. The application herein, however, shows only examples, therefore the application can depend on your kit evaluation.

PIN CONNECTIONS



Pin No.	Pin Name
1	INPUT
2	GND
3	GND
4	OUTPUT
5	GND
6	Vcc

PIN EXPLANATION

Pin No.	Pin Name	Applied Voltage (V)	Pin Voltage (V) ^{Note}	Function and Applications	Internal Equivalent Circuit
1	INPUT	_	0.90	Signal input pin. A internal matching circuit, configured with resisters, enables 50 Ω connection over a wide band. This pin must be coupled to signal source with capacitor for DC cut.	
2 3 5	GND	0	-	Ground pin. This pin should be connected to system ground with minimum inductance. Ground pattern on the board should be formed as wide as possible. All the ground pins must be con- nected together with wide ground pattern to decrease impedance defference.	
4	OUTPUT	Voltage as same as Vcc through external inductor	5	Signal output pin. This pin is de- signed as collector output. Due to the high impedance output, this pin should be externally equipped with LC matching circuit to next stage. For L, a size 1 005 chip inductor can be chosen.	
6	Vcc	2.4 to 3.3	_	Power supply pin. This pin should be externally equipped with bypass capacitor to minimize its impedance.	

Note Pin voltage is measured at Vcc = 3.0 V.

ABSOLUTE MAXIMUM RATINGS

Parameter	Symbol	Test Conditions	Ratings	Unit
Supply Voltage	Vcc	T _A = +25°C, Pin 4, Pin 6	3.6	V
Circuit Current	lcc	T _A = +25°C	15	mA
Power Dissipation	Po	T _A = +85°C Note	232	mW
Operating Ambient Temperature	TA		-40 to +85	°C
Storage Temperature	Tstg		–55 to +150	°C
Input Power	Pin	T _A = +25°C	+5	dBm

Note Mounted on double-sided copper-clad $50 \times 50 \times 1.6$ mm epoxy glass PWB

RECOMMENDED OPERATING RANGE

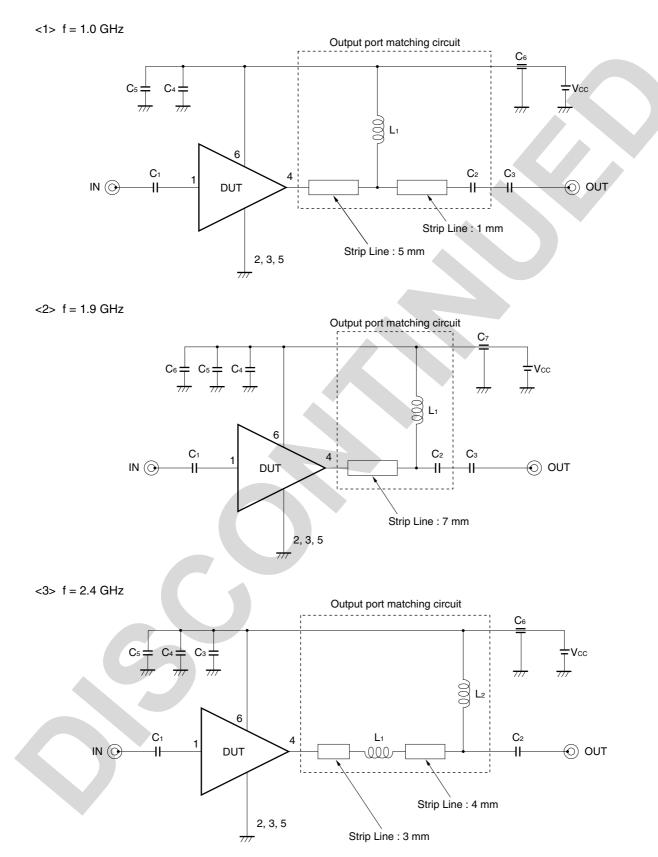
Parameter	Symbol	MIN.	TYP.	MAX.	Unit	Remarks
Supply Voltage	Vcc	2.4	3.0	3.3	V	The same voltage should be applied to pin 4 and pin 6.
Operating Ambient Temperature	TA	-40	+25	+85	°C	

ELECTRICAL CHARACTERISTICS

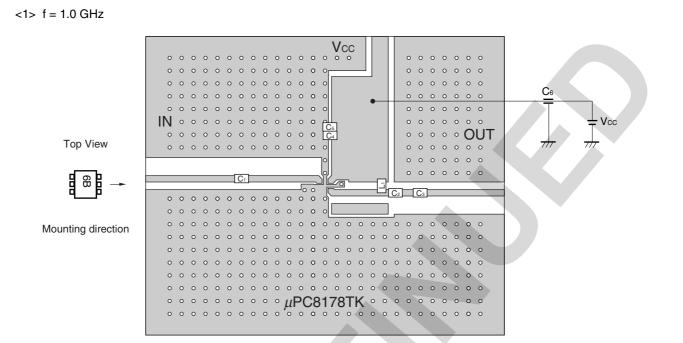
(Unless otherwise specified, $T_A = +25^{\circ}C$, $V_{CC} = V_{out} = 3.0 V$, $Z_S = Z_L = 50 \Omega$, at LC matched frequency)

Parameter	Symbol	Test Conditions	MIN.	TYP.	MAX.	Unit
Circuit Current	lcc	No signal	1.4	1.9	2.4	mA
Power Gain	G₽	f = 1.0 GHz, P _{in} = -30 dBm	9.0	11.0	13.0	dB
		f = 1.9 GHz, P _{in} = -30 dBm	9.0	11.0	13.5	
		$f = 2.4 \text{ GHz}, P_{in} = -30 \text{ dBm}$	9.0	11.0	13.5	
Isolation	ISL	f = 1.0 GHz, P _{in} = -30 dBm	35.0	40.0	-	dB
		f = 1.9 GHz, P _{in} = -30 dBm	36.0	41.0	-	
		$f = 2.4 \text{ GHz}, P_{in} = -30 \text{ dBm}$	37.0	42.0	-	
Gain 1 dB Compression Output	Po(1 dB)	f = 1.0 GHz	-8.0	-5.5	-	dBm
Power		f = 1.9 GHz	-11.0	-8.0	-	
		f = 2.4 GHz	-11.5	-8.0	_	
Noise Figure	NF	f = 1.0 GHz	-	5.5	7.0	dB
		f = 1.9 GHz	_	5.5	7.0	
		f = 2.4 GHz	-	5.5	7.0	
Input Return Loss	RLin	f = 1.0 GHz, P _{in} = -30 dBm	4.0	7.0	-	dB
		f = 1.9 GHz, P _{in} = -30 dBm	5.0	8.0	-	
		$f = 2.4 \text{ GHz}, P_{in} = -30 \text{ dBm}$	6.5	9.5	_	

★ TEST CIRCUITS



★ ILLUSTRATION OF THE TEST CIRCUIT ASSEMBLED ON EVALUATION BOARD

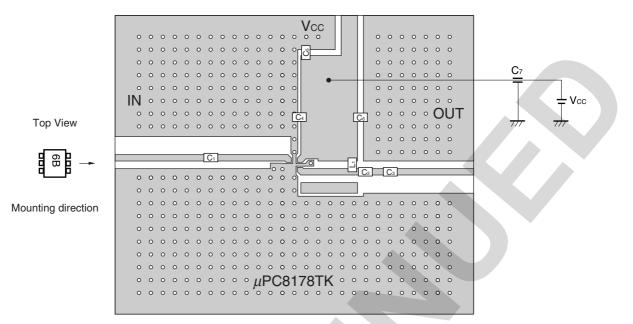


- (*1) $42 \times 35 \times 0.4$ mm polyimide board, double-sided copper clad
- (*2) Back side: GND pattern
- (*3) Gold plated on pattern
- (*4) o: Through holes

COMPONENT LIST

Form	Symbol	Value	Type code	Maker
Chip capacitor	C1, C3	1 000 pF	GRM40CH102J50PT	murata
	C2	0.75 pF	GRM39CKR75C50PT	murata
	C4	20 pF	GRM39CH200J50PT	murata
	C₅	10 pF	GRM39CH100D50PT	murata
Feed-though Capacitor	C ₆	1 000 pF	DFT301-801 × 7R102S50	murata
Chip inductor	Ľ	12 nH	LL1608-FH12N	токо

<2> f = 1.9 GHz

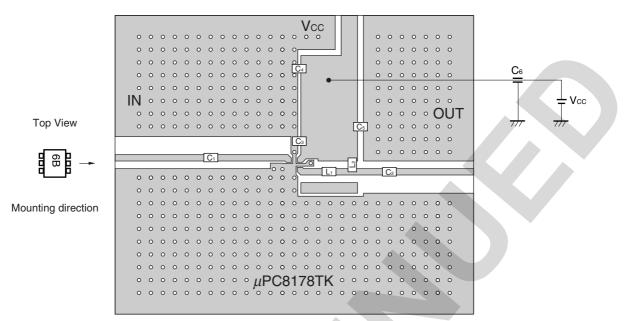


- (*1) $42 \times 35 \times 0.4$ mm polyimide board, double-sided copper clad
- (*2) Back side: GND pattern
- (*3) Gold plated on pattern
- (*4) o: Through holes

COMPONENT LIST

Form	Symbol	Value	Type code	Maker
Chip capacitor	C1, C3, C5, C6	1 000 pF	GRM40CH102J50PT	murata
	C2	0.5 pF	GRM39CKR5C50PT	murata
	C4	8 pF	GRM39CH080D50PT	murata
Feed-though Capacitor	C ₇	1 000 pF	DFT301-801 × 7R102S50	murata
Chip inductor	Lı	2.7 nH	LL1608-FH2N7S	ТОКО

<3> f = 2.4 GHz



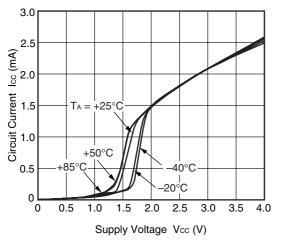
- (*1) $42 \times 35 \times 0.4$ mm polyimide board, double-sided copper clad
- (*2) Back side: GND pattern
- (*3) Gold plated on pattern
- (*4) o: Through holes

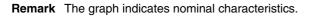
COMPONENT LIST

Form	Symbol	Value	Type code	Maker
Chip capacitor	C1, C2, C4, C5	1 000 pF	GRM40CH102J50PT	murata
	C3	10 pF	GRM39CH100D50PT	murata
Feed-though Capacitor	C ₆	1 000 pF	DFT301-801 × 7R102S50	murata
Chip inductor	Lı	2.7 nH	LL1608-FH2N7S	токо
	L2	1.8 nH	LL1608-FH1N8S	ТОКО

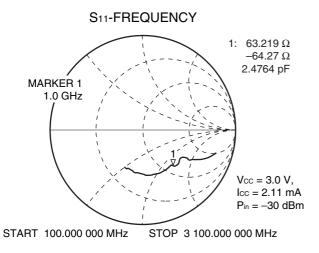
★ TYPICAL CHARACTERISTICS (T_A = +25°C, unless otherwise specified)

CIRCUIT CURRENT vs. SUPPLY VOLTAGE

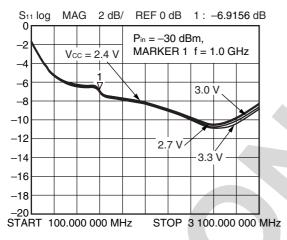


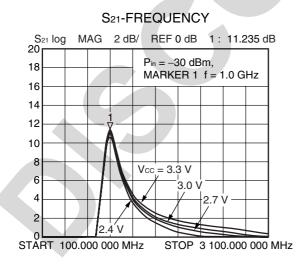


f = 1.0 GHz MATCHING

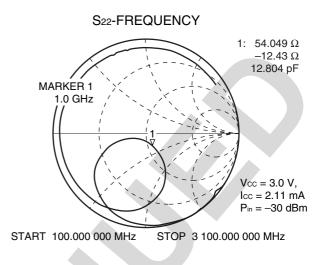


S11-FREQUENCY

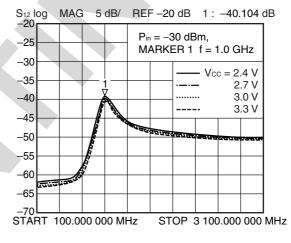


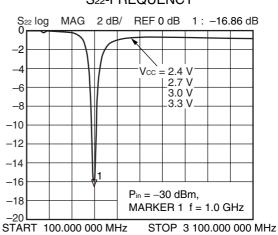


Remark The graphs indicate nominal characteristics.



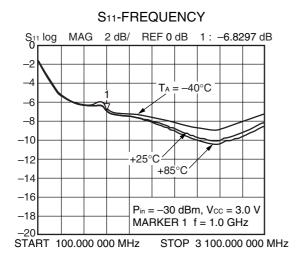
S12-FREQUENCY



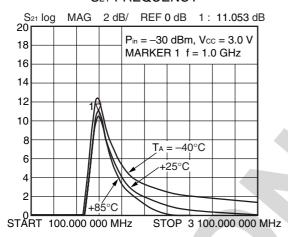


S22-FREQUENCY

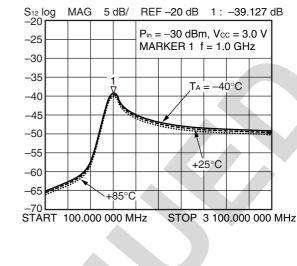
μ**ΡC8178TK**



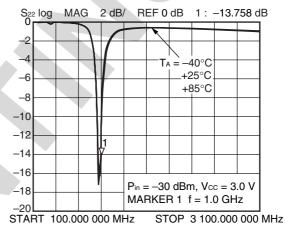
S21-FREQUENCY



Remark The graphs indicate nominal characteristics.

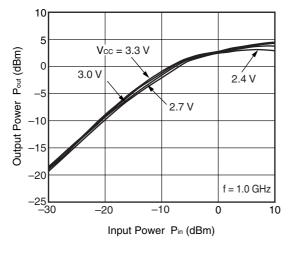


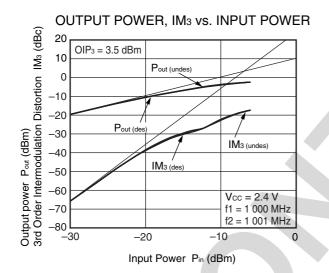
S22-FREQUENCY

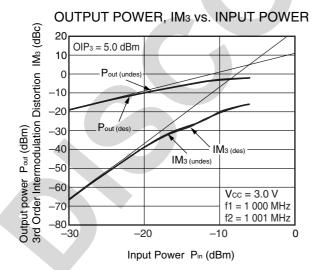


S12-FREQUENCY

OUTPUT POWER vs. INPUT POWER

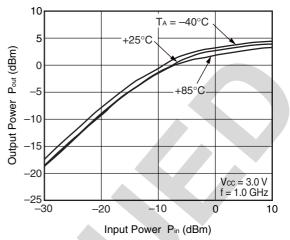




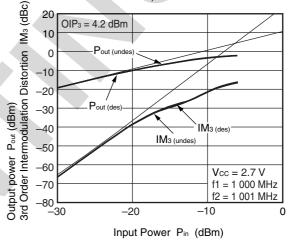


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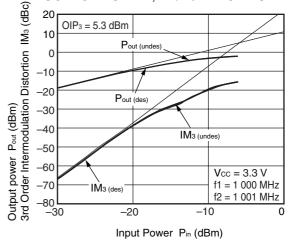
OUTPUT POWER vs. INPUT POWER

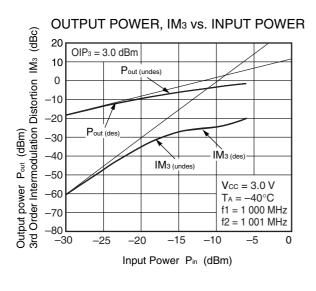


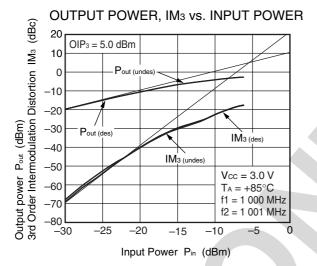
OUTPUT POWER, IM3 vs. INPUT POWER



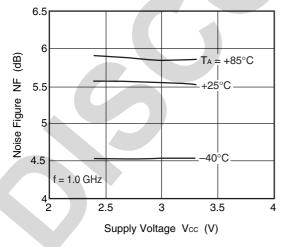
OUTPUT POWER, IM3 vs. INPUT POWER





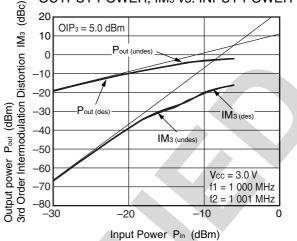


NOISE FIGURE vs. SUPPLY VOLTAGE

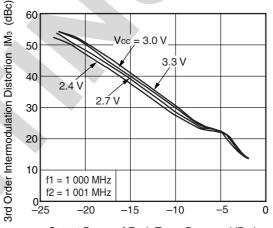


Remark The graphs indicate nominal characteristics.

OUTPUT POWER, IM3 vs. INPUT POWER

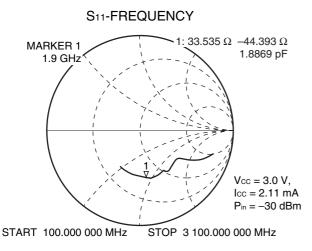


3RD ORDER INTERMODULATION DISTORTION vs. OUTPUT POWER OF EACH TONE



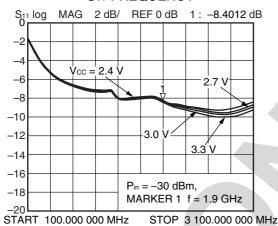
Output Power of Each Tone Pout (each) (dBm)

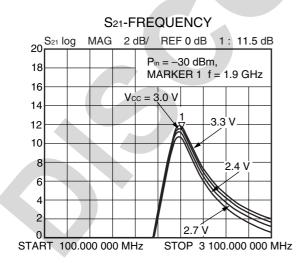
f = 1.9 GHz MATCHING



S22-FREQUENCY MARKER 1 1.9 GHz 1.9 GHz 1.9 GHz Vcc = 3.0 V, lcc = 2.11 mA Pin = -30 dBm START 100.000 000 MHz STOP 3 100.000 000 MHz

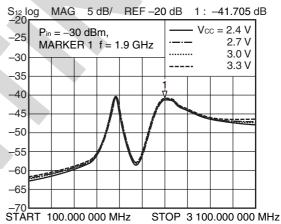
S11-FREQUENCY

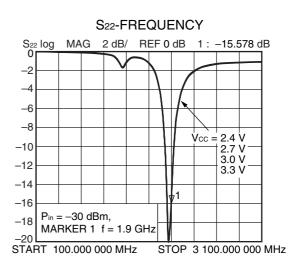




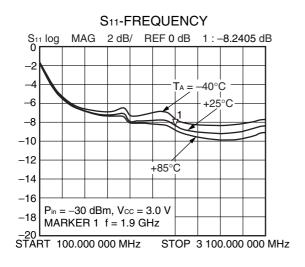
Remark The graphs indicate nominal characteristics.

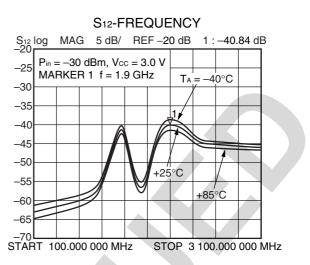
S12-FREQUENCY





μ**ΡC8178TK**

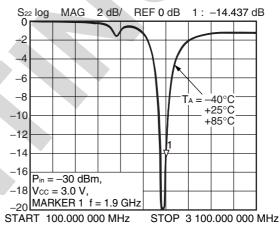




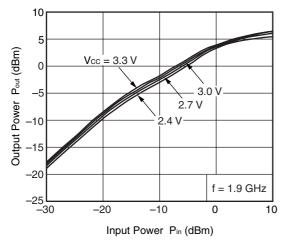
S21-FREQUENCY 20 S21 log MAG 2 dB/ REF 0 dB 1 : 11.553 dB $P_{in} = -30 \text{ dBm}, \text{ Vcc} = 3.0 \text{ V}$ 18 MARKER 1 f = 1.9 GHz 16 14 +85°C 12 $T_A = -40^{\circ}C$ 10 8 +25°C 6 4 2 (STĂRT 100.000 000 MHz STOP 3 100.000 000 MHz

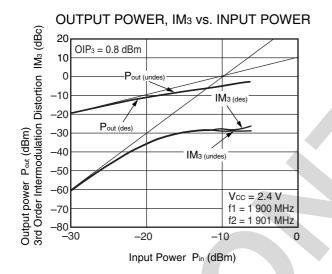
Remark The graphs indicate nominal characteristics.

S22-FREQUENCY

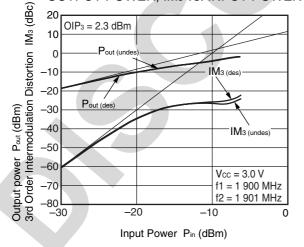


OUTPUT POWER vs. INPUT POWER



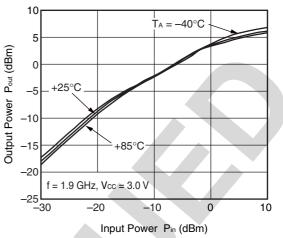




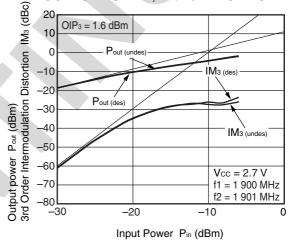


Remark The graphs indicate nominal characteristics.

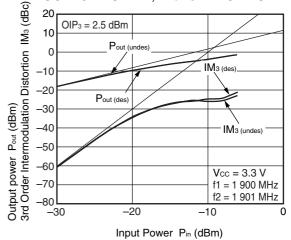
OUTPUT POWER vs. INPUT POWER

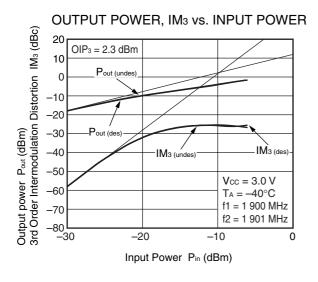


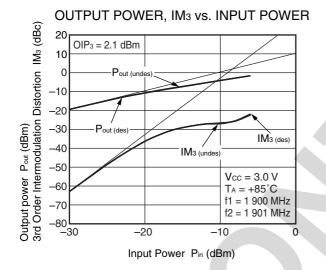
OUTPUT POWER, IM3 vs. INPUT POWER



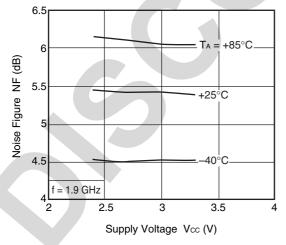
OUTPUT POWER, IM3 vs. INPUT POWER





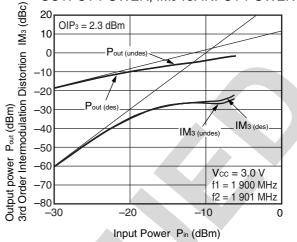


NOISE FIGURE vs. SUPPLY VOLTAGE

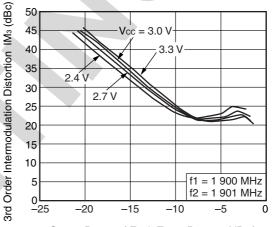


Remark The graphs indicate nominal characteristics.

OUTPUT POWER, IM3 vs. INPUT POWER

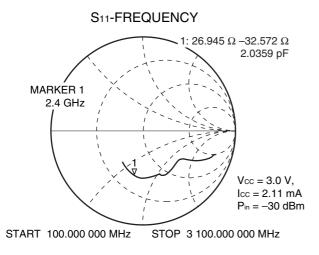


3RD ORDER INTERMODULATION DISTORTION vs. OUTPUT POWER OF EACH TONE



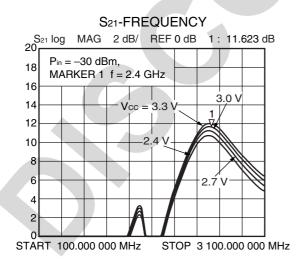
Output Power of Each Tone Pout (each) (dBm)

f = 2.4 GHz MATCHING



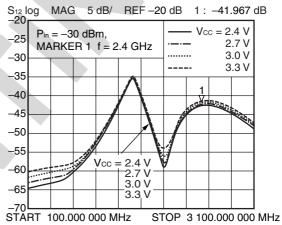
S22-FREQUENCY 1: 47.047 Ω 13.205 Ω 875.69 pH MARKER 1 2.4 GHz Vcc = 3.0 V, lcc = 2.11 mA Pin = -30 dBm START 100.000 000 MHz STOP 3 100.000 000 MHz

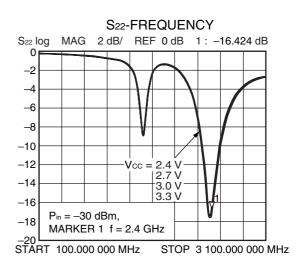
S11-FREQUENCY S11 log MAG 2 dB/ REF 0 dB 1 : -10.022 dB 0 -2 -4 -6 Vcc = 2.4 V -8 1 -10 3.3 V -12 3.0 V -14-16 $P_{in} = -30 \text{ dBm},$ -18 MARKER 1 f = 2.4 GHz -20 START 100.000 000 MHz STOP 3 100.000 000 MHz

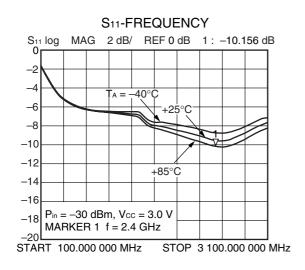


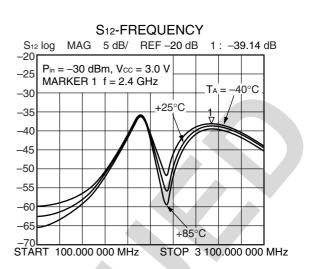
Remark The graphs indicate nominal characteristics.

S12-FREQUENCY



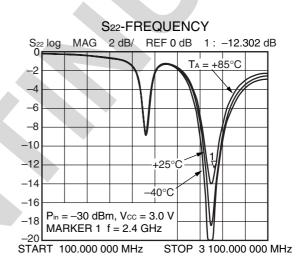




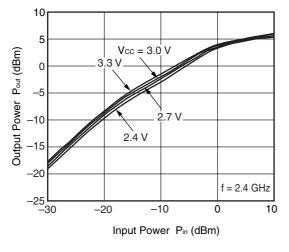


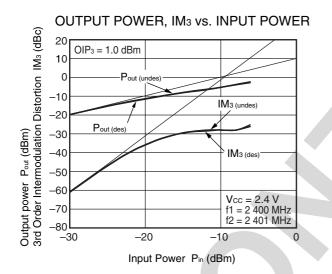
S21-FREQUENCY $S_{21} \ \text{log} \quad MAG \quad 2 \ \text{dB} / \quad \text{REF 0 } \text{dB} \quad 1 : \ 10.851 \ \text{dB}$ 20 $P_{in} = -30 \text{ dBm}, \text{ Vcc} = 3.0 \text{ V}$ 18 MARKER 1 f = 2.4 GHz 16 $T_A = -40^{\circ}C$ 14 12 10 +25°C 8 6 4 +85[°]C 2 0 START 100.000 000 MHz STOP 3 100.000 000 MHz

Remark The graphs indicate nominal characteristics.

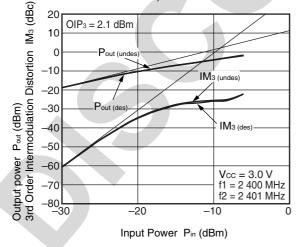


OUTPUT POWER vs. INPUT POWER

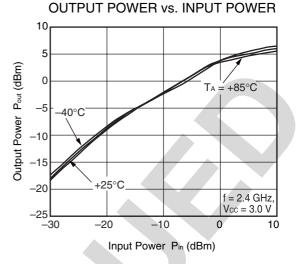




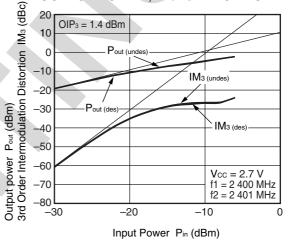




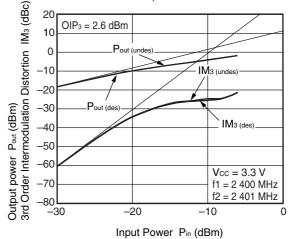
Remark The graphs indicate nominal characteristics.

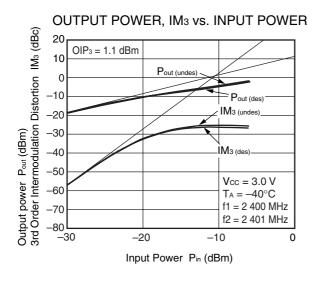


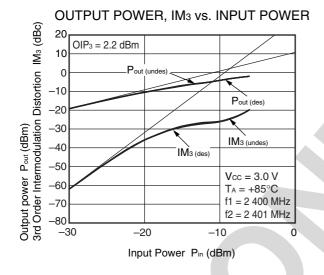
OUTPUT POWER, IM3 vs. INPUT POWER

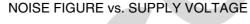


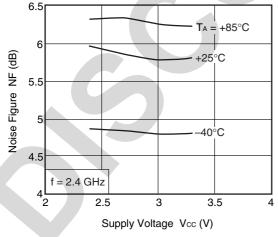
OUTPUT POWER, IM3 vs. INPUT POWER



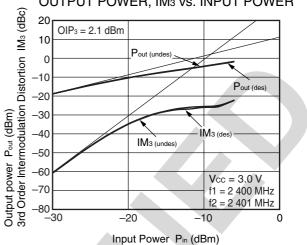




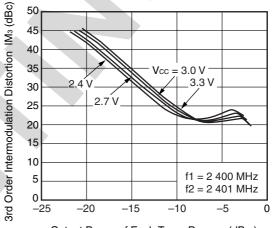




Remark The graphs indicate nominal characteristics.



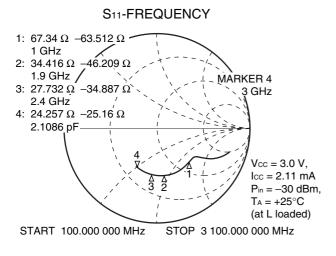
3RD ORDER INTERMODULATION DISTORTION vs. OUTPUT POWER OF EACH TONE



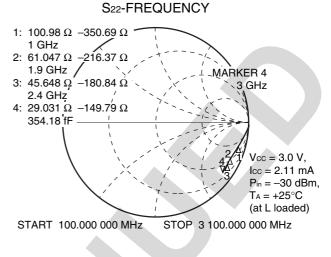
Output Power of Each Tone Pout (each) (dBm)

OUTPUT POWER, IM3 vs. INPUT POWER

f = 3.0 GHz MATCHING

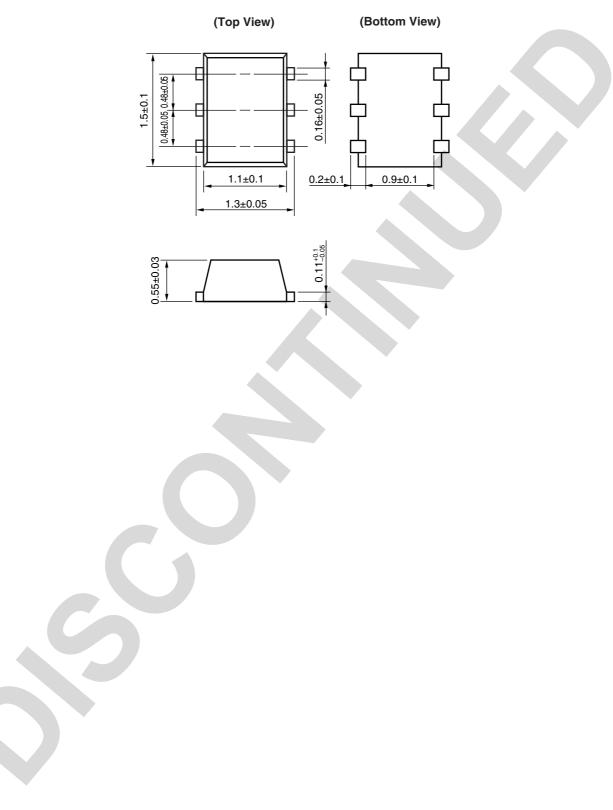






★ PACKAGE DIMENSIONS

6-PIN LEAD-LESS MINIMOLD (1511) (UNIT: mm)



NOTE ON CORRECT USE

- (1) Observe precautions for handling because of electro-static sensitive devices.
- (2) Form a ground pattern as widely as possible to minimize ground impedance (to prevent undesired oscillation).
 All the ground pins must be connected together with wide ground pattern to decrease impedance difference.
- (3) The bypass capacitor should be attached to Vcc line.
- (4) The inductor (L) should be attached between output and Vcc pins. The L and series capacitor (C) values should be adjusted for applied frequency to match impedance to next stage.
- (5) The DC capacitor must be attached to input pin.

★ RECOMMENDED SOLDERING CONDITIONS

6

This product should be soldered and mounted under the following recommended conditions. For soldering methods and conditions other than those recommended below, contact your nearby sales office.

Soldering Method	Soldering Conditions		Condition Symbol
Infrared Reflow	Peak temperature (package surface temperature) Time at peak temperature	: 260°C or below : 10 seconds or less	IR260
	Time at temperature of 220°C or higher	: 60 seconds or less	
	Preheating time at 120 to 180°C	: 120±30 seconds	
	Maximum number of reflow processes	: 3 times	
	Maximum chlorine content of rosin flux (% mass)	: 0.2%(Wt.) or below	
Wave Soldering	Peak temperature (molten solder temperature)	: 260°C or below	WS260
	Time at peak temperature	: 10 seconds or less	
	Preheating temperature (package surface temperature)	: 120°C or below	
	Maximum number of flow processes	: 1 time	
	Maximum chlorine content of rosin flux (% mass)	: 0.2%(Wt.) or below	
Partial Heating	Peak temperature (terminal temperature)	: 350°C or below	HS350
	Soldering time (per side of device)	: 3 seconds or less	
	Maximum chlorine content of rosin flux (% mass)	: 0.2%(Wt.) or below	

Caution Do not use different soldering methods together (except for partial heating).

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