VS-GB150LH120N

Vishay Semiconductors

Molding Type Module IGBT, Chopper in 1 Package, 1200 V and 150 A



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Dual INT-A-PA	(
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PRIMARY CHARACTERISTICS				
V _{CES}	1200 V			
I _C at T _C = 80 °C	150 A			
V _{CE(on)} (typical) at I _C = 150 A, 25 °C	1.87 V			
Speed	8 kHz to 30 kHz			
Package	Dual INT-A-PAK			
Circuit configuration	Low side chopper			

FEATURES

- High short circuit capability, self limiting to 6 x I_C
- 10 µs short circuit capability
- V_{CE(on)} with positive temperature coefficient
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 Maximum junction temperature 150 °C
- Low inductance case
- · Fast and soft reverse recovery antiparallel FWD
- Isolated copper baseplate using DCB (Direct Copper Bonding) technology
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

TYPICAL APPLICATIONS

- AC inverter drives
- Switching mode power supplies
- Electronic welders

DESCRIPTION

Vishay's IGBT power module provides ultra low conduction loss as well as short circuit ruggedness. It is designed for applications such as general inverters and UPS.

ABSOLUTE MAXIMUM RATINGS ($T_C = 25 \text{ °C}$ unless otherwise noted)						
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS		
Collector to emitter voltage	V _{CES}		1200	V		
Gate to emitter voltage	V _{GES}		± 20	v		
Collector ourrent		T _C = 25 °C	300			
Collector current	Ic	T _C = 80 °C	150	-		
Pulsed collector current	I _{CM} ⁽¹⁾	t _p = 1 ms	300	A		
Diode continuous forward current	I _F	T _C = 80 °C	150	-		
Diode maximum forward current	I _{FM}	t _p = 1 ms	300	-		
Maximum power dissipation	PD	T _J = 150 °C	1389	W		
Short circuit withstand time	t _{SC}	T _J = 125 °C	10	μs		
RMS isolation voltage	V _{ISOL}	f = 50 Hz, t = 1 min	2500	V		
l ² t-value, diode	l ² t	V _R = 0 V, t = 10 ms, T _J = 125 °C	4800	A ² s		

Note

⁽¹⁾ Repetitive rating: pulse width limited by maximum junction temperature

IGBT ELECTRICAL SPECIFICATIONS ($T_c = 25 \text{ °C}$ unless otherwise noted)							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS	
Collector to emitter breakdown voltage	V _{(BR)CES}	T _J = 25 °C	1200	-	-		
	V _{CE(on)}	$V_{GE} = 15 \text{ V}, \text{ I}_{C} = 150 \text{ A}, \text{ T}_{J} = 25 ^{\circ}\text{C}$	-	1.87	-	v	
Collector to emitter voltage		V_{GE} = 15 V, I_{C} = 150 A, T_{J} = 125 °C	-	2.08	-		
Gate to emitter threshold voltage	V _{GE(th)}	V_{CE} = V_{GE} , I_C = 12.0 mA, T_J = 25 °C	5.0	6.3	7.0		
Collector cut-off current	I _{CES}	$V_{CE} = V_{CES}, V_{GE} = 0 \text{ V}, \text{ T}_{J} = 25 \text{ °C}$	-	-	1.0	mA	
Gate to emitter leakage current	I _{GES}	$V_{GE} = V_{GES}, V_{CE} = 0 V, T_J = 25 \text{ °C}$	-	-	400	nA	

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SWITCHING CHARACTERISTICS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Turn-on delay time	t _{d(on)}		-	190	-	
Rise time	t _r		-	60	-	
Turn-off delay time	t _{d(off)}	V_{CC} = 600 V, I_{C} = 150 A, R_{g} = 6.8 Ω,	-	460	-	ns
Fall time	t _f	$V_{GE} = \pm 15 \text{ V}, T_{J} = 25 \text{ °C}$	-	55	-	
Turn-on switching loss	E _{on}		-	11.2	-	mJ
Turn-off switching loss	E _{off}		-	9.8	-	mJ
Turn-on delay time	t _{d(on)}		-	220	-	ns
Rise time	t _r		-	60	-	
Turn-off delay time	t _{d(off)}	V_{CC} = 600 V, I _C = 150 A, R _g = 6.8 Ω ,	-	530	-	
Fall time	t _f	$V_{GE} = \pm 15 \text{ V}, T_{J} = 125 \text{ °C}^{\circ}$	-	75	-	
Turn-on switching loss	E _{on}		-	16.7	-	mJ
Turn-off switching loss	E _{off}		-	15.3	-	1110
Input capacitance	Cies		-	10.6	-	
Output capacitance	C _{oes}	V _{GE} = 0 V, V _{CE} = 25 V, f = 1.0 MHz	-	0.71	-	nF
Reverse transfer capacitance	C _{res}		-	0.47	-	
SC data	I _{SC}	$\begin{array}{l} t_{sc} \leq 10 \; \mu s, V_{GE} = 15 \; V, T_{J} = 125 \; ^{\circ}C, \\ V_{CC} = 900 \; V, V_{CEM} \leq 1200 \; V \end{array}$	-	650	-	А
Internal gate resistance	R _{gint}		-	1.5	-	Ω
Stray inductance	L _{CE}		-	-	20	nH
Module lead resistance, terminal to chip	R _{CC'+EE'}	T _C = 25 °C	-	0.35	-	mΩ

DIODE ELECTRICAL SPECIFICATIONS ($T_c = 25$ °C unless otherwise noted)							
PARAMETER	SYMBOL	TEST CONDIT	TEST CONDITIONS MIN		TYP.	MAX.	UNITS
Diada faa aadaallaa	V	I _F = 150 A	T _J = 25 °C	-	2.05	-	v
Diode forward voltage	V _F		T _J = 125 °C	-	2.26	-	
D'ada ana ana ana ana ana ana ana ana ana	Q _{rr}	Q _{rr} I _F = 150 A, V _R = 600 V, dl/dt = -4800 A/µs, V _{GF} = -15 V	T _J = 25 °C	-	7	-	
Diode reverse recovery charge			T _J = 125 °C	-	18	-	μC
Diode peak reverse recovery current	I _{rr}		T _J = 25 °C	-	150	-	
			T _J = 125 °C	-	190	-	А
Diode reverse recovery energy E	-		T _J = 25 °C	-	4.0	-	
	E _{rec}		T _J = 125 °C	-	8.0	-	mJ

THERMAL AND MECHANICAL SPECIFICATIONS							
PARAMETER		SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Operating junction	temperature range	TJ		-40	-	150	°C
Storage temperature range		T _{STG}		-40	-	125	C
L Para la service	IGBT (per 1/2 module)	R _{thJC}		-	-	0.09	K/W
Junction to case	Diode (per 1/2 module)			-	-	0.24	
Case to sink		R _{thCS}	Conductive grease applied	-	0.035	-	
Mounting torque			Power terminal screw: M6		2.5 to 5.0)	Nm
			Mounting screw: M6		3.0 to 6.0)	INM
Weight					300		g

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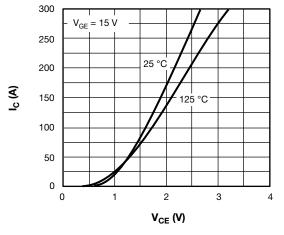


Fig. 1 - Typical Output Characteristics

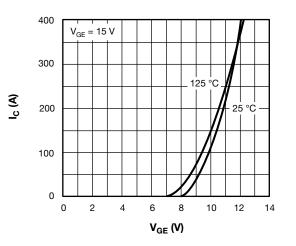


Fig. 2 - Typical Transfer Characteristics

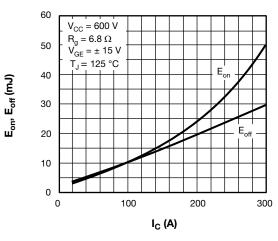


Fig. 3 - Switching Loss vs. Collector Current

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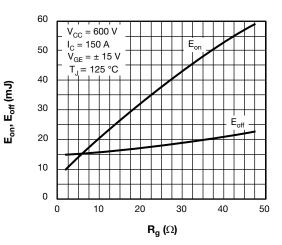


Fig. 4 - Switching Loss vs. gate Resistor

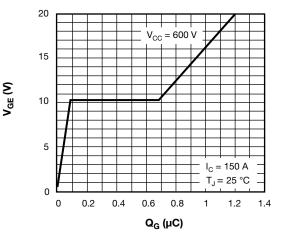


Fig. 5 - Gate Charge Characteristics

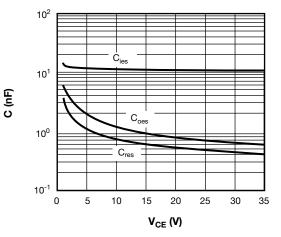


Fig. 6 - Typical Capacitance vs. Collector to Emitter Voltage

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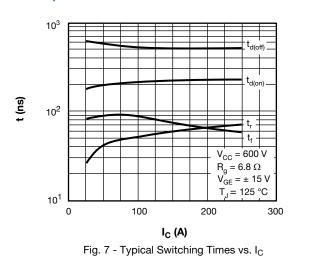
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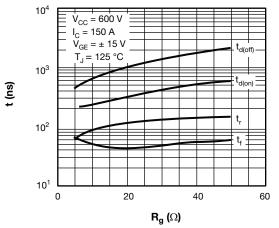
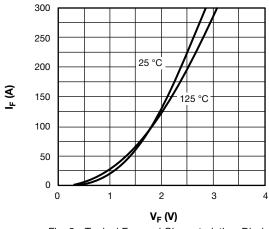
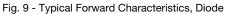


Fig. 8 - Typical Switching Times vs. Gate Resistance R_q





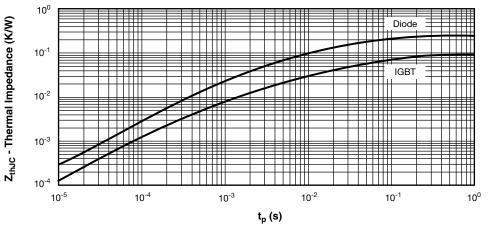
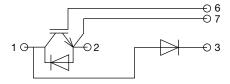


Fig. 10 - Transient Thermal Impedance



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CIRCUIT CONFIGURATION



LINKS TO RELATED DOCUMENTS					
Dimensions www.vishay.com/doc?95525					



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