

DO-214AB (SMC)

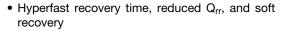
Vishay Semiconductors

Hyperfast Rectifier, 3 A FRED Pt®



PRODUCT SUMMARY				
Package	DO-214AB (SMC)			
I _{F(AV)}	3 A			
V_{R}	200 V			
V _F at I _F	0.69 V			
t _{rr}	25 ns			
T _J max.	175 °C			
Diode variation	Single die			

FEATURES





RoHS

COMPLIANT **HALOGEN**

FREE

- 175 °C maximum operating junction temperature
- Specified for output and snubber operation
- Low forward voltage drop
- · Low leakage current
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- AEC-Q101 qualified, meets JESD 201 class 2 whisker test
- · Material categorization: for definitions of compliance please see www.vishav.com/doc?99912

DESCRIPTION / APPLICATIONS

State of the art hyperfast recovery rectifiers specifically designed with optimized performance of forward voltage drop and hyperfast recovery time.

The planar structure and the platinum doped life time control guarantee the best overall performance, ruggedness, and reliability characteristics.

These devices are intended for use in snubber, boost, lighting, piezo-injection, as high frequency rectifiers, and freewheeling diodes.

The extremely optimized stored charge and low recovery current minimize the switching losses and reduce power dissipation in the switching element.

ABSOLUTE MAXIMUM RATINGS				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Peak repetitive reverse voltage	V_{RRM}		200	V
Average rectified forward current	I _{F(AV)}	T _{Sp} = 142 °C	3	۸
Non-repetitive peak surge current	I _{FSM}	T _J = 25 °C, 6 ms square pulse	130	A
Operating junction and storage temperatures	T _J , T _{Stg}		-55 to +175	°C

ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Breakdown voltage, blocking voltage	V_{BR} , V_{R}	I _R = 100 μA	200	-	-	
Forward voltage, per diode	V _F	I _F = 3 A	-	0.83	0.90	V
Torward voitage, per diode	VF	I _F = 3 A, T _J = 125 °C	-	0.69	0.75	
Reverse leakage current, per diode	_	V _R = V _R rated	-	-	2	
Reverse leakage current, per diode		T _J = 125 °C, V _R = V _R rated	-	2	10	μA
Junction capacitance	C _T	V _R = 200 V	-	23	=	pF



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DYNAMIC RECOVERY CHARACTERISTICS (T _J = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
		$I_F = 1 A, dI_F/dt = 50$	A_{r} , $dI_{F}/dt = 50 A/\mu s$, $V_{R} = 30 V$		27	-	
Reverse recovery time		I _F = 0.5 A, I _R = 1 A, I _{rr} = 0.25 A		-	-	25	
neverse recovery time	t _{rr}	T _J = 25 °C	I _F = 3 A, dI _F /dt = 200 A/μs,	-	18	-	ns
		T _J = 125 °C		-	30	-	
Dook recovery ourrent	Deal, we are supported by	T _J = 25 °C		-	2.1	-	A
Peak recovery current I _{RRM}	T _J = 125 °C	$V_{R} = 100 \text{ V}$	-	4	-		
Reverse recovery charge Q _{rr}	0	T _J = 25 °C		-	19	-	nC
	Q _{rr}	T _J = 125 °C		-	60	-	

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Maximum junction and storage temperature range	T _J , T _{Stg}		-55	-	175	°C
Thermal resistance, junction to case	R _{thJC}	Device mounted on PCB with 2 x 3.5 mm soldering lands	-	7.7	14	°C/W
Approximate weight				0.24		g
Approximate weight				0.008		oz.
Marking device		Case style SMC (DO-214AB)		31	1 2	

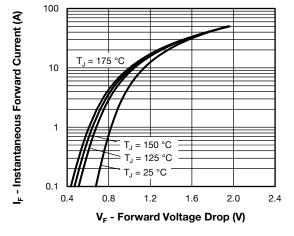


Fig. 1 - Typical Forward Voltage Drop Characteristics

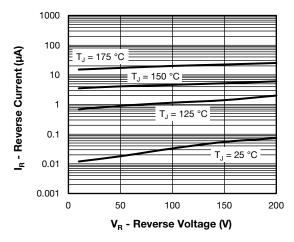


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

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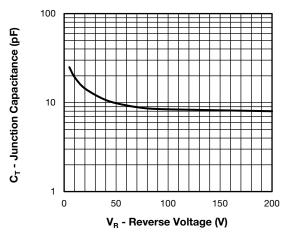


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

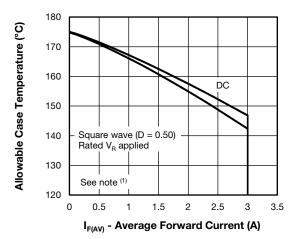


Fig. 4 - Maximum Allowable Case Temperature vs.
Average Forward Current

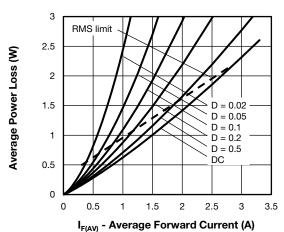


Fig. 5 - Forward Power Loss Characteristics

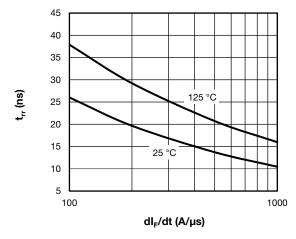


Fig. 6 - Typical Reverse Recovery Time vs. dl_F/dt

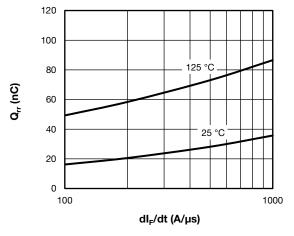
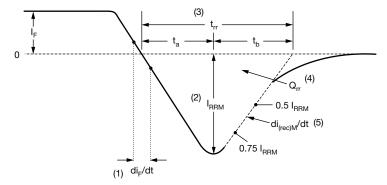


Fig. 7 - Typical Stored Charge vs. dl_F/dt

Note

 $\begin{array}{ll} \text{(1)} & \text{Formula used: } T_C = T_J - (\text{Pd} + \text{Pd}_{\text{REV}}) \times R_{\text{thJC}}; \\ \text{Pd} & = \text{Forward power loss} = I_{\text{F(AV)}} \times V_{\text{FM}} \text{ at } (I_{\text{F(AV)}}/D) \text{ (see fig. 5)}; \\ \text{Pd}_{\text{REV}} & = \text{Inverse power loss} = V_{\text{R1}} \times I_{\text{R}} \text{ (1 - D)}; I_{\text{R}} \text{ at } V_{\text{R1}} = \text{rated } V_{\text{R}} \\ \end{array}$

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- (1) di_F/dt rate of change of current through zero crossing
- (2) I_{RRM} peak reverse recovery current
- (3) $\rm t_{rr}$ reverse recovery time measured from zero crossing point of negative going $\rm I_F$ to point where a line passing through 0.75 $\rm I_{RRM}$ and 0.50 $\rm I_{RRM}$ extrapolated to zero current.
- (4) $\rm Q_{rr}$ area under curve defined by $\rm t_{rr}$ and $\rm I_{RRM}$

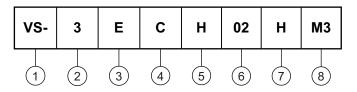
$$Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$$

(5) $di_{(rec)M}/dt$ - peak rate of change of current during t_b portion of t_{rr}

Fig. 8 - Reverse Recovery Waveform and Definitions

ORDERING INFORMATION TABLE

Device code



- 1 Vishay Semiconductors product
- 2 Current code (3 = 3 A)
- 3 Circuit configuration:

E = single diode

- 4 C = SMC package
- **5** Process type,

H = hyperfast recovery

- 6 Voltage code (02 = 200 V)
- 7 H = AEC-Q101 qualified
- 7 M3 = halogen-free, RoHS-compliant, and terminations lead (Pb)-free

ORDERING INFORMATION (Example)					
PREFERRED P/N	QUANTITY PER REEL	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION		
VS-3ECH02HM3/9AT	3500	3500	13"diameter plastic tape and reel		

LINKS TO RELATED DOCUMENTS				
Dimensions	www.vishay.com/doc?95402			
Part marking information	www.vishay.com/doc?95472			
Packaging information	www.vishay.com/doc?95404			

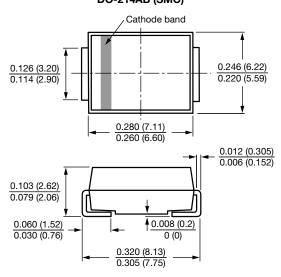


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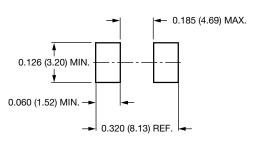
SMC

DIMENSIONS in inches (millimeters)

DO-214AB (SMC)



Mounting Pad Layout





Legal Disclaimer Notice

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