Vishay Semiconductors

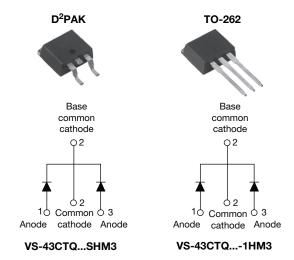
RoHS

COMPLIANT

HALOGEN

FREE

High Performance Schottky Rectifier, 2 x 20 A



www.vishay.com

PRODUCT SUMMARY						
I _{F(AV)}	2 x 20 A					
V _R	80 V, 100 V					
V _F at I _F	0.67 V					
I _{RM} max.	11 mA at 125 °C					
T _J max.	175 °C					
E _{AS}	7.50 mJ					
Package	TO-263AB (D ² PAK), TO-262AA					
Diode variation	Common cathode					

FEATURES

- 175 °C T_J operation
- · Center tap configuration
- Low forward voltage drop
- purity, high High temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance
- High frequency operation
- · Guard ring for enhanced ruggedness and long term reliability
- · Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- AEC-Q101 qualified meets JESD 201 class 1A whisker test
- · Material categorization: For definitions of compliance please see www.vishay.com/doc?99912

DESCRIPTION

This center tap Schottky rectifier series has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 175 °C junction temperature. Typical applications are in switching power supplies, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS							
SYMBOL	CHARACTERISTICS	CHARACTERISTICS VALUES					
I _{F(AV)}	Rectangular waveform	40	A				
V _{RRM}		80/100	V				
I _{FSM}	t _p = 5 μs sine	850	A				
V _F	20 A_{pk} , $T_J = 125 \ ^{\circ}C$ (per leg)	0.67	V				
TJ	Range	-55 to 175	°C				

VOLTAGE RATINGS							
PARAMETER SYMBOL		VS-43CTQ080SHM3 VS-43CTQ080-1HM3	VS-43CTQ100SHM3 VS-43CTQ100-1HM3	UNITS			
Maximum DC reverse voltage	V _R	80	100	V			
Maximum working peak reverse voltage	V _{RWM}	00	100	v			

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ABSOLUTE MAXIMUM RATINGS									
PARAMETER		SYMBOL	TEST COND	ITIONS	VALUES	UNITS			
Maximum average	per leg				20				
forward current See fig. 5	per device	I _{F(AV)}	$F_{(AV)}$ 50 % duty cycle at T _C = 135 °C, rectangular waveform		40	А			
Maximum peak one cycle n	non-repetitive		5 µs sine or 3 µs rect. pulse	Following any rated load	850	A			
surge current per leg See fig. 7		I _{FSM}	10 ms sine or 6 ms rect. pulse	condition and with rated V _{RRM} applied	275				
Non-repetitive avalanche energy per leg		E _{AS}	T _J = 25 °C, I _{AS} = 0.50 A, L = 60 mH		7.50	mJ			
Repetitive avalanche current per leg		I _{AR}	Current decaying linearly to zer Frequency limited by T_J maxim		0.50	А			

ELECTRICAL SPECIFICATIONS								
PARAMETER	SYMBOL	TEST	CONDITIONS	VALUES	UNITS			
		20 A	T ₁ = 25 °C	0.81	V			
Maximum forward voltage drop per leg	V _{FM} ⁽¹⁾	40 A	1j=25 C	0.98				
See fig. 1	VFM (")	20 A	—T = 125 °C	0.67				
		40 A	$I_{\rm J} = 125$ C	0.81				
Maximum reverse leakage current per leg	I _{RM} ⁽¹⁾	T _J = 25 °C	$V_{\rm B}$ = Rated V _B	1	mA			
See fig. 2		T _J = 125 °C	$v_{\rm R}$ = haled $v_{\rm R}$	11				
Threshold voltage	V _{F(TO)}	- T _J = T _J maximum		0.71	V			
Forward slope resistance	r _t			0.43	mΩ			
Maximum junction capacitance per leg	CT	$V_R = 5 V_{DC}$ (test signal range 100 kHz to 1 MHz), 25 °C		1480	pF			
Typical series inductance per leg	Ls	Measured lead to lead 5 mm from package body 8.0			nH			
Maximum voltage rate of change	dV/dt	Rated V _R 10 000			V/µs			

Note

⁽¹⁾ Pulse width < 300 μ s, duty cycle < 2 %

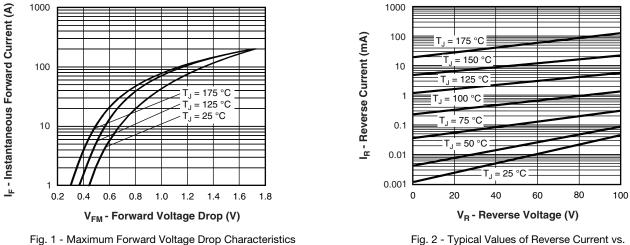
THERMAL - MECH	ANICAL SPI	ECIFICAT	IONS		
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and stor temperature range	rage	T _J , T _{Stg}		-55 to 175	°C
Maximum thermal resistand junction to case per leg	Maximum thermal resistance, junction to case per leg			2.0	
Maximum thermal resistance, junction to case per package		n _{th} JC	R _{thJC} DC operation	1.0	°C/W
Typical thermal resistance, case to heatsink		R _{thCS}	Mounting surface, smooth and greased	0.50	
Approximate weight				2	g
				0.07	oz.
Mounting torque	minimum			6 (5)	kgf ⋅ cm
woulding torque	maximum			12 (10)	(lbf · in)
			$\Omega_{\rm res}$ at $h_{\rm c} D^2 D M$	43CTQ	080SH
Marking device			Case style D ² PAK	43CTQ	100SH
			Case style TO 262	43CTQ	080-1H
			Case style TO-262		100-1H

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(Per Leg)

Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

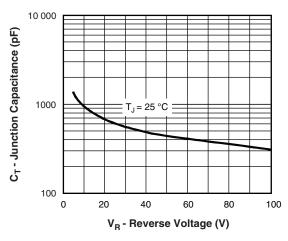
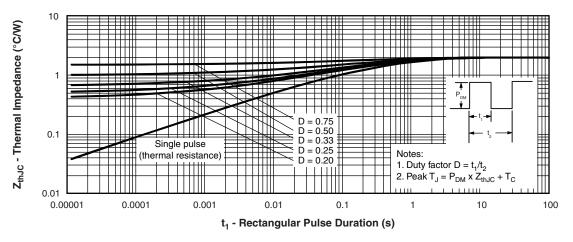


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

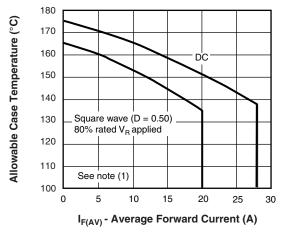


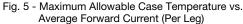


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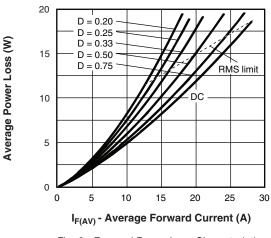


Fig. 6 - Forward Power Loss Characteristics (Per Leg)

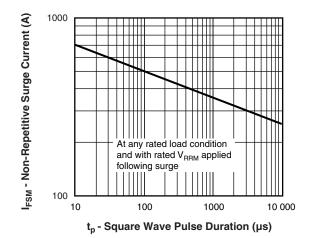


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

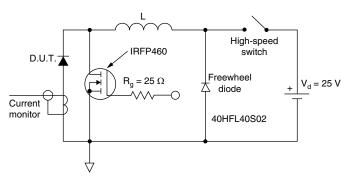


Fig. 8 - Unclamped Inductive Test Circuit

Note

⁽¹⁾ Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$;

 $\begin{array}{l} \mathsf{Pd} = \mathsf{Forward} \ \mathsf{power} \ \mathsf{loss} = \mathsf{I}_{\mathsf{F}(\mathsf{AV})} \times \mathsf{V}_{\mathsf{FM}} \ \mathsf{at} \ (\mathsf{I}_{\mathsf{F}(\mathsf{AV})}/\mathsf{D}) \ (\mathsf{see fig. 6}); \\ \mathsf{Pd}_{\mathsf{REV}} = \mathsf{Inverse} \ \mathsf{power} \ \mathsf{loss} = \mathsf{V}_{\mathsf{R1}} \times \mathsf{I}_{\mathsf{R}} \ (\mathsf{1} - \mathsf{D}); \ \mathsf{I}_{\mathsf{R}} \ \mathsf{at} \ \mathsf{V}_{\mathsf{R1}} = \mathsf{10} \ \mathsf{V} \end{array}$

Revision: 06-Mar-14

4

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Vishay Semiconductors

ORDERING INFORMATION TABLE

Device code	VS-	43	С	т	Q	100	S	TRL	н	М3
		2	3	4	5	6	7	8	9	10
	1	 Vishay Semiconductors product Current rating (40 A) 								
	3	- Circ	uit conf	iguratior		common	cathoo	le		
	片		T = TO-220 Schottky "Q" series							
	6 · 7 ·		tage rati = D ² PA	-		080 = 100 = 1				
			= TO-2							
	8		 None = Tube TRL = Tape and reel (left oriented - for D²PAK only) 							
	9			ipe and 101 qua		ht orien	ted - fo	r D ² PAk	(only)	
	10					-complia	ant and	termina	tion lea	d (Pb)-fr

ORDERING INFORMATION							
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION				
VS-43CTQ080SHM3	50	1000	Antistatic plastic tubes				
VS-43CTQ080STRRHM3	800	800	13" diameter reel				
VS-43CTQ080STRLHM3	800	800	13" diameter reel				
VS-43CTQ080-1HM3	50	1000	Antistatic plastic tubes				
VS-43CTQ100SHM3	50	1000	Antistatic plastic tubes				
VS-43CTQ100STRRHM3	800	800	13" diameter reel				
VS-43CTQ100STRLHM3	800	800	13" diameter reel				
VS-43CTQ100-1HM3	50	1000	Antistatic plastic tubes				

LINKS TO RELATED DOCUMENTS						
Dimensions —	TO-263AB (D ² PAK)	www.vishay.com/doc?95046				
Dimensions	TO-262AA	www.vishay.com/doc?95419				
Part marking information —	TO-263AB (D ² PAK)	www.vishay.com/doc?95444				
	TO-262AA	www.vishay.com/doc?95443				
Packaging information		www.vishay.com/doc?95032				
SPICE model		www.vishay.com/doc?95065				

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Outline Dimensions



D²PAK

DIMENSIONS in millimeters and inches

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SHA



SYMBOL	MILLIM	IETERS	INC	HES	NOTES	SYMBOL	MILLIM	IETERS	INC	HES	NOTES
STMBOL	MIN.	MAX.	MIN.	MAX.	NOTES	STWDUL	MIN.	MAX.	MIN.	MAX.	NOTES
А	4.06	4.83	0.160	0.190		D1	6.86	8.00	0.270	0.315	3
A1	0.00	0.254	0.000	0.010		E	9.65	10.67	0.380	0.420	2, 3
b	0.51	0.99	0.020	0.039		E1	7.90	8.80	0.311	0.346	3
b1	0.51	0.89	0.020	0.035	4	е	2.54	BSC	0.100	BSC	
b2	1.14	1.78	0.045	0.070		Н	14.61	15.88	0.575	0.625	
b3	1.14	1.73	0.045	0.068	4	L	1.78	2.79	0.070	0.110	
С	0.38	0.74	0.015	0.029		L1	-	1.65	-	0.066	3
c1	0.38	0.58	0.015	0.023	4	L2	1.27	1.78	0.050	0.070	
c2	1.14	1.65	0.045	0.065		L3	0.25	BSC	0.010	BSC	
D	8.51	9.65	0.335	0.380	2	L4	4.78	5.28	0.188	0.208	

Notes

⁽¹⁾ Dimensioning and tolerancing per ASME Y14.5 M-1994

⁽²⁾ Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body

⁽³⁾ Thermal pad contour optional within dimension E, L1, D1 and E1

⁽⁴⁾ Dimension b1 and c1 apply to base metal only

⁽⁵⁾ Datum A and B to be determined at datum plane H

⁽⁶⁾ Controlling dimension: inch

⁽⁷⁾ Outline conforms to JEDEC[®] outline TO-263AB

Revision: 08-Jul-15

1

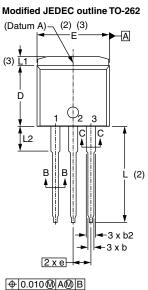


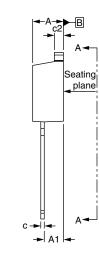
Outline Dimensions

Vishay Semiconductors

TO-262

DIMENSIONS in millimeters and inches

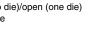


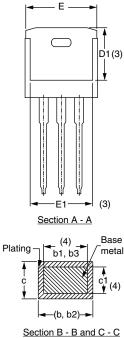


Lead assignments



Diodes 1. - Anode (two die)/open (one die) 2., 4. - Cathode 3. - Anode





Scale: None

SYMBOL		INC	NOTES		
STMBOL	MIN.	MAX.	MIN.	MAX.	NOTES
А	4.06	4.83	0.160	0.190	
A1	2.03	3.02	0.080	0.119	
b	0.51	0.99	0.020	0.039	
b1	0.51	0.89	0.020	0.035	4
b2	1.14	1.78	0.045	0.070	
b3	1.14	1.73	0.045	0.068	4
С	0.38	0.74	0.015	0.029	
c1	0.38	0.58	0.015	0.023	4
c2	1.14	1.65	0.045	0.065	
D	8.51	9.65	0.335	0.380	2
D1	6.86	8.00	0.270	0.315	3
E	9.65	10.67	0.380	0.420	2, 3
E1	7.90	8.80	0.311	0.346	3
е	2.54	BSC	0.10	0 BSC	
L	13.46	14.10	0.530	0.555	
L1	-	1.65	-	0.065	3
L2	3.56	3.71	0.140	0.146	

Notes

Revision: 04-Oct-10

⁽¹⁾ Dimensioning and tolerancing as per ASME Y14.5M-1994

⁽²⁾ Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body

⁽⁴⁾ Dimension b1 and c1 apply to base metal only

(5) Controlling dimension: inches

(6) Outline conform to JEDEC TO-262 except A1 (maximum), b (minimum) and D1 (minimum) where dimensions derived the actual package outline

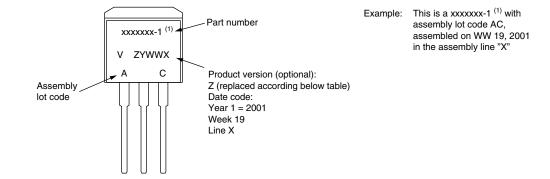
⁽³⁾ Thermal pad contour optional within dimension E, L1, D1 and E1

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TO-262



Note

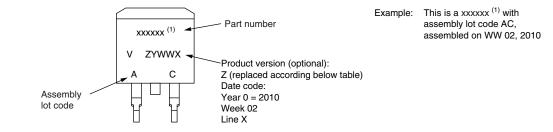
⁽¹⁾ If part number contain "H" as last digit, product is AEC-Q101 qualified

ENVIRONMENTAL NAMING CODE (Z)	PRODUCT DEFINITION		
A	Termination lead (Pb)-free		
В	Totally lead (Pb)-free		
E	RoHS-compliant and termination lead (Pb)-free		
F	RoHS-compliant and totally lead (Pb)-free		
М	Halogen-free, RoHS-compliant and termination lead (Pb)-free		
Ν	Halogen-free, RoHS-compliant and totally lead (Pb)-free		
G	Green		



Vishay Semiconductors

D²PAK



Note

⁽¹⁾ If part number contain "H" as last digit, product is AEC-Q101 qualified

ENVIRONMENTAL NAMING CODE (Z)	PRODUCT DEFINITION
A	Termination lead (Pb)-free
В	Totally lead (Pb)-free
E	RoHS-compliant and termination lead (Pb)-free
F	RoHS-compliant and totally lead (Pb)-free
М	Halogen-free, RoHS-compliant, and termination lead (Pb)-free
N	Halogen-free, RoHS-compliant, and totally lead (Pb)-free
G	Green



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