

**74ACT374PC Information**


For Reference Only

**Part Number** [74ACT374PC](#)  
**Manufacturer** Fairchild/ON Semiconductor  
**Category** Integrated Circuits (ICs)  
[Logic - Flip Flops](#)  
**Description** IC D-TYPE POS TRG SNGL 20DIP  
**Package** 20-DIP (0.300", 7.62mm)  
 For the pricing/inventory/lead time, please contact us  
 Website: <https://www.heisener.com>  
 E-mail: [salesdept@heisener.com](mailto:salesdept@heisener.com)


[Request a Quote](#)
**Certified Quality**

Heisener's commitment to quality has shaped our processes for sourcing, testing, shipping, and every step in between. This foundation underlies each component we sell.


**74ACT374PC Specifications**

Manufacturer Part Number	<a href="#">74ACT374PC</a>
Manufacturer	Fairchild/ON Semiconductor
Category	Integrated Circuits (ICs)
	<a href="#">Logic - Flip Flops</a>
Package	20-DIP (0.300", 7.62mm)
Series	74ACT
Function	Standard
Type	D-Type
Output Type	Tri-State, Non-Inverted
Number of Elements	1
Number of Bits per Element	8
Clock Frequency	160MHz
Max Propagation Delay @ V, Max CL	10ns @ 5V, 50pF
Trigger Type	Positive Edge
Current - Output High, Low	24mA, 24mA
Voltage - Supply	4.5 V ~ 5.5 V
Current - Quiescent (Iq)	40µA
Input Capacitance	4.5pF
Operating Temperature	-40°C ~ 85°C (TA)
Mounting Type	Through Hole
Package / Case	20-DIP (0.300", 7.62mm)
<a href="#">Report errors?</a>	

## 74ACT374PC Guarantees



### Quality Guarantees

We provide 90 days warranty. \*

If the items you received were not in perfect quality, we would be responsible for your refund or replacement, but the items must be returned in their original condition.



### Service Guarantees

We guarantee 100% customer satisfaction.

Our experienced sales team and tech support team back our services to satisfy all our customers.

## 74ACT374PC Payment Methods



## 74ACT374PC Shipping Methods



If you have any question about 74ACT374PC, please do not hesitate to contact us!

Website: <https://www.heisener.com>

E-mail: [salesdept@heisener.com](mailto:salesdept@heisener.com)