



#### **AD5541JRZ Information**



For Reference Only

Part Number AD5541JRZ

Manufacturer Analog Devices Inc.

Category Integrated Circuits (ICs)

Data Acquisition - Digital to Analog Converters

(DAC)

**Description** IC DAC 16BIT SERIAL IN 8-SOIC **Package** 8-SOIC (0.154", 3.90mm Width)

For the pricing/inventory/lead time, please contact

us

Website: https://www.heisener.com E-mail: salesdept@heisener.com



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# **AD5541JRZ Specifications**

Manufacturer Part Number       AD5541JRZ         Manufacturer       Analog Devices Inc.         Category       Integrated Circuits (ICs)         Data Acquisition - Digital to Analog Converters (DAC)         Package       8-SOIC (0.154", 3.90mm Width)         Series       -         Number of Bits       16         Number of D/A Converters       1         Settling Time       1μs (Typ)         Output Type       Voltage - Unbuffered         Differential Output       No         Data Interface       SPI, DSP         Reference Type       External         Voltage - Supply, Analog       2.7 V ~ 5.5 V         Voltage - Supply, Digital       2.7 V ~ 5.5 V         INL/DNL (LSB)       ±0.5, ±0.5         Architecture       R-2R         Opporting Temperature       0°C - 70°C		
CategoryIntegrated Circuits (ICs)Data Acquisition - Digital to Analog Converters (DAC)Package8-SOIC (0.154", 3.90mm Width)Series-Number of Bits16Number of D/A Converters1Settling Time1μs (Typ)Output TypeVoltage - UnbufferedDifferential OutputNoData InterfaceSPI, DSPReference TypeExternalVoltage - Supply, Analog2.7 V ~ 5.5 VVoltage - Supply, Digital2.7 V ~ 5.5 VINL/DNL (LSB)±0.5, ±0.5ArchitectureR-2R	Manufacturer Part Number	AD5541JRZ
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Package8-SOIC (0.154", 3.90mm Width)Series-Number of Bits16Number of D/A Converters1Settling Time1μs (Typ)Output TypeVoltage - UnbufferedDifferential OutputNoData InterfaceSPI, DSPReference TypeExternalVoltage - Supply, Analog2.7 V ~ 5.5 VVoltage - Supply, Digital2.7 V ~ 5.5 VINL/DNL (LSB)±0.5, ±0.5ArchitectureR-2R	Category	Integrated Circuits (ICs)
Series - Number of Bits 16  Number of D/A Converters 1  Settling Time 1 $\mu$ s (Typ)  Output Type Voltage - Unbuffered  Differential Output No  Data Interface SPI, DSP  Reference Type External  Voltage - Supply, Analog 2.7 V ~ 5.5 V  Voltage - Supply, Digital 2.7 V ~ 5.5 V  INL/DNL (LSB) $\pm 0.5, \pm 0.5$ Architecture R-2R		Data Acquisition - Digital to Analog Converters (DAC)
Number of Bits 16  Number of D/A Converters 1  Settling Time 1 $\mu$ s (Typ)  Output Type Voltage - Unbuffered  Differential Output No  Data Interface SPI, DSP  Reference Type External  Voltage - Supply, Analog 2.7 V ~ 5.5 V  Voltage - Supply, Digital 2.7 V ~ 5.5 V  INL/DNL (LSB) $\pm$ 0.5, $\pm$ 0.5  Architecture R-2R	Package	8-SOIC (0.154", 3.90mm Width)
$\begin{array}{cccccccccccccccccccccccccccccccccccc$	Series	-
Settling Time $1\mu s \ (Typ)$ $Output \ Type \qquad Voltage - Unbuffered$ $Differential \ Output \qquad No$ $Data \ Interface \qquad SPI, \ DSP$ $Reference \ Type \qquad External$ $Voltage - Supply, \ Analog \qquad 2.7 \ V \sim 5.5 \ V$ $Voltage - Supply, \ Digital \qquad 2.7 \ V \sim 5.5 \ V$ $INL/DNL \ (LSB) \qquad \pm 0.5, \pm 0.5$ $Architecture \qquad R-2R$	Number of Bits	16
Output TypeVoltage - UnbufferedDifferential OutputNoData InterfaceSPI, DSPReference TypeExternalVoltage - Supply, Analog $2.7 \text{ V} \sim 5.5 \text{ V}$ Voltage - Supply, Digital $2.7 \text{ V} \sim 5.5 \text{ V}$ INL/DNL (LSB) $\pm 0.5, \pm 0.5$ ArchitectureR-2R	Number of D/A Converters	1
Differential OutputNoData InterfaceSPI, DSPReference TypeExternalVoltage - Supply, Analog $2.7 \text{ V} \sim 5.5 \text{ V}$ Voltage - Supply, Digital $2.7 \text{ V} \sim 5.5 \text{ V}$ INL/DNL (LSB) $\pm 0.5, \pm 0.5$ ArchitectureR-2R	Settling Time	1μs (Typ)
Data InterfaceSPI, DSPReference TypeExternalVoltage - Supply, Analog $2.7 \text{ V} \sim 5.5 \text{ V}$ Voltage - Supply, Digital $2.7 \text{ V} \sim 5.5 \text{ V}$ INL/DNL (LSB) $\pm 0.5, \pm 0.5$ ArchitectureR-2R	Output Type	Voltage - Unbuffered
Reference TypeExternalVoltage - Supply, Analog $2.7 \text{ V} \sim 5.5 \text{ V}$ Voltage - Supply, Digital $2.7 \text{ V} \sim 5.5 \text{ V}$ INL/DNL (LSB) $\pm 0.5, \pm 0.5$ Architecture $R-2R$	Differential Output	No
Voltage - Supply, Analog $2.7 \text{ V} \sim 5.5 \text{ V}$ Voltage - Supply, Digital $2.7 \text{ V} \sim 5.5 \text{ V}$ INL/DNL (LSB) $\pm 0.5, \pm 0.5$ Architecture R-2R	Data Interface	SPI, DSP
Voltage - Supply, Digital 2.7 V $\sim$ 5.5 V INL/DNL (LSB) $\pm 0.5, \pm 0.5$ Architecture R-2R	Reference Type	External
INL/DNL (LSB) $\pm 0.5, \pm 0.5$ Architecture R-2R	Voltage - Supply, Analog	2.7 V ~ 5.5 V
Architecture R-2R	Voltage - Supply, Digital	2.7 V ~ 5.5 V
	INL/DNL (LSB)	$\pm 0.5, \pm 0.5$
Operating Temperature 0°C - 70°C	Architecture	R-2R
Operating Temperature 0 C ~ 70 C	Operating Temperature	0°C ~ 70°C
Package / Case 8-SOIC (0.154", 3.90mm Width)	Package / Case	8-SOIC (0.154", 3.90mm Width)
Supplier Device Package 8-SOIC	Supplier Device Package	8-SOIC
Mounting Type -	Mounting Type	-
Report errors		Report errors?

### **AD5541JRZ Guarantees**



### **Quality Guarantees**

We provide 90 days warranty. \*

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### **AD5541JRZ Payment Methods**



















## **AD5541JRZ Shipping Methods**













If you have any question about AD5541JRZ, please do not hesitate to contact us!

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