

**CD4027BM96 Information**


For Reference Only

**Part Number** [CD4027BM96](#)  
**Manufacturer** Texas Instruments  
**Category** Integrated Circuits (ICs)  
[Logic - Flip Flops](#)  
**Description** IC JK TYPE POS TRG DUAL 16SOIC  
**Package** 16-SOIC (0.154", 3.90mm Width)  
 For the pricing/inventory/lead time, please contact us  
 Website: <https://www.heisener.com>  
 E-mail: [salesdept@heisener.com](mailto:salesdept@heisener.com)


[Request a Quote](#)
**Certified Quality**

Heisener's commitment to quality has shaped our processes for sourcing, testing, shipping, and every step in between. This foundation underlies each component we sell.


**CD4027BM96 Specifications**

Manufacturer Part Number	<a href="#">CD4027BM96</a>
Manufacturer	Texas Instruments
Category	Integrated Circuits (ICs) <a href="#">Logic - Flip Flops</a>
Package	16-SOIC (0.154", 3.90mm Width)
Series	4000B
Function	Set(Preset) and Reset
Type	JK Type
Output Type	Differential
Number of Elements	2
Number of Bits per Element	1
Clock Frequency	24MHz
Max Propagation Delay @ V, Max CL	90ns @ 15V, 50pF
Trigger Type	Positive Edge
Current - Output High, Low	6.8mA, 6.8mA
Voltage - Supply	3 V ~ 18 V
Current - Quiescent (Iq)	4µA
Input Capacitance	5pF
Operating Temperature	-55°C ~ 125°C (TA)
Mounting Type	Surface Mount
Package / Case	16-SOIC (0.154", 3.90mm Width)

[Report errors?](#)

## CD4027BM96 Guarantees



### Quality Guarantees

We provide 90 days warranty. \*

If the items you received were not in perfect quality, we would be responsible for your refund or replacement, but the items must be returned in their original condition.



### Service Guarantees

We guarantee 100% customer satisfaction.

Our experienced sales team and tech support team back our services to satisfy all our customers.

## CD4027BM96 Payment Methods



## CD4027BM96 Shipping Methods



If you have any question about CD4027BM96, please do not hesitate to contact us!

Website: <https://www.heisener.com>

E-mail: [salesdept@heisener.com](mailto:salesdept@heisener.com)