



# **CDCE949QPWRQ1 Information**



For Reference Only

Part Number CDCE949QPWRQ1

Manufacturer Texas Instruments

Category Integrated Circuits (ICs)

Clock/Timing - Clock Generators, PLLs,

Frequency Synthesizers

**Description**IC 4-PLL VCXO LVCMOS 24TSSOP**Package**24-TSSOP (0.173", 4.40mm Width)

For the pricing/inventory/lead time, please contact

us

Website: https://www.heisener.com E-mail: salesdept@heisener.com



Request a Quote

### **Certified Quality**

Heisener's commitment to quality has shaped our processes for sourcing, testing, shipping, and every step in between. This foundation underlies each component we sell.









### **CDCE949QPWRQ1 Specifications**

Manufacturer Part Number	CDCE949QPWRQ1	
Manufacturer	Texas Instruments	
Category	Integrated Circuits (ICs)	
	Clock/Timing - Clock Generators, PLLs, Frequency Synthesizers	
Package	24-TSSOP (0.173", 4.40mm Width)	
Series	Automotive, AEC-Q100	
Type	Clock Synthesizer	
PLL	Yes with Bypass	
Input	LVCMOS, Crystal	
Output	LVCMOS	
Number of Circuits	1	
Ratio - Input:Output	1:9	
Differential - Input:Output	No/No	
Frequency - Max	230MHz	
Divider/Multiplier	Yes/No	
Voltage - Supply	1.7 V ~ 1.9 V	
Operating Temperature	-40°C ~ 125°C	
Mounting Type	Surface Mount	
Package / Case	24-TSSOP (0.173", 4.40mm Width)	
Supplier Device Package	24-TSSOP	
	Report err	ors?

## **CDCE949QPWRQ1 Guarantees**



#### **Quality Guarantees**

We provide 90 days warranty. \*

If the items you received were not in perfect quality, we would be responsible for your refund or replacement, but the items must be returned in their original condition.



#### **Service Guarantees**

We guarantee 100% customer satisfaction.

Our experienced sales team and tech support team back our services to satisfy all our customers.

### **CDCE949QPWRQ1 Payment Methods**



















# **CDCE949QPWRQ1 Shipping Methods**













If you have any question about CDCE949QPWRQ1, please do not hesitate to contact us!

Website: https://www.heisener.com E-mail: salesdept@heisener.com