



### **DM74ALS874BWM Information**



For Reference Only

Part Number DM74ALS874BWM

Manufacturer Fairchild/ON Semiconductor
Category Integrated Circuits (ICs)

Logic - Flip Flops

**Description** IC D-TYPE POS TRG DUAL 24SOIC **Package** 24-SOIC (0.295", 7.50mm Width)

For the pricing/inventory/lead time, please contact

us

Website: https://www.heisener.com E-mail: salesdept@heisener.com



Request a Quote

## **Certified Quality**

Heisener's commitment to quality has shaped our processes for sourcing, testing, shipping, and every step in between. This foundation underlies each component we sell.









# **DM74ALS874BWM Specifications**

Manufacturer Part Number	DM74ALS874BWM
Manufacturer	Fairchild/ON Semiconductor
Category	Integrated Circuits (ICs)
	Logic - Flip Flops
Package	24-SOIC (0.295", 7.50mm Width)
Series	74ALS
Function	Master Reset
Type	D-Type
Output Type	Tri-State, Non-Inverted
Number of Elements	2
Number of Bits per Element	4
Clock Frequency	30MHz
Max Propagation Delay @ V, Max CL	14ns @ 5V, 50pF
Trigger Type	Positive Edge
Current - Output High, Low	2.6mA, 24mA
Voltage - Supply	4.5 V ~ 5.5 V
Current - Quiescent (Iq)	21mA
Input Capacitance	-
Operating Temperature	$0^{\circ}\text{C} \sim 70^{\circ}\text{C} \text{ (TA)}$
Mounting Type	Surface Mount
Package / Case	24-SOIC (0.295", 7.50mm Width)
	Report errors?

### **DM74ALS874BWM Guarantees**



### **Quality Guarantees**

We provide 90 days warranty. \*

If the items you received were not in perfect quality, we would be responsible for your refund or replacement, but the items must be returned in their original condition.



#### **Service Guarantees**

We guarantee 100% customer satisfaction.

Our experienced sales team and tech support team back our services to satisfy all our customers.

### **DM74ALS874BWM Payment Methods**



















# DM74ALS874BWM Shipping Methods













If you have any question about DM74ALS874BWM, please do not hesitate to contact us!

Website: https://www.heisener.com E-mail: salesdept@heisener.com