

**DS1089LU-866+ Information**


For Reference Only

**Part Number** DS1089LU-866+  
**Manufacturer** Maxim Integrated  
**Category** Integrated Circuits (ICs)  
 Clock/Timing - Clock Generators, PLLs,  
 Frequency Synthesizers  
**Description** IC ECONOSCILLATOR SS 8-USOP  
**Package** 8-TSSOP, 8-MSOP (0.118", 3.00mm Width)  
 For the pricing/inventory/lead time, please contact  
 us  
 Website: <https://www.heisener.com>  
 E-mail: [salesdept@heisener.com](mailto:salesdept@heisener.com)



Request a Quote

**Certified Quality**

Heisener's commitment to quality has shaped our processes for sourcing, testing, shipping, and every step in between. This foundation underlies each component we sell.


**DS1089LU-866+ Specifications**

Manufacturer Part Number	DS1089LU-866+
Manufacturer	Maxim Integrated
Category	Integrated Circuits (ICs) Clock/Timing - Clock Generators, PLLs, Frequency Synthesizers
Package	8-TSSOP, 8-MSOP (0.118", 3.00mm Width)
Series	EconOscillator?
Type	Clock Generator
PLL	No
Input	Clock
Output	Clock
Number of Circuits	1
Ratio - Input:Output	1:1
Differential - Input:Output	No/No
Frequency - Max	66.6MHz
Divider/Multiplier	Yes/No
Voltage - Supply	2.7 V ~ 3.6 V
Operating Temperature	-40°C ~ 85°C
Mounting Type	Surface Mount
Package / Case	8-TSSOP, 8-MSOP (0.118", 3.00mm Width)
Supplier Device Package	8-uMAX

[Report errors?](#)

## DS1089LU-866+ Guarantees



### Quality Guarantees

We provide 90 days warranty. \*

If the items you received were not in perfect quality, we would be responsible for your refund or replacement, but the items must be returned in their original condition.



### Service Guarantees

We guarantee 100% customer satisfaction.

Our experienced sales team and tech support team back our services to satisfy all our customers.

## DS1089LU-866+ Payment Methods



## DS1089LU-866+ Shipping Methods



If you have any question about DS1089LU-866+, please do not hesitate to contact us!

Website: <https://www.heisener.com>

E-mail: [salesdept@heisener.com](mailto:salesdept@heisener.com)