

# ISL80103IR18Z-T

#### **ISL80103IR18Z-T Information**

to the second second	 ISL80103IR18Z-T Renesas Electronics America Integrated Circuits (ICs) PMIC - Voltage Regulators - Linear IC REG LINEAR 1.8V 3A 10DFN 10-VFDFN Exposed Pad For the pricing/inventory/lead time, please contact	
For Reference Only	us Website: https://www.heisener.com E-mail: salesdept@heisener.com	Request a Quote

# **Certified Quality**

Heisener's commitment to quality has shaped our processes for sourcing, testing, shipping, and every step in between. This foundation underlies each component we sell.



## ISL80103IR18Z-T Specifications

Manufacturer Part Number	ISL80103IR18Z-T
Manufacturer	Renesas Electronics America
Category	Integrated Circuits (ICs)
	PMIC - Voltage Regulators - Linear
Package	10-VFDFN Exposed Pad
Series	-
Output Configuration	Positive
Output Type	Fixed
Number of Regulators	1
Voltage - Input (Max)	6V
Voltage - Output (Min/Fixed)	1.8V
Voltage - Output (Max)	-
Voltage Dropout (Max)	-
Current - Output	3A
Current - Quiescent (Iq)	9mA
Current - Supply (Max)	12mA
PSRR	62dB ~ 55dB (120Hz ~ 1kHz)
Control Features	Enable, Power Good, Soft Start
Protection Features	Over Temperature, Short Circuit
Operating Temperature	-40°C ~ 125°C
Mounting Type	Surface Mount
Package / Case	10-VFDFN Exposed Pad
Supplier Device Package	10-DFN (3x3)
	Report errors?

#### ISL80103IR18Z-T Guarantees



Quality Guarantees

We provide 90 days warranty. \* If the items you received were not in perfect quality, we would be responsible for your refund or replacement, but the items must be returned in their original condition.

# SERVICE BUARANTEE

#### **Service Guarantees**

We guarantee 100% customer satisfaction. Our experienced sales team and tech support team back our services to satisfy all our customers.

DISCOVER

#### ISL80103IR18Z-T Payment Methods



### ISL80103IR18Z-T Shipping Methods



If you have any question about ISL80103IR18Z-T, please do not hesitate to contact us! Website: https://www.heisener.com E-mail: salesdept@heisener.com