

LTC2156CUP-12#PBF Information


For Reference Only

Part Number [LTC2156CUP-12#PBF](#)
Manufacturer Linear Technology
Category Integrated Circuits (ICs)
[Data Acquisition - Analog to Digital Converters \(ADC\)](#)
Description IC ADC DUAL 12BIT 210M 64-QFN
Package 64-WFQFN Exposed Pad
 For the pricing/inventory/lead time, please contact us
 Website: <https://www.heisener.com>
 E-mail: salesdept@heisener.com


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Certified Quality

Heisener's commitment to quality has shaped our processes for sourcing, testing, shipping, and every step in between. This foundation underlies each component we sell.


LTC2156CUP-12#PBF Specifications

Manufacturer Part Number	LTC2156CUP-12#PBF
Manufacturer	Linear Technology
Category	Integrated Circuits (ICs) Data Acquisition - Analog to Digital Converters (ADC)
Package	64-WFQFN Exposed Pad
Series	-
Number of Bits	12
Sampling Rate (Per Second)	210M
Number of Inputs	2
Input Type	Differential
Data Interface	LVDS - Parallel
Configuration	S/H-ADC
Ratio - S/H:ADC	1:1
Number of A/D Converters	2
Architecture	Pipelined
Reference Type	External, Internal
Voltage - Supply, Analog	1.7 V ~ 1.9 V
Voltage - Supply, Digital	1.7 V ~ 1.9 V
Features	Simultaneous Sampling
Operating Temperature	0°C ~ 70°C
Package / Case	64-WFQFN Exposed Pad
Supplier Device Package	64-QFN (9x9)
Mounting Type	-

[Report errors?](#)

LTC2156CUP-12#PBF Guarantees



Quality Guarantees

We provide 90 days warranty. *

If the items you received were not in perfect quality, we would be responsible for your refund or replacement, but the items must be returned in their original condition.



Service Guarantees

We guarantee 100% customer satisfaction.

Our experienced sales team and tech support team back our services to satisfy all our customers.

LTC2156CUP-12#PBF Payment Methods



LTC2156CUP-12#PBF Shipping Methods



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