

# **M74HC592TTR**

a Quote

### M74HC592TTR Information

Part Number	M74HC592TTR	
Manufacturer	STMicroelectronics	
Category	Integrated Circuits (ICs) Logic - Counters, Dividers	1.50
Description	IC COUNTER BIN 8BIT REG 16-TSSOP	- Tour
Package	16-TSSOP (0.173", 4.40mm Width)	
	For the pricing/inventory/lead time, please contact us	
	Website: https://www.heisener.com E-mail: salesdept@heisener.com	Request
	Manufacturer Category Description	Logic - Counters, DividersDescriptionIC COUNTER BIN 8BIT REG 16-TSSOPPackage16-TSSOP (0.173", 4.40mm Width)For the pricing/inventory/lead time, please contact us Website: https://www.heisener.com

# **Certified Quality**

Heisener's commitment to quality has shaped our processes for sourcing, testing, shipping, and every step in between. This foundation underlies each component we sell.



# M74HC592TTR Specifications

Manufacturer Part Number	M74HC592TTR
Manufacturer	STMicroelectronics
Category	Integrated Circuits (ICs)
	Logic - Counters, Dividers
Package	16-TSSOP (0.173", 4.40mm Width)
Series	74HC
Logic Type	Binary Counter
Direction	Up
Number of Elements	1
Number of Bits per Element	8
Reset	-
Timing	-
Count Rate	53MHz
Trigger Type	Positive Edge
Voltage - Supply	2 V ~ 6 V
Operating Temperature	-55°C ~ 125°C
Mounting Type	Surface Mount
Package / Case	16-TSSOP (0.173", 4.40mm Width)
Supplier Device Package	16-TSSOP
	Report errors?

#### M74HC592TTR Guarantees



Quality Guarantees

We provide 90 days warranty. \* If the items you received were not in perfect quality, we would be responsible for your refund or replacement, but the items must be returned in their original condition.

# SERVICE EUARANTEE

#### **Service Guarantees**

We guarantee 100% customer satisfaction. Our experienced sales team and tech support team back our services to satisfy all our customers.

#### M74HC592TTR Payment Methods



## M74HC592TTR Shipping Methods



If you have any question about M74HC592TTR, please do not hesitate to contact us! Website: https://www.heisener.com E-mail: salesdept@heisener.com