



### MCP1812BT-010/LT Information

Heisener.com

Part NumberMCP1812BT-010/LTManufacturerMicrochip TechnologyCategoryIntegrated Circuits (ICs)

PMIC - Voltage Regulators - Linear

**Description**ULTRA LOW IQ LDO**Package**5-TSSOP, SC-70-5, SOT-353

For the pricing/inventory/lead time, please contact

us

For Reference Only

Website: https://www.heisener.com
E-mail: salesdept@heisener.com



Request a Quote

# **Certified Quality**

Heisener's commitment to quality has shaped our processes for sourcing, testing, shipping, and every step in between. This foundation underlies each component we sell.









## MCP1812BT-010/LT Specifications

Manufacturer Part Number	MCP1812BT-010/LT
Manufacturer	Microchip Technology
Category	Integrated Circuits (ICs)
	PMIC - Voltage Regulators - Linear
Package	5-TSSOP, SC-70-5, SOT-353
Series	-
Output Configuration	Positive
Output Type	Fixed
Number of Regulators	1
Voltage - Input (Max)	5.5V
Voltage - Output (Min/Fixed)	1V
Voltage - Output (Max)	-
Voltage Dropout (Max)	0.6V @ 300mA
Current - Output	300mA
Current - Quiescent (Iq)	0.5μΑ
Current - Supply (Max)	220μΑ
PSRR	50dB (1kHz)
Control Features	Enable
Protection Features	Over Current
Operating Temperature	-40°C ~ 85°C (TJ)
Mounting Type	Surface Mount
Package / Case	5-TSSOP, SC-70-5, SOT-353
Supplier Device Package	SC-70-5
	Report errors?

#### MCP1812BT-010/LT Guarantees



#### **Quality Guarantees**

We provide 90 days warranty. \*

If the items you received were not in perfect quality, we would be responsible for your refund or replacement, but the items must be returned in their original condition.



#### **Service Guarantees**

We guarantee 100% customer satisfaction.

Our experienced sales team and tech support team back our services to satisfy all our customers.

### MCP1812BT-010/LT Payment Methods



















### MCP1812BT-010/LT Shipping Methods













If you have any question about MCP1812BT-010/LT, please do not hesitate to contact us!

Website: https://www.heisener.com E-mail: salesdept@heisener.com