

MIC5281-5.0YMM-TR

MIC5281-5.0YMM-TR Information

- FR	Part Number	MIC5281-5.0YMM-TR
	Manufacturer	Microchip Technology
www.ballonencem		Integrated Circuits (ICs) PMIC - Voltage Regulators - Linear
Per De	Description	IC REG LINEAR 5V 25MA 8MSOP
200	Package	8-TSSOP, 8-MSOP (0.118", 3.00mm Width)
-		For the pricing/inventory/lead time, please contact
For Reference Only		us Website: https://www.heisener.com E-mail: salesdept@heisener.com



Request a Quote

Certified Quality

Heisener's commitment to quality has shaped our processes for sourcing, testing, shipping, and every step in between. This foundation underlies each component we sell.



MIC5281-5.0YMM-TR Specifications

Manufacturer Part Number	MIC5281-5.0YMM-TR	
Manufacturer	Microchip Technology	
Category	Integrated Circuits (ICs)	
	PMIC - Voltage Regulators - Linear	
Package	8-TSSOP, 8-MSOP (0.118", 3.00mm Width)	
Series	-	
Output Configuration	Positive	
Output Type	Fixed	
Number of Regulators	1	
Voltage - Input (Max)	120V	
Voltage - Output (Min/Fixed)	5V	
Voltage - Output (Max)	-	
Voltage Dropout (Max)	3V @ 25mA	
Current - Output	25mA	
Current - Quiescent (Iq)	-	
Current - Supply (Max)	11µA	
PSRR	90dB (20kHz ~ 2MHz)	
Control Features	-	
Protection Features	Over Current, Over Temperature	
Operating Temperature	-40°C ~ 125°C	
Mounting Type	Surface Mount	
Package / Case	8-TSSOP, 8-MSOP (0.118", 3.00mm Width)	
Supplier Device Package	8-MSOP	
	Report errors	

MIC5281-5.0YMM-TR Guarantees



Quality Guarantees

We provide 90 days warranty. * If the items you received were not in perfect quality, we would be responsible for your refund or replacement, but the items must be returned in their original condition.



Service Guarantees

We guarantee 100% customer satisfaction. Our experienced sales team and tech support team back our services to satisfy all our customers.

MIC5281-5.0YMM-TR Payment Methods





If you have any question about MIC5281-5.0YMM-TR, please do not hesitate to contact us! Website: https://www.heisener.com E-mail: salesdept@heisener.com