

SI5345A-D06748-GM

SI5345A-D06748-GM Information

| Heisener.com | Part Number Manufacturer Category Description | SI5345A-D06748-GM Silicon Labs Integrated Circuits (ICs) Clock/Timing - Clock Generators, PLLs, Frequency Synthesizers IC CLOCK MULTIPLIER ATTENUATOR | |
|--------------------|--|--|-----------------|
| | Package | - | 前部接票 |
| For Reference Only | | For the pricing/inventory/lead time, please contact us Website: https://www.heisener.com E-mail: salesdept@heisener.com | Request a Quote |

Certified Quality

Heisener's commitment to quality has shaped our processes for sourcing, testing, shipping, and every step in between. This foundation underlies each component we sell.



SI5345A-D06748-GM Specifications

| Manufacturer Part Number | SI5345A-D06748-GM | | |
|-----------------------------|---|--|--|
| Manufacturer | Silicon Labs | | |
| Category | Integrated Circuits (ICs) | | |
| | Clock/Timing - Clock Generators, PLLs, Frequency Synthesizers | | |
| Package | _ | | |
| Series | * | | |
| Туре | _ | | |
| PLL | - | | |
| Input | - | | |
| Output | | | |
| Number of Circuits | _ | | |
| Ratio - Input:Output | - | | |
| Differential - Input:Output | - | | |
| Frequency - Max | - | | |
| Divider/Multiplier | - | | |
| Voltage - Supply | - | | |
| Operating Temperature | - | | |
| Mounting Type | _ | | |
| Package / Case | | | |
| Supplier Device Package | - | | |
| | Report errors? | | |

SI5345A-D06748-GM Guarantees



Quality Guarantees

We provide 90 days warranty. * If the items you received were not in perfect quality, we would be responsible for your refund or replacement, but the items must be returned in their original condition.

SERVICE BUARANTEE

Service Guarantees

We guarantee 100% customer satisfaction. Our experienced sales team and tech support team back our services to satisfy all our customers.

SI5345A-D06748-GM Payment Methods



If you have any question about SI5345A-D06748-GM, please do not hesitate to contact us! Website: https://www.heisener.com E-mail: salesdept@heisener.com