

XD9261A3ADER-Q

t a Quote

XD9261A3ADER-Q Information

Contract territor com	Part Number	XD9261A3ADER-Q	
	Manufacturer	Torex Semiconductor Ltd	
	Category	Integrated Circuits (ICs) PMIC - Voltage Regulators - DC DC Switching Regulators	
	Description	HISAT-COT CONTROL 1.5A SYNCHRONO	- 9785
	Package	6-UFDFN Exposed Pad	
For Reference Only		For the pricing/inventory/lead time, please contact us Website: https://www.heisener.com	Dequest
		E-mail: salesdept@heisener.com	Request

Certified Quality

Heisener's commitment to quality has shaped our processes for sourcing, testing, shipping, and every step in between. This foundation underlies each component we sell.



XD9261A3ADER-Q Specifications

	VD02C1424DED 0	
Manufacturer Part Number	XD9261A3ADER-Q	
Manufacturer	Torex Semiconductor Ltd	
Category	Integrated Circuits (ICs)	
	PMIC - Voltage Regulators - DC DC Switching Regulators	
Package	6-UFDFN Exposed Pad	
Series	XD9261	
Function	Step-Down	
Output Configuration	Positive	
Topology	Buck	
Output Type	Fixed	
Number of Outputs	1	
Voltage - Input (Min)	2.7V	
Voltage - Input (Max)	5.5V	
Voltage - Output (Min/Fixed)	3.05V	
Voltage - Output (Max)	-	
Current - Output	1.5A	
Frequency - Switching	3MHz	
Synchronous Rectifier	Yes	
Operating Temperature	-40°C ~ 105°C (TA)	
Mounting Type	Surface Mount	
Package / Case	6-UFDFN Exposed Pad	
Supplier Device Package	6-USPC (1.8x2)	
	Report errors?	

XD9261A3ADER-Q Guarantees



Quality Guarantees

We provide 90 days warranty. * If the items you received were not in perfect quality, we would be responsible for your refund or replacement, but the items must be returned in their original condition.

SERVICE ELARANTEE

Service Guarantees

We guarantee 100% customer satisfaction. Our experienced sales team and tech support team back our services to satisfy all our customers.

XD9261A3ADER-Q Payment Methods



XD9261A3ADER-Q Shipping Methods



If you have any question about XD9261A3ADER-Q, please do not hesitate to contact us! Website: https://www.heisener.com E-mail: salesdept@heisener.com