# Fast Avalanche Sinterglass Diode 



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Models
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## MECHANICAL DATA

Case: SOD-64
Terminals: plated axial leads, solderable per MIL-STD-750, method 2026

Polarity: color band denotes cathode end
Mounting position: any
Weight: approx. 858 mg

## FEATURES

- Glass passivated junction
- Hermetically sealed package
- Low reverse current
- Soft recovery characteristics
- Low forward voltage drop
- High pulse current capability
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912


## APPLICATIONS

- Fast rectification diode in SMPS

| ORDERING INFORMATION (Example) |  |  |  |
| :--- | :---: | :---: | :---: |
| DEVICE NAME | ORDERING CODE | TAPED UNITS | MINIMUM ORDER QUANTITY |
| BYW172G | BYW172G-TR | 2500 per 10" tape and reel | 12500 |
| BYW172G | BYW172G-TAP | 2500 per ammopack | 12500 |


| PARTS TABLE |  |  |  |
| :--- | :---: | :---: | :---: |
| PART | TYPE DIFFERENTIATION | PACKAGE |  |
| BYW172D | $\mathrm{V}_{\mathrm{R}}=200 \mathrm{~V} ; \mathrm{I}_{\mathrm{F}(\mathrm{AV})}=3 \mathrm{~A}$ | SOD-64 |  |
| BYW172F | $\mathrm{V}_{\mathrm{R}}=300 \mathrm{~V} ; \mathrm{I}_{\mathrm{F}(\mathrm{AV}}=3 \mathrm{~A}$ | SOD-64 |  |
| BYW172G | $\mathrm{V}_{\mathrm{R}}=400 \mathrm{~V} ; \mathrm{I}_{\mathrm{F}(\mathrm{AV})}=3 \mathrm{~A}$ | SOD-64 |  |


| ABSOLUTE MAXIMUM RATINGS ( $\mathrm{T}_{\mathrm{amb}}=25^{\circ} \mathrm{C}$, unless otherwise specified) |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: |
| PARAMETER | TEST CONDITION | PART | SYMBOL | VALUE | UNIT |
| Reverse voltage $=$ repetitive peak reverse voltage | See electrical characteristics | BYW172D | $\mathrm{V}_{\mathrm{R}}=\mathrm{V}_{\text {RRM }}$ | 200 | V |
|  |  | BYW172F | $\mathrm{V}_{\mathrm{R}}=\mathrm{V}_{\text {RRM }}$ | 300 | V |
|  |  | BYW172G | $\mathrm{V}_{\mathrm{R}}=\mathrm{V}_{\text {RRM }}$ | 400 | V |
| Peak forward surge current | $\mathrm{t}_{\mathrm{p}}=10 \mathrm{~ms}$, half sine wave |  | $\mathrm{I}_{\text {FSM }}$ | 100 | A |
| Average forward current |  |  | $\mathrm{I}_{\text {(AV) }}$ | 3 | A |
| Non repetitive reverse avalanche energy | $\mathrm{l}_{(\mathrm{BR}) \mathrm{R}}=1 \mathrm{~A}$ |  | $\mathrm{E}_{\mathrm{R}}$ | 20 | mJ |
| Junction and storage temperature range |  |  | $\mathrm{T}_{\mathrm{j}}=\mathrm{T}_{\text {stg }}$ | -55 to +175 | ${ }^{\circ} \mathrm{C}$ |

MAXIMUM THERMAL RESISTANCE ( $\mathrm{T}_{\mathrm{amb}}=25^{\circ} \mathrm{C}$, unless otherwise specified)

| PARAMETER | TEST CONDITION | SYMBOL | VALUE | UNIT |
| :--- | :---: | :---: | :---: | :---: |
| Junction ambient | Lead length $\mathrm{I}=10 \mathrm{~mm}, \mathrm{~T}_{\mathrm{L}}=$ constant | $\mathrm{R}_{\mathrm{thJA}}$ | 25 | $\mathrm{~K} / \mathrm{W}$ |
|  | On PC board with spacing 25 mm | $\mathrm{R}_{\mathrm{thJA}}$ | 70 | $\mathrm{~K} / \mathrm{W}$ |

ELECTRICAL CHARACTERISTICS $\left(T_{\mathrm{amb}}=25^{\circ} \mathrm{C}\right.$, unless otherwise specified)

| PARAMETER | TEST CONDITION | PART | SYMBOL | MIN. | TYP. | MAX. | UNIT |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Forward voltage | $\mathrm{I}_{\mathrm{F}}=3 \mathrm{~A}$ |  | $V_{F}$ | - | - | 1.1 | V |
|  | $\mathrm{I}_{\mathrm{F}}=9 \mathrm{~A}$ |  | $\mathrm{V}_{\mathrm{F}}$ | - | - | 1.5 | V |
| Reverse current | $\mathrm{V}_{\mathrm{R}}=\mathrm{V}_{\mathrm{RRM}}$ |  | $\mathrm{I}_{\mathrm{R}}$ | - | - | 1 | $\mu \mathrm{A}$ |
|  | $\mathrm{V}_{\mathrm{R}}=\mathrm{V}_{\text {RRM }}, \mathrm{T}_{\mathrm{j}}=100^{\circ} \mathrm{C}$ |  | $\mathrm{I}_{\mathrm{R}}$ | - | - | 20 | $\mu \mathrm{A}$ |
| Reverse recovery time | $\mathrm{I}_{\mathrm{F}}=0.5 \mathrm{~A}, \mathrm{I}_{\mathrm{R}}=1 \mathrm{~A}, \mathrm{i}_{\mathrm{R}}=0.25 \mathrm{~A}$ |  | $\mathrm{t}_{\mathrm{rr}}$ | - | 75 | 100 | ns |

TYPICAL CHARACTERISTICS $\left(T_{a m b}=25^{\circ} \mathrm{C}\right.$, unless otherwise specified)


Fig. 1 - Max. Thermal Resistance vs. Lead Length


Fig. 2 - Max. Forward Current vs. Forward Voltage


Fig. 3 - Max. Average Forward Current vs. Ambient Temperature


Fig. 4 - Max. Reverse Current vs. Junction Temperature

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Fig. 5 - Max. Reverse Power Dissipation vs. Junction Temperature


Fig. 6 - Diode Capacitance vs. Reverse Voltage


Fig. 7 - Thermal Response
PACKAGE DIMENSIONS in millimeters (inches): SOD-64


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