RoHS

COMPLIANT HALOGEN

FREE



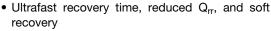
Vishay Semiconductors

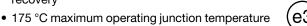
Ultrafast Rectifier, 2 x 15 A FRED Pt®

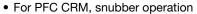


PRIMARY CHARACTERISTICS				
I _{F(AV)}	2 x 15 A			
V_{R}	600 V			
V _F at I _F	0.9 V			
t _{rr}	55 ns			
T _J max.	175 °C			
Package	SMPD (TO-263AC)			
Circuit configuration	Dual serial			

FEATURES







· Low forward voltage drop

· Low leakage current

 Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C

AEC-Q101 qualified, meets JESD 201 class 2 whisker test

 Material categorization: for definitions of compliance please see www.vishav.com/doc?99912

DESCRIPTION / APPLICATIONS

State of the art ultrafast recovery rectifiers designed with optimized performance of forward voltage drop and ultrafast recovery time, and soft recovery.

The planar structure and the platinum doped life time control guarantee the best overall performance, ruggedness, and reliability characteristics.

These devices are intended for use in PFC, boost, in the AC/DC section of SMPS, freewheeling and clamp diodes.

Their extremely optimized stored charge and low recovery current minimize the switching losses and reduce power dissipation in the switching element and snubbers.

ABSOLUTE MAXIMUM RATINGS					
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS
Peak repetitive reverse voltage		V_{RRM}		600	V
A	per device	I _{F(AV)}	T _{solder pad} = 143 °C	30	
Average rectified forward current	per diode			15	۸
Non vanatitiva naak ayyaa ayyyant	per device		T _J = 25 °C, 6 ms square pulse	300	А
Non-repetitive peak surge current	per diode	IFSM		160	

ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Breakdown voltage, blocking voltage	V_{BR} , V_{R}	I _R = 100 μA	600	-	-	
Forward voltage, per diode V _F	\/	I _F = 15 A	-	1.03	1.25	V
	I _F = 15 A, T _J = 150 °C	-	0.9	1.1		
Be and below a supplied at		V _R = V _R rated	-	-	15	
Reverse leakage current, per diode I _R		T _J = 150 °C, V _R = V _R rated	-	70	300	μΑ
Junction capacitance, per diode	C _T	V _R = 600 V	-	13	-	pF



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DYNAMIC RECOVERY CHARACTERISTICS (T _J = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
		$I_F = 1 A, dI_F/dt = 50 A$	= 50 A/µs, V _R = 30 V		55	1	
Reverse recovery time	t _{rr}	I _F = 0.5 A, I _R = 1 A, I _{rr} = 0.25 A		-	-	65	
Reverse recovery time		T _J = 25 °C	I _F = 15 A, dI _F /dt = 500 A/μs, V _R = 400 V	-	96	-	ns
		T _J = 125 °C		-	150	-	
Dook receives augreent	1	T _J = 25 °C		-	18	-	Α
Peak recovery current I _{RRI}	I _{RRM}	T _J = 125 °C		-	26	-	_ ^
Poverse receivent charge	Q _{rr}	T _J = 25 °C		-	1.0	1	μC
Reverse recovery charge		T _J = 125 °C		-	2.0	-	μΟ

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Maximum junction and storage temperature range	T _J , T _{Stg}		-55	-	+175	°C
Thermal resistance, per diode junction to solder pad	R _{thJ-Sp}		=	1.2	1.7	°C/W
Approximate weight				0.55		g
Approximate weight				0.02		oz.
Marking device		Case style SMPD (TO-263AC)		30C	DU06	-

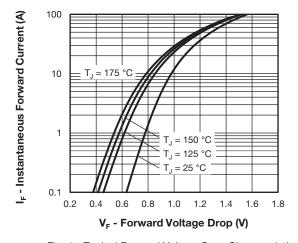


Fig. 1 - Typical Forward Voltage Drop Characteristics

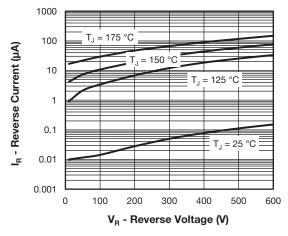


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage



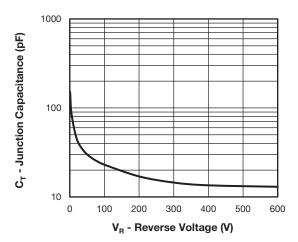


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

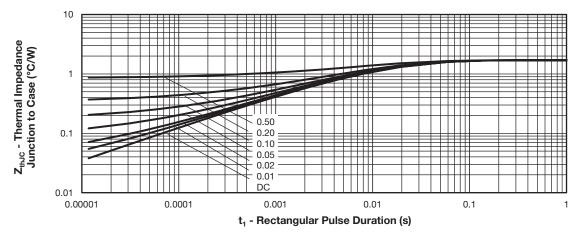


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

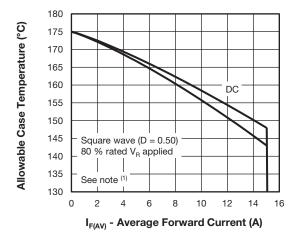


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

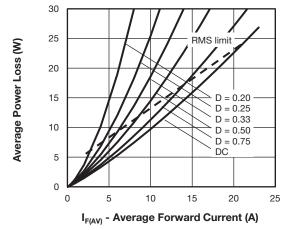


Fig. 6 - Forward Power Loss Characteristics

Note

⁽¹⁾ Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$; Pd = forward power loss = $I_{F(AV)} \times V_{FM}$ at $(I_{F(AV)}/D)$ (see fig. 5); Pd_{REV} = inverse power loss = $V_{R1} \times I_{R} (1 - D)$; I_{R} at V_{R1} = rated V_{R1}

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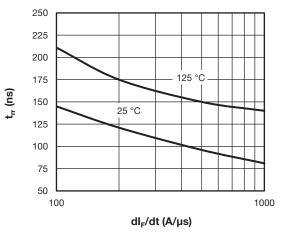


Fig. 7 - Typical Reverse Recovery Time vs. dl_F/dt

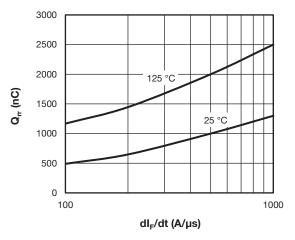
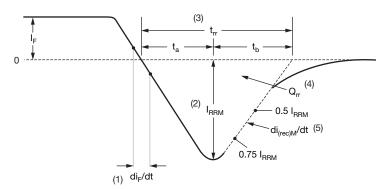


Fig. 8 - Typical Stored Charge vs. dl_F/dt



- (1) di_F/dt rate of change of current through zero crossing
- (2) I_{RRM} peak reverse recovery current
- (3) $\rm t_{rr}$ reverse recovery time measured from zero crossing point of negative going $\rm l_{r}$ to point where a line passing through 0.75 $\rm l_{RRM}$ and 0.50 $\rm l_{RRM}$ extrapolated to zero current.
- (4) $\mathbf{Q}_{\rm rr}$ area under curve defined by $\mathbf{t}_{\rm rr}$ and $\mathbf{I}_{\rm RRM}$

$$Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$$

(5) di_{(rec)M}/dt - peak rate of change of current during t_b portion of t_{rr}

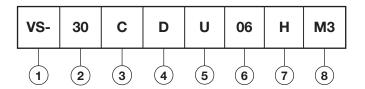
Fig. 9 - Reverse Recovery Waveform and Definitions



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ORDERING INFORMATION TABLE

Device code



1 - Vishay Semiconductors product

Current rating (30 A)

3 - Circuit configuration:

C = common cathode

4 - D = SMPD package

- Process type,

U = ultrafast recovery

6 - Voltage code (06 = 600 V)

7 - H = AEC-Q101 qualified

8 - M3 = halogen-free, RoHS-compliant, and terminations lead (Pb)-free

ORDERING INFORMATION (Example)					
PREFERRED P/N	QUANTITY PER REEL	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION		
VS-30CDU06HM3/I	2000	2000	13" diameter plastic tape and reel		

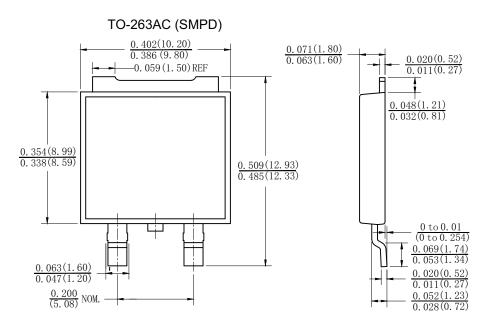
LINKS TO RELATED DOCUMENTS				
Dimensions <u>www.vishay.com/doc?95604</u>				
Part marking information	www.vishay.com/doc?95566			
Packaging information	www.vishay.com/doc?88869			
SPICE model	www.vishay.com/doc?96576			



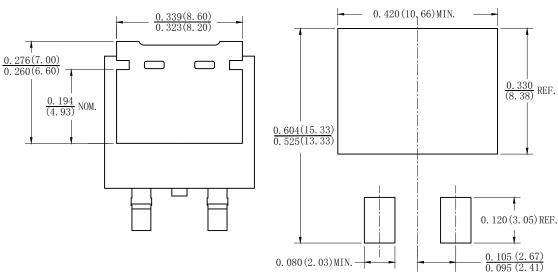
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TO-263AC (SMPD)

DIMENSIONS in inches (millimeters)



Mounting Pad Layout





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