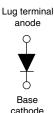
# High Performance Schottky Rectifier, 240 A



www.vishay.com



HALF-PAK (D-67)

cathode

PRIMARY CHARACTERISTICS			
I <sub>F(AV)</sub> 240 A			
V <sub>R</sub>	15 V		
Package	HALF-PAK (D-67)		
Circuit configuration	Single		

### **FEATURES**

- 125 °C T<sub>J</sub> operation
- Low forward voltage drop
- High frequency operation
- · Guard ring for enhanced ruggedness and long term reliability
- · Designed and qualified for industrial level
- UL approved file E222165
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

### DESCRIPTION

The VS-245NQ.. high current Schottky rectifier module series has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 150 °C junction temperature. Typical applications are in high current switching power supplies, plating power supplies, UPS systems, converters, freewheeling diodes, welding, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS				
SYMBOL	CHARACTERISTICS	VALUES	UNITS	
I <sub>F(AV)</sub>	Rectangular waveform	240	A	
V <sub>RRM</sub>		15	V	
I <sub>FSM</sub>	t <sub>p</sub> = 5 μs sine	20 000	A	
V <sub>F</sub>	240 A <sub>pk</sub> , T <sub>J</sub> = 75 °C	0.37	V	
TJ	Range	-55 to +125	°C	

VOLTAGE RATINGS					
PARAMETER	SYMBOL	VS-245NQ015PbF	UNITS		
Maximum DC reverse voltage	V <sub>R</sub> 15		V		
Maximum working peak reverse voltage	V <sub>RWM</sub>	25	V		

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average forward current See fig. 5	I <sub>F(AV)</sub>	50 % duty cycle at T <sub>C</sub> = 73 °C, rectangular waveform		240	
Maximum peak one cycle non-repetitive surge current See fig. 7		5 µs sine or 3 µs rect. pulse	Following any rated load condition and with rated V <sub>RRM</sub> applied	20 000	А
	IFSM	10 ms sine or 6 ms rect. pulse		3000	
Non-repetitive avalanche energy	E <sub>AS</sub>	T <sub>J</sub> = 25 °C, I <sub>AS</sub> = 5 A, L = 1 mH		12	mJ
Repetitive avalanche current	I <sub>AR</sub>	Current decaying linearly to zero in 1 $\mu$ s Frequency limited by T <sub>J</sub> maximum V <sub>A</sub> = 1.5 x V <sub>R</sub> typical		2	A

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COMPLIANT



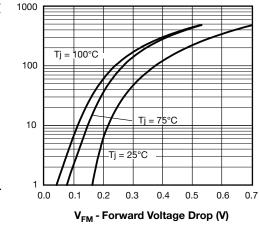
ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	L TEST CONDITIONS VALU		VALUES	UNITS
Maximum forward voltage drop See fig. 1	V <sub>FM</sub> <sup>(1)</sup>	240 A	T <sub>J</sub> = 25 °C	0.52	V
		480 A		0.61	
		240 A	- T <sub>J</sub> = 125 °C	0.37	
		480 A		045	
Maximum reverse leakage current See fig. 2	I <sub>RM</sub> (1)	T <sub>J</sub> = 25 °C	V <sub>R</sub> = Rated V <sub>R</sub>	80	mA
		T <sub>J</sub> = 125 °C		4000	
Maximum junction capacitance	CT	$V_R = 5 V_{DC}$ (test signal range 100 kHz to 1 MHz) 25 °C		15 800	pF
Typical series inductance	L <sub>S</sub>	From top of terminal hole to mounting plane		5.0	nH
Maximum voltage rate of change	dV/dt	Rated V <sub>R</sub> 10 000 V/		V/µs	

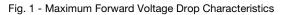
Note

<sup>(1)</sup> Pulse width < 500  $\mu$ s

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum junction and storage temperature range	)	T <sub>J</sub> , T <sub>Stg</sub>		-55 to 125	°C	
Maximum thermal resistance, junction to case		R <sub>thJC</sub>	DC operation 0.19		°C/W	
Typical thermal resistance, case to heatsink		R <sub>thCS</sub>	Mounting surface, smooth and greased	0.05	0/10	
Approximate weight				30	g	
				1.06	oz.	
minir				3 (26.5)		
Mounting torque –	maximum		Non-lubricated threads	4 (35.4)	N · m (lbf · in)	
Terminal torque	minimum			3.4 (30)		
	maximum		5			
Case style				HALF-PA	K module	







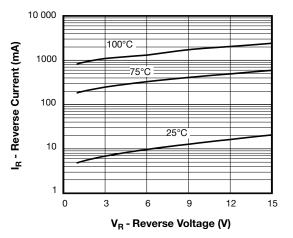


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

Revision: 08-May-17

2

Document Number: 94464

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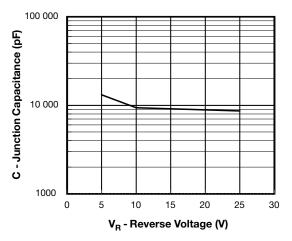
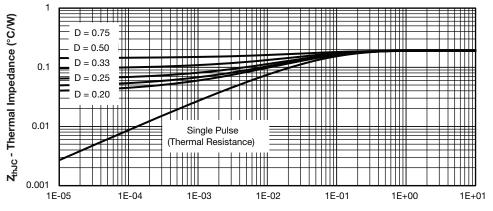
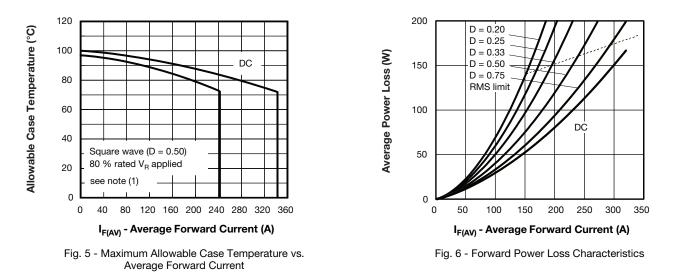


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage



t<sub>1</sub> - Rectangular Pulse Duration (s)

Fig. 4 - Maximum Thermal Impedance Z<sub>thJC</sub> Characteristics



3

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## VS-245NQ015PbF

## **Vishay Semiconductors**



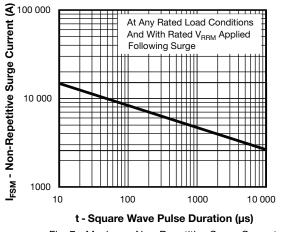


Fig. 7 - Maximum Non-Repetitive Surge Current

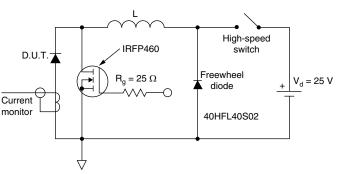


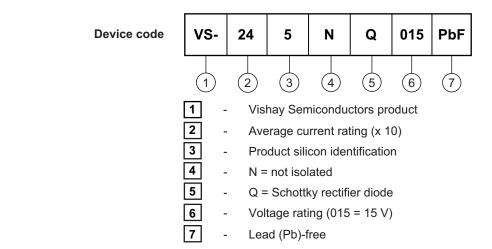
Fig. 8 - Unclamped Inductive Test Circuit

#### Note

<sup>(1)</sup> Formula used:  $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$ ;

Pd = forward power loss =  $I_{F(AV)} \times V_{FM}$  at ( $I_{F(AV)}/D$ ) (see fig. 6); Pd<sub>REV</sub> = inverse power loss =  $V_{R1} \times I_R$  (1 - D);  $I_R$  at  $V_{R1}$  = rated  $V_R$ 

## **ORDERING INFORMATION TABLE**



LINKS TO RELATED DOCUMENTS			
Dimensions www.vishay.com/doc?95020			

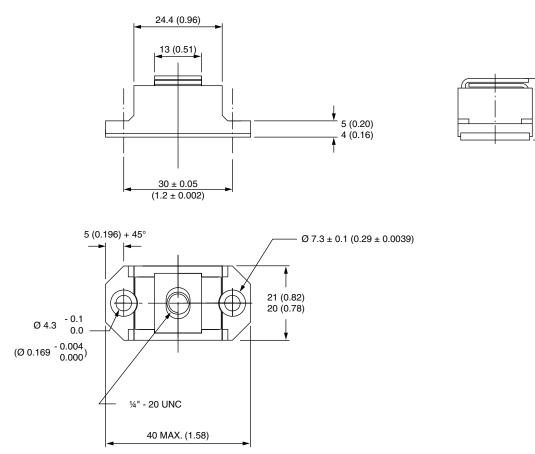
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17.5 (0.69) 16.5 (0.65)



## **DIMENSIONS** in millimeters (inches)

SHAY





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