# **VS-GB300LH120N**

**Vishay Semiconductors** 

## Molding Type Module IGBT, Chopper in 1 Package, 1200 V and 300 A



Dual	INT-A	-PAK
Duu		1

PRIMARY CHARACTERISTICS					
V <sub>CES</sub>	1200 V				
$I_C$ at $T_C$ = 80 °C	300 A				
V <sub>CE(on)</sub> (typical) at I <sub>C</sub> = 300 A, 25 °C	2.0 V				
Speed	8 kHz to 30 kHz				
Package	Double INT-A-PAK				
Circuit configuration	Low side chopper				

#### **FEATURES**

- Low V<sub>CE(on)</sub> SPT and IGBT technology
- 10 µs short circuit capability
- V<sub>CE(on)</sub> with positive temperature coefficient
- Low inductance case
- · Fast and soft reverse recovery antiparallel FWD
- Isolated copper baseplate using DCB (Direct Copper Bonding) technology
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

#### **TYPICAL APPLICATIONS**

- Inverter for motor drive
- AC and DC servo drive amplifier
- Uninterruptible power supply (UPS)

#### DESCRIPTION

Vishay's IGBT power module provides ultra low conduction loss as well as short circuit ruggedness. It is designed for applications such as general inverters and UPS.

<b>ABSOLUTE MAXIMUM RATINGS</b> (T <sub>C</sub> = 25 °C unless otherwise noted)					
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS	
Collector to emitter voltage	V <sub>CES</sub>		1200	V	
Gate to emitter voltage	V <sub>GES</sub>		± 20	v	
Collector current	1-	T <sub>C</sub> = 25 °C	500		
Collector current	IC	T <sub>C</sub> = 80 °C	300		
Pulsed collector current	I <sub>CM</sub> <sup>(1)</sup>	t <sub>p</sub> = 1 ms	600	А	
Diode continuous forward current	١ <sub>F</sub>	T <sub>C</sub> = 80 °C	300		
Diode maximum forward current	I <sub>FM</sub>	t <sub>p</sub> = 1 ms	600		
Maximum power dissipation	PD	T <sub>J</sub> = 150 °C	1645	W	
Short circuit withstand time	t <sub>SC</sub>	T <sub>J</sub> = 125 °C	10	μs	
RMS isolation voltage	V <sub>ISOL</sub>	f = 50 Hz, t = 1 min	2500	V	

#### Note

<sup>(1)</sup> Repetitive rating: pulse width limited by maximum junction temperature

<b>IGBT ELECTRICAL SPECIFICATIONS</b> ( $T_c = 25 \text{ °C}$ unless otherwise noted)						
PARAMETER	SYMBOL	SYMBOL TEST CONDITIONS MIN. TYP.		TYP.	MAX.	UNITS
Collector to emitter breakdown voltage	V <sub>(BR)CES</sub>	$T_J = 25 \ ^{\circ}C$	1200	-	-	
Collector to emitter voltage	Maria	$V_{GE}$ = 15 V, $I_C$ = 300 A, $T_J$ = 25 $^\circ C$	-	2.0	-	v
Collector to entitler voltage	V <sub>CE(on)</sub>	$V_{GE}$ = 15 V, $I_{C}$ = 300 A, $T_{J}$ = 125 °C	-	2.2	-	v
Gate to emitter threshold voltage	V <sub>GE(th)</sub>	$V_{CE}$ = $V_{GE}$ , $I_C$ = 12.0 mA, $T_J$ = 25 °C	5.0	6.2	7.0	
Collector cut-off current	I <sub>CES</sub>	$V_{CE} = V_{CES}, V_{GE} = 0 \text{ V}, \text{ T}_{J} = 25 ^{\circ}\text{C}$	-	-	5.0	mA
Gate to emitter leakage current	I <sub>GES</sub>	$V_{GE} = V_{GES}, V_{CE} = 0 \text{ V}, \text{ T}_{J} = 25 ^{\circ}\text{C}$	-	-	400	nA

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<sup>1</sup> For technical questions within your region: DiodesAmericas@vishay.com, DiodesAsia@vishay.com, DiodesEurope@vishay.com THIS DOCUMENT IS SUBJECT TO CHANGE WITHOUT NOTICE. THE PRODUCTS DESCRIBED HEREIN AND THIS DOCUMENT ARE SUBJECT TO SPECIFIC DISCLAIMERS, SET FORTH AT www.vishav.com/doc?91000





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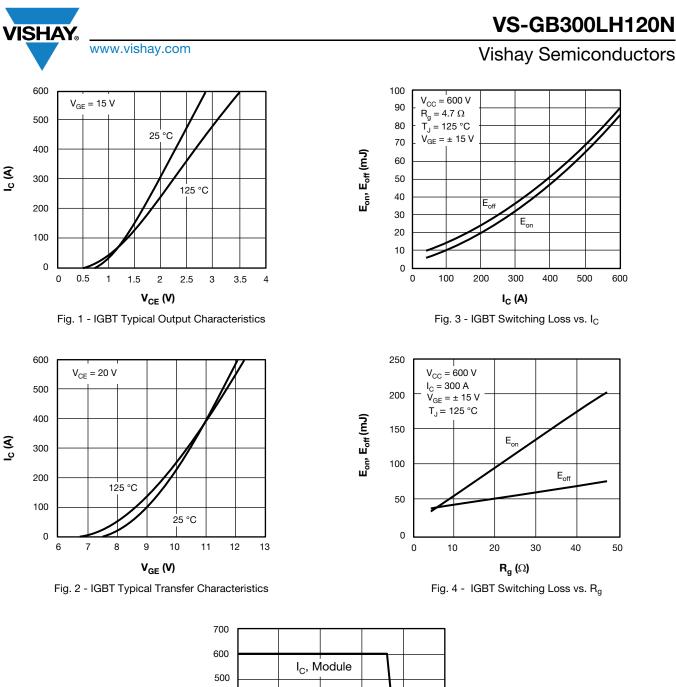
SWITCHING CHARACTERISTICS	S					
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Turn-on delay time	t <sub>d(on)</sub>		-	574	-	
Rise time	t <sub>r</sub>		-	133	-	
Turn-off delay time	t <sub>d(off)</sub>	$V_{CC} = 600 \text{ V}, \text{ I}_{C} = 300 \text{ A}, \text{ R}_{g} = 4.7 \Omega,$	-	563	-	ns
Fall time	t <sub>f</sub>	V <sub>GE</sub> = ± 15 V, T <sub>J</sub> = 25 °C	-	120	-	
Turn-on switching loss	E <sub>on</sub>		-	23.9	-	
Turn-off switching loss	E <sub>off</sub>		-	25.3	-	mJ
Turn-on delay time	t <sub>d(on)</sub>		-	604	-	
Rise time	t <sub>r</sub>		-	137	-	- ns
Turn-off delay time	t <sub>d(off)</sub>	$V_{CC} = 600 \text{ V}, \text{ I}_{C} = 300 \text{ A}, \text{ R}_{g} = 4.7 \Omega,$	-	629	-	
Fall time	t <sub>f</sub>	V <sub>GE</sub> = ± 15 V, T <sub>J</sub> = 125 °C	-	167	-	
Turn-on switching loss	E <sub>on</sub>		-	31.5	-	
Turn-off switching loss	E <sub>off</sub>		-	35.9	-	mJ
Input capacitance	Cies		-	21.2	-	
Output capacitance	C <sub>oes</sub>	$V_{GE} = 0 V, V_{CE} = 25 V, f = 1.0 MHz$	-	1.42	-	nF
Reverse transfer capacitance	C <sub>res</sub>		-	0.94	-	
SC data	I <sub>SC</sub>	$\label{eq:tsc} \begin{array}{l} t_{sc} \leq 10 \ \mu s, \ V_{GE} = 15 \ V, \ T_J = 125 \ ^{\circ}C, \\ V_{CC} = 900 \ V, \ V_{CEM} \leq 1200 \ V \end{array}$	-	1800	-	А
Internal gate resistance	Rg		-	1.0	-	Ω
Stray inductance	L <sub>CE</sub>		-	-	20	nH
Module lead resistance, terminal to chip	R <sub>CC'+EE'</sub>	T <sub>C</sub> = 25 °C	-	0.35	-	mΩ

<b>DIODE ELECTRICAL SPECIFICATIONS</b> ( $T_C = 25$ °C unless otherwise noted)											
PARAMETER	SYMBOL	TEST CONDITI	IONS	MIN.	TYP.	MAX.	UNITS				
Diado forward voltago	<b>M</b> -	$V_{\rm E}$   $I_{\rm E} = 300 \rm{A}$	T <sub>J</sub> = 25 °C	-	1.82	2.25	v				
Diode forward voltage	VF		T <sub>J</sub> = 125 °C	-	1.95	-					
Diada rayarga ragayany aharga	Q <sub>rr</sub>		T <sub>J</sub> = 25 °C	-	20.2	-	μC				
Diode reverse recovery charge		Qrr		T <sub>J</sub> = 125 °C	-	40.1	-	μΟ			
Diada paak rayaraa raaayany ayrrant	I <sub>rr</sub>	I <sub>rr</sub> dI <sub>F</sub> /dt = -		I		I <sub>F</sub> = 300 A, V <sub>R</sub> = 600 V, dI <sub>F</sub> /dt = -2360 A/μs,	T <sub>J</sub> = 25 °C	-	170	-	А
Diode peak reverse recovery current			$V_{GF} = -15 V$	T <sub>J</sub> = 125 °C	-	250	-	A			
	E <sub>rec</sub>	Erro –	VGE - 10 V	T <sub>J</sub> = 25 °C	-	8.2	-	ml			
Diode reverse recovery energy			T <sub>J</sub> = 125 °C	-	21.7	-	mJ				

THERMAL AND MECHANICAL SPECIFICATIONS							
PARAMETER		SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Operating junction temperature		TJ		-	-	150	°C
Storage temperature range		T <sub>STG</sub>		-40	-	125	C
Junction to case	IGBT	в		-	-	0.076	
Sunction to case –	Diode	R <sub>thJC</sub>		-	-	0.100	K/W
Case to sink		R <sub>thCS</sub>	Conductive grease applied	-	0.035	-	
Mounting torque			Power terminal screw: M6	2.5 to 5.0		)	Nm
		Mounting screw: M6	3.0 to 5.0		INITI		
Weight					300		g

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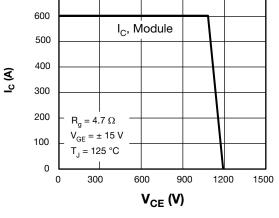
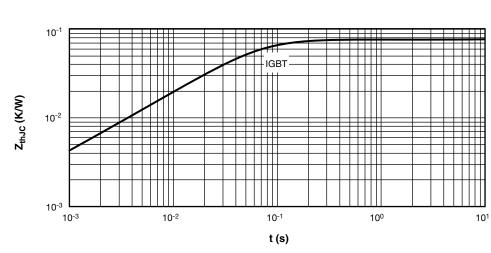


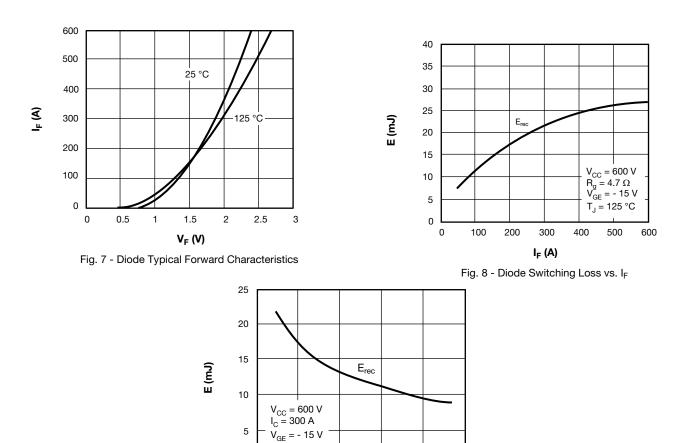
Fig. 5 - RBSOA

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Fig. 6 - IGBT Transient Thermal Impedance



T<sub>J</sub> = 125 °C

10

20

30

 $\mathbf{R}_{g}$  (Ω) Fig. 9 - Diode Switching Loss vs.  $\mathbf{R}_{q}$ 

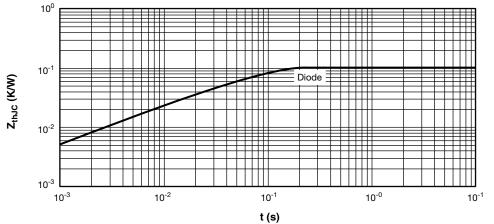
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## VS-GB300LH120N

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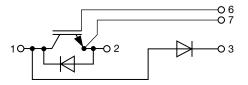




#### CIRCUIT CONFIGURATION

ISHA

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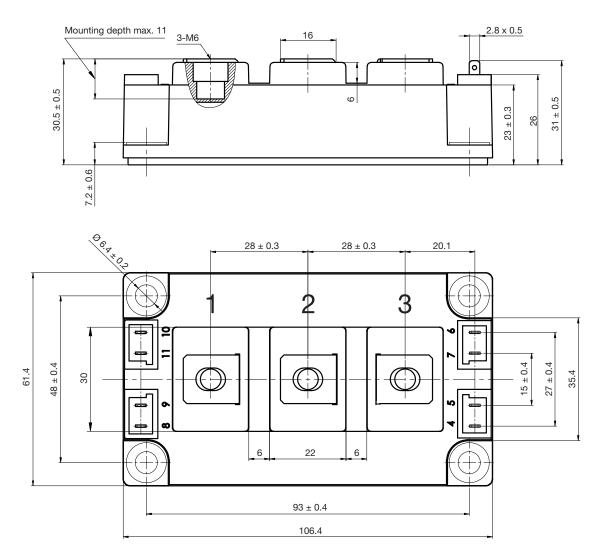
LINKS TO RELATED DOCUMENTS			
Dimensions	www.vishay.com/doc?95525		



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## Double INT-A-PAK

#### **DIMENSIONS** in millimeters (inches)





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