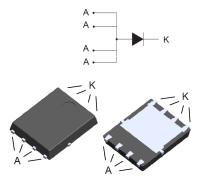


FERD15S50

Datasheet

50 V field effect rectifier



PowerFLAT™ 5x6 (non-contractual)

Features

- ST patented rectifier process
- Stable leakage current over reverse voltage
- Low forward voltage drop
- High frequency operation
- ECOPACK[®]2 compliant

Applications

- Set-top box
- Battery charger
- DC / DC converter

Description

This single rectifier is based on a patented technology, enabling to achieve the best in class $V_{\text{F}}/I_{\text{R}}$ trade-off for a given silicon surface.

Packaged in PowerFLAT[™] 5x6, the FERD15S50 is optimized for use in rectification and freewheeling operations in switch mode power supplies.

Product status			
FERD15S50			
Product summary			
Symbol Value			
I _{F(AV)}	15 A		
V _{RRM}	50 V		
T _{j(max.)}	150 °C		
V _{F(typ.)}	0.48 V		

1 Characteristics

[]

Table 1. Absolute ratings (limiting values at 25 °C, unless otherwise specified, anode terminals short circuited)

Symbol	Parameter	Value	Unit	
V _{RRM}	Repetitive peak reverse voltage	50	V	
I _{F(RMS)}	Forward rms current	25	А	
I _{F(AV)}	Average forward current, δ = 0.5 square wave T _C = 120 °C		15	Α
I _{FSM}	Surge non repetitive forward current	80	Α	
T _{stg}	Storage temperature range	-65 to +175	°C	
Тj	Maximum operating junction temperature ⁽¹⁾	+150	°C	

1. $(dP_{tot'}/dT_j) < (1/R_{th(j-a)})$ condition to avoid thermal runaway for a diode on its own heatsink.

Table 2. Thermal resistance parameter

Symbol	Parameter	Max. value	Unit
R _{th(j-c)}	Junction to case	2.8	°C/W

For more information, please refer to the following application note :

AN5088 : Rectifiers thermal management, handling and mounting recommendations

Table 3. Static electrical characteristics (anode terminals short circuited)

Symbol	Parameter	Test conditions		Min.	Тур.	Max.	Unit
L (1)	Reverse leakage current	T _j = 25 °C	V _R = V _{RRM}	-	250	650	μA
I _R ⁽¹⁾		T _j = 125 °C		-	20	40	mA
	Forward voltage drop	T _j = 25 °C	I _F = 5 A	-	0.35		
		T _j = 125 °C		-	0.30	0.35	
VF ⁽²⁾		T _j = 25 °C	10.4	-	0.42	0.48	V
		T _j = 125 °C	I _F = 10 A	-	0.41	0.45	
		T _j = 25 °C	I _F = 15 A	-	0.48		

1. Pulse test: $t_p = 5 ms$, $\delta < 2\%$

2. Pulse test: t_p = 380 µs, δ < 2%

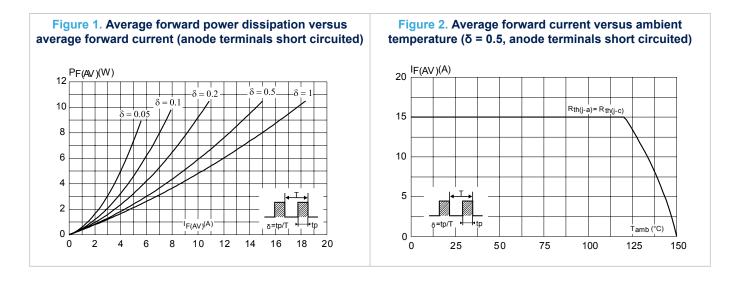
To evaluate the conduction losses, use the following equation:

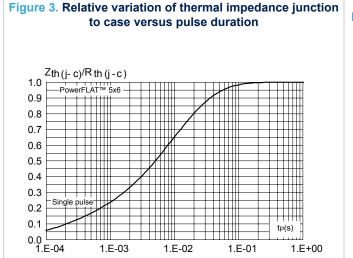
 $P = 0.25 \times I_{F(AV)} + 0.02 \times I_{F}^{2}(RMS)$

For more information, please refer to the following application notes related to the power losses :

- AN604: Calculation of conduction losses in a power rectifier
- AN4021: Calculation of reverse losses on a power diode

1.1 Characteristics (curves)





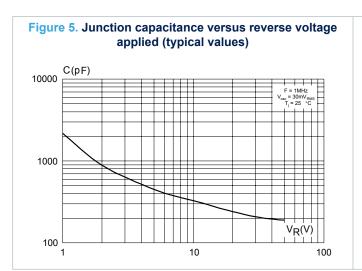


Figure 4. Reverse leakage current versus reverse voltage applied (typical values)

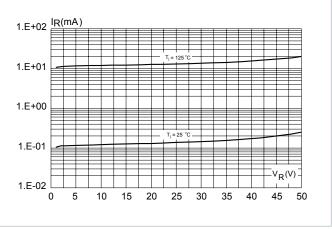
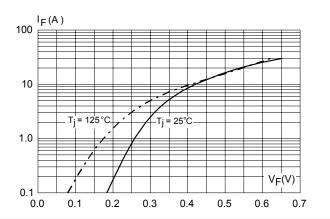


Figure 6. Forward voltage drop versus forward current (typical values, anode terminals short circuited)



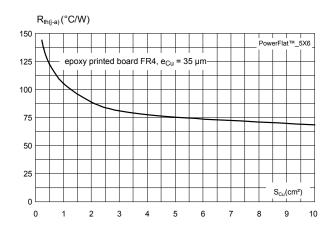


Figure 7. Thermal resistance junction to ambient versus copper surface under tab (typical values)

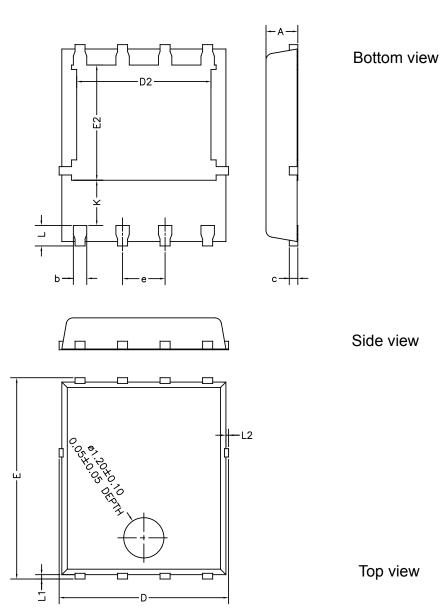
2 Package information

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK[®] packages, depending on their level of environmental compliance. ECOPACK[®] specifications, grade definitions and product status are available at: www.st.com. ECOPACK[®] is an ST trademark.

2.1 PowerFLAT[™] 5x6 package information

- Epoxy meets UL 94,V0
- Cooling method: by conduction (C)

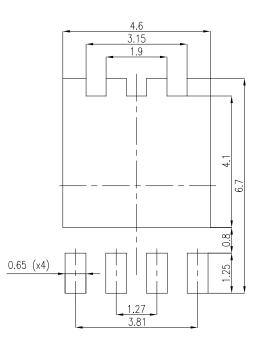




	Dimensions						
Def	Millimeters			Inches (for reference only)			
Ref	Min.	Тур.	Max.	Min.	Тур.	Max.	
A	0.80		1.00	0.031		0.039	
b	0.30		0.50	0.01		0.02	
С		0.25			0.010		
D	4.80		5.40	0.189		0.212	
D2	3.91		4.45	0.154		0.175	
е		1.27			0.050		
E	5.90		6.35	0.232		0.250	
E2	3.34		3.70	0.138		0.146	
L	0.50		0.80	0.020		0.031	
К	1.10		1.575	0.015		0.023	
L1	0.06		0.20	0.002		0.009	
L2			0.10			0.004	

Table 4. PowerFLAT™ 5x6 mechanical data

Figure 9. PowerFLAT™ 5x6 recommended footprint (dimensions are in mm)





3 Ordering information

Order code	Marking	Package	Weight	Base qty.	Delivery mode
FERD15S50DJF-TR	FD15 S50	PowerFLAT™ 5x6	95 mg	3000	Tape and reel

Table 5. Ordering information

Revision history

Table 6. Document revision history

Date	Version	Changes
09-Oct-2013	1	Initial release.
09-Nov-2018	2	Updated Section Cover image and Section Features. Updated Figure 8. PowerFLAT [™] 5x6 package outline (non-contractual) Added Section Applications, Table 4. PowerFLAT [™] 5x6 mechanical data and Figure 9. PowerFLAT [™] 5x6 recommended footprint (dimensions are in mm).



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