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Please note: As part of the Fairchild Semiconductor integration, some of the Fairchild orderable part numbers will need to change in order to meet ON Semiconductor's system requirements. Since the ON Semiconductor product management systems do not have the ability to manage part nomenclature that utilizes an underscore (\_), the underscore (\_) in the Fairchild part numbers will be changed to a dash (-). This document may contain device numbers with an underscore (\_). Please check the ON Semiconductor website to verify the updated device numbers. The most current and up-to-date ordering information can be found at <a href="www.onsemi.com">www.onsemi.com</a>. Please email any questions regarding the system integration to Fairchild <a href="guestions@onsemi.com">guestions@onsemi.com</a>.

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October 2013

# FGB20N60SFD\_F085 600V, 20A Field Stop IGBT

#### **Features**

- · High current capability
- Low saturation voltage: V<sub>CE(sat)</sub> = 2.2V @ I<sub>C</sub> = 20A
- · High input impedance
- · Fast switching
- · Qualified to Automotive Requirements of AEC-Q101
- · RoHS complaint

#### **Applications**

- Inverters, SMPS, PFC, UPS
- · Automotive Chargers, Converters, High Voltage Auxiliaries





**General Description** 

Using novel field-stop IGBT technology, Fairchild's new series of field-stop IGBTs offers the optimum performance for

automotive chargers, inverters, and other applications where

low conduction and switching losses are essential.



### **Absolute Maximum Ratings**

Symbol	Description		Ratings	Units
V <sub>CES</sub>	Collector to Emitter Voltage		600	V
V <sub>GES</sub>	Gate to Emitter Voltage		± 20	V
I <sub>C</sub>	Collector Current	@ T <sub>C</sub> = 25°C	40	Α
•0	Collector Current	@ T <sub>C</sub> = 100°C	20	A
I <sub>CM (1)</sub>	Pulsed Collector Current @ T <sub>C</sub> = 25°C		60	Α
I <sub>F</sub>	Diode Forward Current	@ T <sub>C</sub> = 25°C	20	Α
	Diode Forward Current	@ T <sub>C</sub> = 100°C	10	А
I <sub>FM(1)</sub>	Pulsed Diode Maximum Forward Current		60	Α
P <sub>D</sub>	Maximum Power Dissipation	@ T <sub>C</sub> = 25°C	208	W
	Maximum Power Dissipation	@ T <sub>C</sub> = 100°C	83	W
T <sub>J</sub>	Operating Junction Temperature		-55 to +150	°C
T <sub>stg</sub>	Storage Temperature Range		-55 to +150	°C
T <sub>L</sub>	Maximum Lead Temp. for soldering Purposes, 1/8" from case for 5 seconds		300	°C

#### **Thermal Characteristics**

Symbol	Parameter	Ratings	Units	
$R_{\theta JC}(IGBT)_{(2)}$	Thermal Resistance, Junction to Case	0.6	°C/W	
$R_{\theta JC}(Diode)$	Thermal Resistance, Junction to Case	2.6	°C/W	

Symbol	Parameter	Тур.	Units	
$R_{ hetaJA}$	Thermal Resistance, Junction to Ambient (PCB Mount)(2)	75	°C/W	

# **Package Marking and Ordering Information**

			Packaging		Max Qty
<b>Device Marking</b>	Device	Package	Type	Qty per Tube	per Box
FGB20N60SFD	FGB20N60SFD_F085	TO-263	Tube	50ea	-

## Electrical Characteristics of the IGBT $T_C = 25$ °C unless otherwise noted

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Units
Off Charac	eteristics					
BV <sub>CES</sub>	Collector to Emitter Breakdown Voltage	V <sub>GE</sub> = 0V, I <sub>C</sub> = 250μA	600	-	-	V
$\Delta BV_{CES} \over \Delta T_J$	Temperature Coefficient of Breakdown Voltage	V <sub>GE</sub> = 0V, I <sub>C</sub> = 250μA	-	0.79	-	V/°C
I <sub>CES</sub>	Collector Cut-Off Current	V <sub>CE</sub> = V <sub>CES</sub> , V <sub>GE</sub> = 0V	-	-	250	_
		ICES at 80%*BVCES, 150°C	-	-	250	μΑ
I <sub>GES</sub>	G-E Leakage Current	V <sub>GE</sub> = V <sub>GES</sub> , V <sub>CE</sub> = 0V	-	-	±400	nA
On Charac	teristics					11
V <sub>GE(th)</sub>	G-E Threshold Voltage	$I_{C} = 250 \mu A, V_{CE} = V_{GE}$	4.0	4.8	6.5	V
- (- /		I <sub>C</sub> = 20A, V <sub>GE</sub> = 15V	-	2.2	2.85	V
V <sub>CE(sat)</sub>	Collector to Emitter Saturation Voltage	I <sub>C</sub> = 20A, V <sub>GE</sub> = 15V, T <sub>C</sub> = 125°C	-	2.4	-	٧
Dvnamic C	Characteristics		1			<u>I</u>
C <sub>ies</sub>	Input Capacitance		-	940	1250	pF
C <sub>oes</sub>	Output Capacitance	$V_{CE} = 30V, V_{GE} = 0V,$	-	110	146	pF
C <sub>res</sub>	Reverse Transfer Capacitance	f = 1MHz	-	40	53	pF
Switching	Characteristics				ı	I
t <sub>d(on)</sub>	Turn-On Delay Time		-	10	13	ns
t <sub>r</sub>	Rise Time		-	16	21	ns
t <sub>d(off)</sub>	Turn-Off Delay Time	V <sub>CC</sub> = 400V, I <sub>C</sub> = 20A,	-	90	120	ns
t <sub>f</sub>	Fall Time	$R_G = 10\Omega$ , $V_{GE} = 15V$ , Inductive Load, $T_C = 25^{\circ}C$	-	24	36	ns
E <sub>on</sub>	Turn-On Switching Loss		-	0.31	0.41	mJ
E <sub>off</sub>	Turn-Off Switching Loss		-	0.13	0.21	mJ
E <sub>ts</sub>	Total Switching Loss		-	0.44	0.59	mJ
t <sub>d(on)</sub>	Turn-On Delay Time		-	12	16	ns
t <sub>r</sub>	Rise Time		-	16	21	ns
t <sub>d(off)</sub>	Turn-Off Delay Time	$V_{CC} = 400V, I_{C} = 20A,$	-	95	126	ns
t <sub>f</sub>	Fall Time	$V_{CC} = 400V$ , $I_C = 20A$ , $R_G = 10\Omega$ , $V_{GE} = 15V$ , Inductive Load, $T_C = 125^{\circ}C$	-	28	43	ns
E <sub>on</sub>	Turn-On Switching Loss		-	0.45	0.60	mJ
E <sub>off</sub>	Turn-Off Switching Loss		-	0.21	0.38	mJ
E <sub>ts</sub>	Total Switching Loss		-	0.66	0.88	mJ
			_	63	95	nC
$Q_{\alpha}$	Total Gate Charge					
$\frac{Q_g}{Q_{ge}}$	Gate to Emitter Charge	V <sub>CE</sub> = 400V, I <sub>C</sub> = 20A, V <sub>GE</sub> = 15V	-	7	11	nC

## Electrical Characteristics of the Diode T<sub>C</sub> = 25°C unless otherwise noted

Symbol	Parameter	Test Conditions		Min.	Тур.	Max	Units
V <sub>FM</sub>	V <sub>FM</sub> Diode Forward Voltage	Dilage   I = I UA	$T_{\rm C} = 25^{\rm o}{\rm C}$	-	1.9	2.5	V
V-FM   Blode Forward	2.040 : 0.114.4 : 0.1490		T <sub>C</sub> = 125°C	-	1.7	-	]
t <sub>rr</sub> Diode	Diode Reverse Recovery Time	I <sub>ES</sub> = 10A, dI <sub>ES</sub> /dt = 200A/μs	$T_C = 25^{\circ}C$	-	111	-	ns
11			T <sub>C</sub> = 125°C	-	204	-	
Q <sub>rr</sub>	Q <sub>rr</sub> Diode Reverse Recovery Charge		$T_C = 25^{\circ}C$	-	174	244	nC
111	y enange		$T_{\rm C}$ = 125°C	-	463	1	

#### Notes:

Rthja for D2-PAK: according to JESD51-2, test method environmental condition and JESD51-3, low effective thermal conductivity test board for leaded surface mount package. thermal measurements. JESD51-2: Integrated Circuits Thermal Test Method Environmental Conditions - Natural Convection (Still Air).

<sup>1:</sup> Repetitive rating: Pulse width limited by max. junction temperature

<sup>2:</sup>Rthjc for D2-PAK: according to Mil standard 883-1012 test method.

Figure 1. Typical Output Characteristics

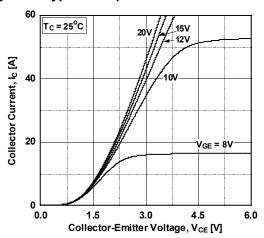


Figure 3. Typical Saturation Voltage Characteristics

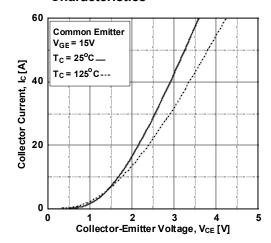
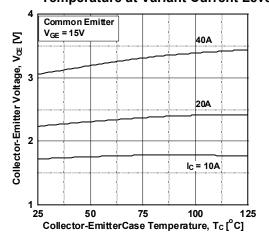


Figure 5. Saturation Voltage vs. Case
Temperature at Variant Current Level



**Figure 2. Typical Output Characteristics** 

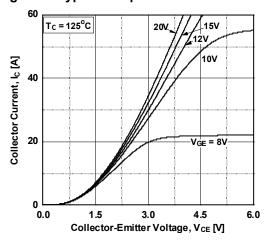


Figure 4. Transfer Characteristics

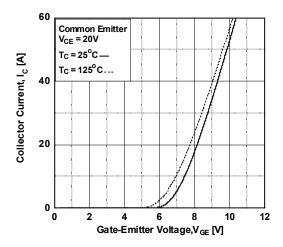


Figure 6. Saturation Voltage vs.  $V_{\text{GE}}$ 

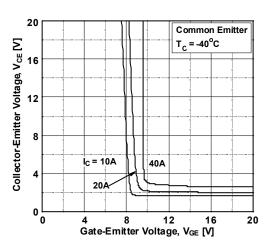


Figure 7. Saturation Voltage vs. V<sub>GE</sub>

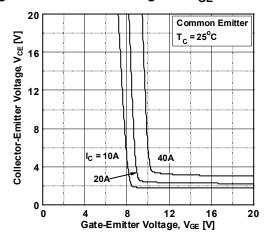


Figure 9. Capacitance Characteristics

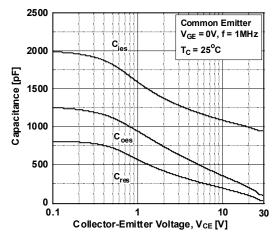


Figure 11. SOA Characteristics

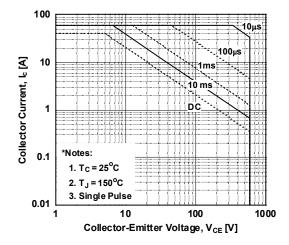


Figure 8. Saturation Voltage vs. V<sub>GE</sub>

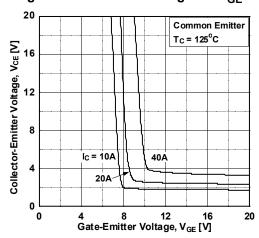


Figure 10. Gate charge Characteristics

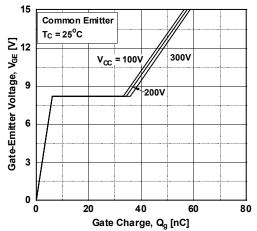


Figure 12. Turn-on Characteristics vs. Gate Resistance

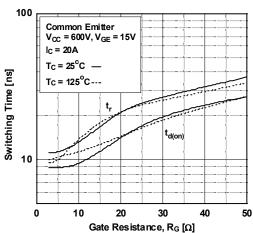


Figure 13. Turn-off Characteristics vs.
Gate Resistance

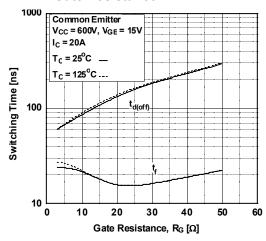


Figure 15. Turn-off Characteristics vs. Collector Current

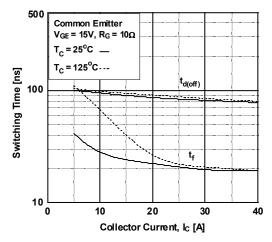


Figure 17. Switching Loss vs. Collector Current

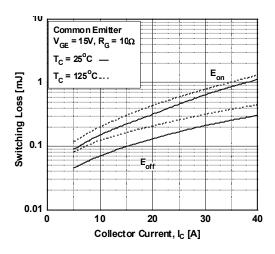


Figure 14. Turn-on Characteristics vs.
Collector Current

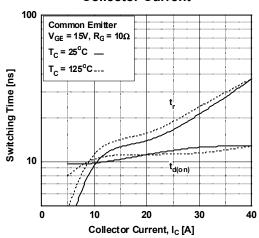


Figure 16. Switching Loss vs. Gate Resistance

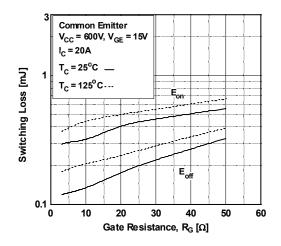


Figure 18. Turn off Switching SOA Characteristics

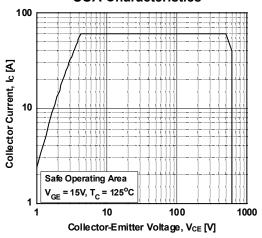


Figure 19. Forward Characteristics

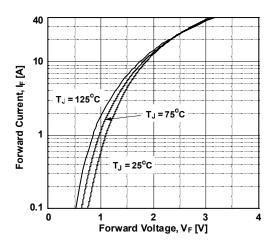


Figure 20. Typical Reverse Current vs. Reverse Voltage

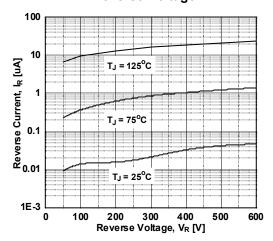


Figure 21. Stored Charge

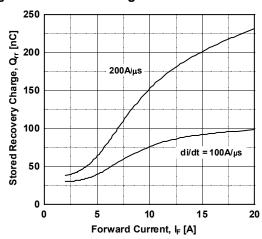


Figure 22. Reverse Recovery Time

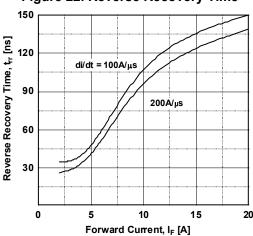
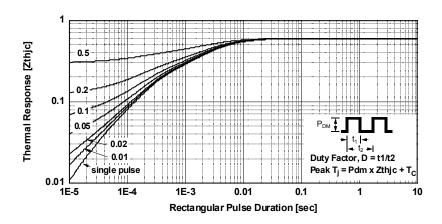
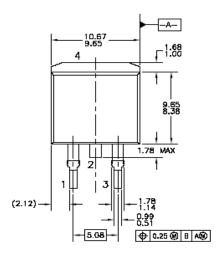


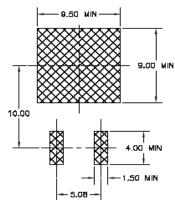
Figure 23. Transient Thermal Impedance of IGBT



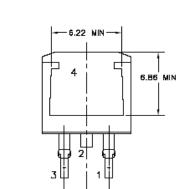
#### **Mechanical Dimensions**

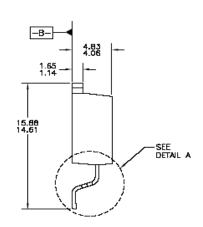
# D<sup>2</sup>PAK

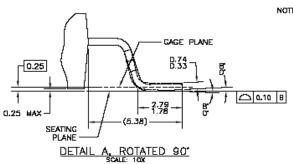




LAND PATTERN RECOMMENDATION







- NOTES: LINLESS OTHERWISE SPECIFIED

  A) ALL DIMENSIONS ARE IN MILLIMETERS.

  B) REFERENCE JEDEC, TO—263, ISSUE D,
  VARIATION AB, DATED JULY 2003.

  C) DIMENSIONING AND TOLERANCING PER
  ANSI Y14.5M 1982.

  D) LOCATION OF THE PIN HOLE MAY VARY
  (LOWER LEFT CORNER, LOWER CENTER
  AND CENTER OF THE PACKAGE).

  B) PRESENCE OF TRIMMED CENTER LEAD
  IS OPTIONAL

**Dimensions in Millimeters** 

TO283AD2REVD





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Datasheet Identification	Product Status	Definition		
Advance Information Formative / In Design		Datasheet contains the design specifications for product development. Specifications may change in any manner without notice.		
Preliminary	First Production	Datasheet contains preliminary data; supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.		
No Identification Needed	No Identification Needed Full Production Datasheet contains final specifications. Fairchild Semiconductor reserves make changes at any time without notice to improve the design.			
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