VS-GT50TP120N

Vishay Semiconductors



Molding Type Module IGBT, 2 in 1 Package, 1200 V, 50 A



PRIMARY CHARACTERISTICS					
V _{CES} 1200 V					
I _C at T _C = 80 °C	50 A				
V _{CE(on)} (typical) at I _C = 50 A, 25 °C	1.65 V				
Speed	8 kHz to 30 kHz				
Package	INT-A-PAK				
Circuit configuration	Half bridge				

FEATURES

- Low V_{CE(on)} trench IGBT technology
- Low switching losses
- 10 µs short circuit capability
- V_{CE(on)} with positive temperature coefficient
- Maximum junction temperature 175 °C
- Low inductance case
- · Fast and soft reverse recovery antiparallel FWD
- Isolated copper baseplate using DCB (Direct Copper Bonding) technology
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

TYPICAL APPLICATIONS

- UPS (Uninterruptable Power Supply)
- Electronic welders
- Switching mode power supplies

DESCRIPTION

Vishay's IGBT power module provides ultra low conduction loss as well as short circuit ruggedness. It is designed for applications such as UPS and SMPS.

ABSOLUTE MAXIMUM RATINGS (T _C = 25 °C unless otherwise noted)					
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS	
Collector to emitter voltage	V _{CES}		1200	V	
Gate to emitter voltage	V _{GES}		± 20	v	
Collector current		T _C = 25 °C	100		
	IC	T _C = 80 °C	50		
Pulsed collector current	I _{CM} ⁽¹⁾	t _p = 1 ms	100	А	
Diode continuous forward current	IF		50		
Diode maximum forward current	I _{FM} ⁽¹⁾		100		
Maximum power dissipation	PD	T _J = 175 °C	405	W	
RMS isolation voltage	V _{ISOL}	f = 50 Hz, t = 1 min	2500	V	

Note

⁽¹⁾ Repetitive rating: pulse width limited by maximum junction temperature

IGBT ELECTRICAL SPECIFICATIONS ($T_c = 25 \text{ °C}$ unless otherwise noted)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Collector to emitter breakdown voltage	V _{(BR)CES}	T _J = 25 °C	1200	-	-	
Collector to emitter voltage	Maria a	V_{GE} = 15 V, I_C = 50 A, T_J = 25 °C	-	1.90	2.35	v
Collector to emitter voltage V _{CE(on)}	V_{GE} = 15 V, I _C = 50 A, T _J = 175 °C	-	2.50	-	v	
Gate to emitter threshold voltage	V _{GE(th)}	$V_{CE} = V_{GE}$, $I_C = 1.4$ mA, $T_J = 25$ °C	5.0	5.5	7.5	
Collector cut-off current	I _{CES}	$V_{CE} = V_{CES}, V_{GE} = 0 \text{ V}, \text{ T}_{J} = 25 ^{\circ}\text{C}$	-	-	5.0	mA
Gate to emitter leakage current	I _{GES}	$V_{GE} = V_{GES}, V_{CE} = 0 \text{ V}, T_J = 25 ^{\circ}\text{C}$	-	-	400	nA

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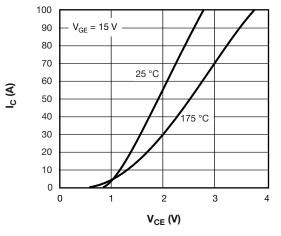
SWITCHING CHARACTERISTICS	5					
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Turn-on delay time	t _{d(on)}		-	148	-	- ns
Rise time	t _r		-	84	-	
Turn-off delay time	t _{d(off)}	$V_{CC} = 600 \text{ V}, \text{ I}_{C} = 50 \text{ A}, \text{ R}_{g} = 15 \Omega,$	-	245	-	
Fall time	t _f	$V_{GE} = \pm 15 \text{ V}, \text{ T}_{J} = 25 \text{ °C}^{9}$	-	251	-	
Turn-on switching loss	E _{on}		-	5.51	-	
Turn-off switching loss	E _{off}		-	2.70	-	mJ
Turn-on delay time	t _{d(on)}		-	263	-	ns
Rise time	t _r		-	81	-	
Turn-off delay time	t _{d(off)}		-	256	-	
Fall time	t _f		-	292	-	
Turn-on switching loss	E _{on}		-	6.63	-	mJ
Turn-off switching loss	E _{off}		-	3.25	-	mJ
Input capacitance	Cies		-	6.24	-	
Output capacitance	C _{oes}	$V_{GE} = 0 V$, $V_{CE} = 30 V$, f = 1.0 MHz	-	0.23	-	nF
Reverse transfer capacitance	C _{res}		-	0.15	-	
SC data	I _{SC}	$\begin{array}{l} t_p \leq 10 \; \mu s, \; V_{GE} = 15 \; V, \; T_J = 125 \; ^{\circ}C, \\ V_{CC} = 600 \; V, \; V_{CEM} \leq 1200 \; V \end{array}$	-	450	-	А
Stray inductance	L _{CE}		-	-	30	nH
Module lead resistance, terminal to chip	R _{CC'+EE'}		-	0.75	-	mΩ

DIODE ELECTRICAL SPECIFICATIONS ($T_c = 25$ °C unless otherwise noted)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
Forward voltage	N/	I _F = 50 A	T _J = 25 °C	-	1.85	2.25	- V
Forward voltage	V _F		T _J = 125 °C	-	1.95	-	
Reverse recovery charge	0	Q _{rr}	T _J = 25 °C	-	3.1	-	μC
neverse recovery charge	Q _{rr}		T _J = 125 °C	-	6.1	-	μΟ
Poole reverse receivery ourrent	I _{rr}	V _{GE} = -15 V	T _J = 25 °C	-	24	-	А
Peak reverse recovery current			T _J = 125 °C	-	31	-	A
Reverse recovery energy E _{rec}	F		T _J = 25 °C	-	0.98	-	mJ
		T _J = 125 °C	-	2.06	-	IIIJ	

THERMAL AND MECHANICAL SPECIFICATIONS							
PARAMETER		SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Operating junction temperature	9	TJ		-	-	175	°C
Storage temperature range		T _{Stg}		-40	-	125	°C
Junction to case	IGBT	- R _{thJC}		-	-	0.37	K/W
	Diode			-	-	0.49	
Case to sink (conductive greas	e applied)	R _{thCS}		-	0.05	-	
Mounting torque			Power terminal screw: M5		2.5 to 5.0		Nime
			Mounting screw: M6		3.0 to 5.0)	Nm
Weight			Weight of module	-	150	-	g



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Fig. 1 - IGBT Typical Output Characteristics

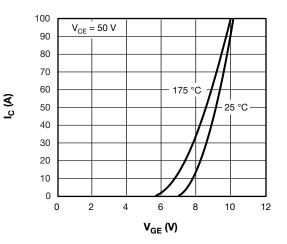


Fig. 2 - IGBT Transfer Characteristics

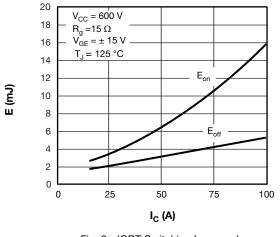


Fig. 3 - IGBT Switching Loss vs. I_C

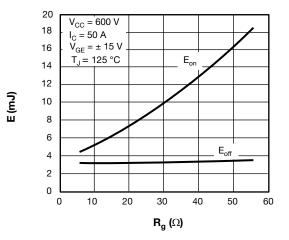
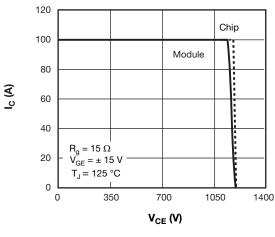


Fig. 4 - IGBT Switching Loss vs. R_G



V_{CE} (V) Fig. 5 - RBSOA

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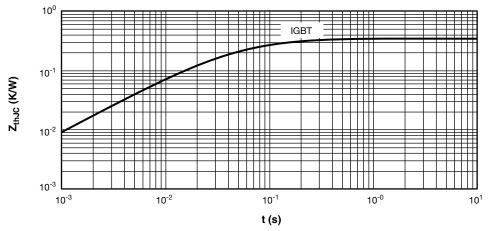
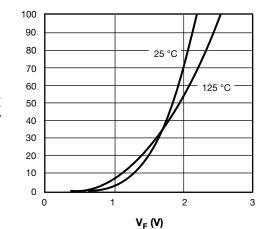
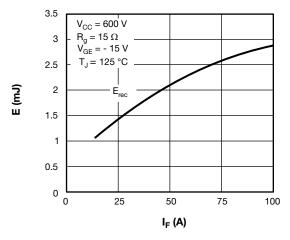


Fig. 6 - IGBT Transient Thermal Impedance



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Fig. 7 - Diode Forward Characteristics





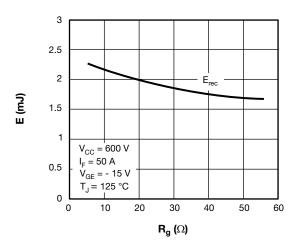


Fig. 9 - Diode Switching Loss vs. R_G

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I_F (A)

SHA

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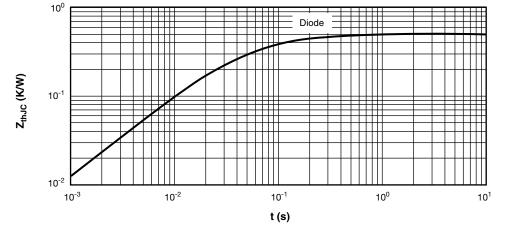
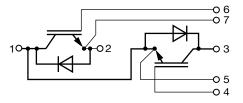


Fig. 10 - Diode Transient Thermal Impedance

CIRCUIT CONFIGURATION



LINKS TO RELATED DOCUMENTS				
Dimensions	www.vishay.com/doc?95524			



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