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Vishay Siliconix

# Automotive P-Channel 60 V (D-S) 175 °C MOSFET

PRODUCT SUMMARY				
V <sub>DS</sub> (V)	- 60			
$R_{DS(on)}(\Omega)$ at $V_{GS}$ = - 10 V	0.085			
$R_{DS(on)}(\Omega)$ at $V_{GS} = -4.5 \text{ V}$	0.115			
I <sub>D</sub> (A)	- 4.6			
Configuration	Single			

#### **FEATURES**

- Halogen-free According to IEC 61249-2-21 Definition
- TrenchFET® Power MOSFET
- AEC-Q101 Qualified<sup>c</sup>
- 100 % Rq and UIS Tested
- Compliant to RoHS Directive 2002/95/EC







SO-8		s o
S 1 S 2 S 3 G 4	8 D 7 D 6 D 5 D	G
Top View		P-Channel MOSFET

ORDERING INFORMATION	
Package	SO-8
Lead (Pb)-free and Halogen-free	SQ9407EY-T1-GE3

ABSOLUTE MAXIMUM RATING	S (T <sub>C</sub> = 25 °C, unles	s otherwise noted	d)	
PARAMETER		SYMBOL	LIMIT	UNIT
Drain-Source Voltage		$V_{DS}$	- 60	
Gate-Source Voltage		V <sub>GS</sub>	± 20	V
Continuous Duois Current	T <sub>C</sub> = 25 °C	- I <sub>D</sub>	- 4.6	
Continuous Drain Current	T <sub>C</sub> = 125 °C		- 2.6	
Continuous Source Current (Diode Conduction)		I <sub>S</sub>	- 3.4	А
Pulsed Drain Current <sup>a</sup>		I <sub>DM</sub>	- 18.5	
Single Pulse Avalanche Current	. 0.4	I <sub>AS</sub>	- 20	
Single Pulse Avalanche Energy	L = 0.1 mH	E <sub>AS</sub>	20	mJ
	T <sub>C</sub> = 25 °C	P <sub>D</sub>	3.75	w
Maximum Power Dissipation <sup>a</sup>	T <sub>C</sub> = 125 °C		1.25	
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	- 55 to + 175	°C

THERMAL RESISTANCE RATINGS					
PARAMETER		SYMBOL	LIMIT	UNIT	
Junction-to-Ambient	-to-Ambient PCB Mount <sup>b</sup>		100	°C/W	
Junction-to-Foot (Drain)		$R_{thJF}$	40	- C/VV	

#### Notes

- a. Pulse test; pulse width  $\leq$  300 µs, duty cycle  $\leq$  2 %.
- b. When mounted on 1" square PCB (FR-4 material).
- c. Parametric verification ongoing.



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PARAMETER	SYMBOL	vise noted)  TEST CONDITIONS		MIN.	TYP.	MAX.	UNIT
Static					I	l	
Drain-Source Breakdown Voltage	$V_{DS}$	$V_{GS} = 0 \text{ V}, I_D = -250 \mu\text{A}$		- 60	-	-	V
Gate-Source Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =	$V_{DS} = V_{GS}, I_D = -250 \mu\text{A}$		- 2.0	- 2.5	
Gate-Source Leakage	I <sub>GSS</sub>	V <sub>DS</sub> =	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$		-	± 100	nA
		V <sub>GS</sub> = 0 V	V <sub>DS</sub> = - 60 V	-	-	- 1	μA
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>GS</sub> = 0 V	V <sub>DS</sub> = - 60 V, T <sub>J</sub> = 125 °C	-	-	- 50	
		V <sub>GS</sub> = 0 V	V <sub>DS</sub> = - 60 V, T <sub>J</sub> = 175 °C	-	-	- 150	
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	V <sub>GS</sub> = - 10 V	$V_{DS} \le -5 V$	- 12	-	-	Α
		V <sub>GS</sub> = - 4.5 V	I <sub>D</sub> = - 2.5 A	-	0.087	0.115	Ω
Drain Course On State Resistance	В	V <sub>GS</sub> = - 10 V	I <sub>D</sub> = - 3.5 A	-	0.067	0.085	
Drain-Source On-State Resistance <sup>a</sup>	R <sub>DS(on)</sub>	V <sub>GS</sub> = - 10 V	I <sub>D</sub> = - 3.5 A, T <sub>J</sub> = 125 °C	-	-	0.143	
		V <sub>GS</sub> = - 10 V	I <sub>D</sub> = - 3.5 A, T <sub>J</sub> = 175 °C	-	-	0.176	
Forward Transconductanceb	9 <sub>fs</sub>	V <sub>DS</sub> =	V <sub>DS</sub> = - 15 V, I <sub>D</sub> = - 3.5 A		10	-	S
Dynamic <sup>b</sup>							
Input Capacitance	C <sub>iss</sub>		V <sub>GS</sub> = 0 V V <sub>DS</sub> = - 30 V, f = 1 MHz	-	912	1140	pF
Output Capacitance	C <sub>oss</sub>	$V_{GS} = 0 V$		-	100	125	
Reverse Transfer Capacitance	C <sub>rss</sub>			-	60	75	
Total Gate Charge <sup>c</sup>	Qg			-	26.5	40	nC
Gate-Source Charge <sup>c</sup>	Q <sub>gs</sub>	V <sub>GS</sub> = - 10 V	$V_{DS} = -30 \text{ V}, I_{D} = -4.3 \text{ A}$	-	3.8	-	
Gate-Drain Charge <sup>c</sup>	Q <sub>gd</sub>			-	5.8	-	
Gate Resistance	R <sub>g</sub>	f = 1 MHz		3.5	7.1	10.7	Ω
Turn-On Delay Time <sup>c</sup>	t <sub>d(on)</sub>			-	11	17	
Rise Time <sup>c</sup>	t <sub>r</sub>	$V_{DD} = -30 \text{ V}, R_L = 8.8 \Omega$ $I_D \cong -3.4 \text{ A}, V_{GEN} = -10 \text{ V}, R_g = 1 \Omega$		-	13	20	ns
Turn-Off Delay Time <sup>c</sup>	t <sub>d(off)</sub>			-	36	54	
Fall Time <sup>c</sup>	t <sub>f</sub>			-	8	12	
Source-Drain Diode Ratings and Chara	acteristics <sup>b</sup>						
Pulsed Current <sup>a</sup>	I <sub>SM</sub>			-	-	- 18.5	Α
Forward Voltage	$V_{SD}$	I <sub>F</sub> = - 3 A, V <sub>GS</sub> = 0 V		-	- 0.84	- 1.1	V

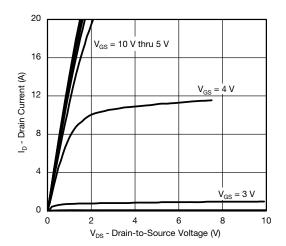
#### Notes

- a. Pulse test; pulse width  $\leq$  300 µs, duty cycle  $\leq$  2 %.
- b. Guaranteed by design, not subject to production testing.
- c. Independent of operating temperature.

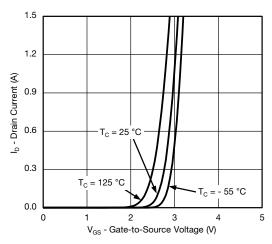
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



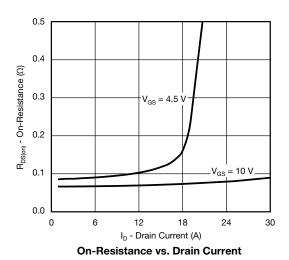
### **TYPICAL CHARACTERISTICS** (T<sub>A</sub> = 25 °C, unless otherwise noted)

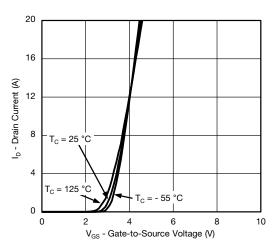


#### **Output Characteristics**

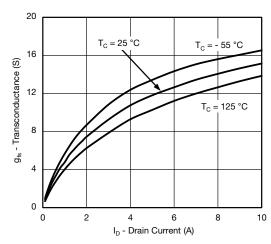


**Transfer Characteristics** 

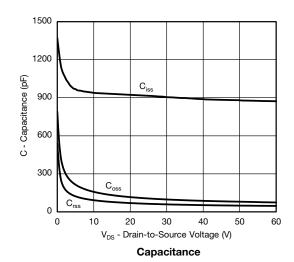




**Transfer Characteristics** 

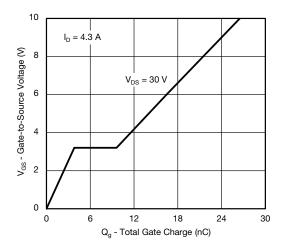


Transconductance

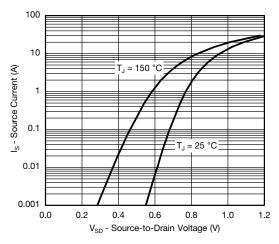




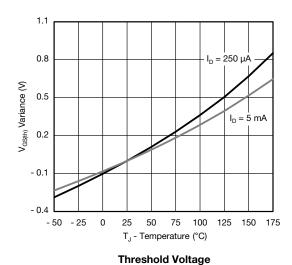
### **TYPICAL CHARACTERISTICS** (T<sub>A</sub> = 25 °C, unless otherwise noted)



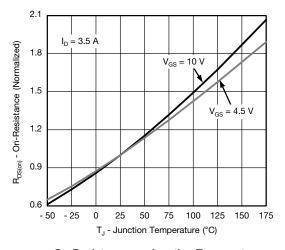
#### **Gate Charge**



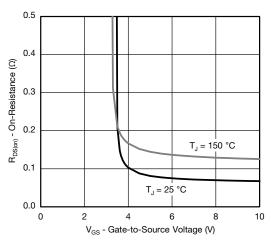
#### **Source Drain Diode Forward Voltage**



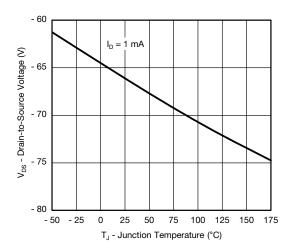
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On-Resistance vs. Junction Temperature



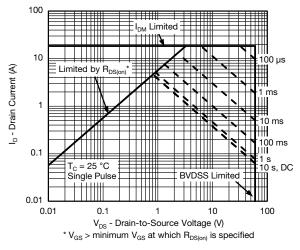
On-Resistance vs. Gate-to-Source Voltage



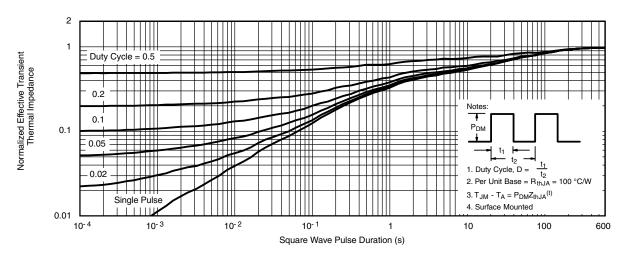
Drain Source Breakdown vs. Junction Temperature



### **THERMAL RATINGS** ( $T_A = 25$ °C, unless otherwise noted)

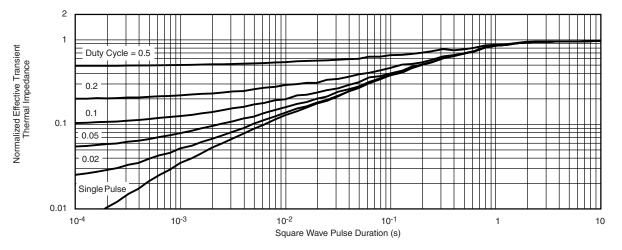


Safe Operating Area



Normalized Thermal Transient Impedance, Junction-to-Ambient

### THERMAL RATINGS (T<sub>A</sub> = 25 °C, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Foot

#### Note

- The characteristics shown in the two graphs
- Normalized Transient Thermal Impedance Junction-to-Ambient (25 °C)
- Normalized Transient Thermal Impedance Junction-to-Foot (25 °C)

are given for general guidelines only to enable the user to get a "ball park" indication of part capabilities. The data are extracted from single pulse transient thermal impedance characteristics which are developed from empirical measurements. The latter is valid for the part mounted on printed circuit board - FR4, size 1" x 1" x 0.062", double sided with 2 oz. copper, 100 % on both sides. The part capabilities can widely vary depending on actual application parameters and operating conditions.

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see www.vishay.com/ppg?67582.



SOIC (NARROW): 8-LEAD JEDEC Part Number: MS-012







	MILLIMETERS INCHES			HES	
DIM	Min	Max	Min	Max	
Α	1.35	1.75	0.053	0.069	
A <sub>1</sub>	0.10	0.20	0.004	0.008	
В	0.35	0.51	0.014	0.020	
С	0.19	0.25	0.0075	0.010	
D	4.80	5.00	0.189	0.196	
Е	3.80	4.00	0.150	0.157	
е	1.27 BSC		0.050 BSC		
Н	5.80	6.20	0.228	0.244	
h	0.25	0.50	0.010	0.020	
L	0.50	0.93	0.020	0.037	
q	0°	8°	0°	8°	
S	0.44	0.64	0.018	0.026	
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### **RECOMMENDED MINIMUM PADS FOR SO-8**



Recommended Minimum Pads Dimensions in Inches/(mm)

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