

# Molding Type Module IGBT, 2-in-1 Package, 1200 V and 300 A



PRIMARY CHARACTERISTICS					
$V_{CES}$	1200 V				
$I_C$ at $T_C = 80$ °C	300 A				
$V_{CE(on)}$ (typical) at $I_C = 300$ A, 25 °C	2.00 V				
Speed	8 kHz to 30 kHz				
Package	Dual INT-A-PAK				
Circuit configuration	Half bridge				

### **FEATURES**

- 10 µs short circuit capability
- V<sub>CE(on)</sub> with positive temperature coefficient
- Maximum junction temperature 150 °C
- Low inductance case
- · Fast and soft reverse recovery antiparallel FWD
- Isolated copper baseplate using DCB (Direct Copper Bonding) technology
- Material categorization: for definitions of compliance please see <a href="https://www.vishay.com/doc?99912">www.vishay.com/doc?99912</a>

### **TYPICAL APPLICATIONS**

- UPS
- · Inverter for motor drive
- AC and DC servo drive amplifier

#### DESCRIPTION

Vishay's IGBT power module provides ultra low conduction loss as well as short circuit ruggedness. It is designed for applications such as general inverters and UPS.

ABSOLUTE MAXIMUM RATINGS (T <sub>C</sub> = 25 °C unless otherwise noted)				
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS
Collector to emitter voltage	V <sub>CES</sub>		1200	V
Gate to emitter voltage	$V_{GES}$		± 20	V
Collector current		T <sub>C</sub> = 25 °C	500	
Collector current	I <sub>C</sub>	T <sub>C</sub> = 80 °C	300	
Pulsed collector current	I <sub>CM</sub> <sup>(1)</sup>	t <sub>p</sub> = 1 ms	600	Α
Diode continuous forward current	I <sub>F</sub>	T <sub>C</sub> = 80 °C	300	
Diode maximum forward current	I <sub>FM</sub>	t <sub>p</sub> = 1 ms	600	
Maximum power dissipation	P <sub>D</sub>	T <sub>J</sub> = 150 °C	1645	W
Short circuit withstand time	t <sub>SC</sub>	T <sub>J</sub> = 125 °C	10	μs
RMS isolation voltage	V <sub>ISOL</sub>	f = 50 Hz, t = 1 min	2500	V

#### Note

<sup>(1)</sup> Repetitive rating: pulse width limited by maximum junction temperature

IGBT ELECTRICAL SPECIFICATIONS (T <sub>C</sub> = 25 °C unless otherwise noted)						
PARAMETER	SYMBOL	SYMBOL TEST CONDITIONS MIN.		TYP.	MAX.	UNITS
Collector to emitter breakdown voltage	V <sub>(BR)CES</sub>	T <sub>J</sub> = 25 °C	1200	-	-	
Collector to emitter voltage	V	$V_{GE} = 15 \text{ V}, I_{C} = 300 \text{ A}, T_{J} = 25 ^{\circ}\text{C}$	-	2.00	2.45	v
Collector to enlitter voltage	V <sub>CE(on)</sub>	V <sub>GE</sub> = 15 V, I <sub>C</sub> = 300 A, T <sub>J</sub> = 125 °C	-	2.20	-	]
Gate to emitter threshold voltage	$V_{GE(th)}$	$V_{CE} = V_{GE}$ , $I_C = 12$ mA, $T_J = 25$ °C	5.0	6.2	7.0	
Collector cut-off current	I <sub>CES</sub>	$V_{CE} = V_{CES}$ , $V_{GE} = 0$ V, $T_{J} = 25$ °C	-	-	5.0	mA
Gate to emitter leakage current	I <sub>GES</sub>	$V_{GE} = V_{GES}$ , $V_{CE} = 0$ V, $T_{J} = 25$ °C	-	-	400	nA



PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Turn-on delay time	t <sub>d(on)</sub>		-	574	-	ns mJ
Rise time	t <sub>r</sub>		-	133	-	
Turn-off delay time	t <sub>d(off)</sub>	$V_{CC} = 600 \text{ V}, I_{C} = 300 \text{ A}, R_{g} = 4.7 \Omega,$	-	563	-	
Fall time	t <sub>f</sub>	V <sub>GE</sub> = ± 15 V, T <sub>J</sub> = 25 °C	-	120	-	
Turn-on switching loss	E <sub>on</sub>		-	23.9	-	
Turn-off switching loss	E <sub>off</sub>		-	25.3	-	
Turn-on delay time	t <sub>d(on)</sub>		-	604	-	ns ns
Rise time	t <sub>r</sub>		-	137	-	
Turn-off delay time	t <sub>d(off)</sub>	$V_{CC} = 600 \text{ V}, I_{C} = 300 \text{ A}, R_{g} = 4.7 \Omega,$ $V_{GE} = \pm 15 \text{ V}, T_{J} = 125 ^{\circ}\text{C}$	-	629	-	
Fall time	t <sub>f</sub>		-	167	-	
Turn-on switching loss	E <sub>on</sub>		-	31.5	-	m l
Turn-off switching loss	E <sub>off</sub>		-	35.9	-	- mJ
Input capacitance	C <sub>ies</sub>		-	21.2	-	
Output capacitance	C <sub>oes</sub>	$V_{GE} = 0 \text{ V}, V_{CE} = 25 \text{ V}, f = 1.0 \text{ MHz}$	-	1.42	-	nF
Reverse transfer capacitance	C <sub>res</sub>		-	0.94	-	
SC data	I <sub>SC</sub>	$t_{\text{SC}} \leq 10 \; \mu\text{s},  V_{\text{GE}} = 15 \; \text{V},  T_{\text{J}} = 125 \; ^{\circ}\text{C}, \\ V_{\text{CC}} = 900 \; \text{V},  V_{\text{CEM}} \leq 1200 \; \text{V}$	-	1800	-	Α
Internal gate resistance	R <sub>gint</sub>		-	1.0	-	Ω
Stray inductance	L <sub>CE</sub>		-	-	20	nΗ
Module lead resistance, terminal to chip	R <sub>CC'+EE'</sub>	T <sub>C</sub> = 25 °C	-	0.35	-	mΩ

<b>DIODE ELECTRICAL SPECIFICATIONS</b> (T <sub>C</sub> = 25 °C unless otherwise noted)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
Diede femueral veltere		I 000 A	T <sub>J</sub> = 25 °C	-	1.82	2.25	V
Diode forward voltage	V <sub>F</sub>	I <sub>F</sub> = 300 A	T <sub>J</sub> = 125 °C	-	1.95	-	
Diode reverse recovery charge	Q <sub>rr</sub>		T <sub>J</sub> = 25 °C	-	20.2	-	
Diode reverse recovery charge			T <sub>J</sub> = 125 °C	-	40.1	-	μC
Diede peek verseer verserver versent	I <sub>rr</sub>	I <sub>F</sub> = 300 A, V <sub>R</sub> = 600 V, dl/dt = -2360 A/μs, V <sub>GE</sub> = -15 V	T <sub>J</sub> = 25 °C	-	170		^
Diode peak reverse recovery current			T <sub>J</sub> = 125 °C	-	250		Α
Diede versene verenen en ever	Г		T <sub>J</sub> = 25 °C	-	8.2	-	I
Diode reverse recovery energy	E <sub>rec</sub>		T <sub>J</sub> = 125 °C	-	21.7	-	mJ

THERMAL AND MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Operating junction temperature range	TJ		-	-	150	°C
Storage temperature range	T <sub>STG</sub>		-40	-	125	
Junction to case	D		-	-	0.076	
Diode	$R_{thJC}$		-	-	0.100	K/W
Case to sink	R <sub>thCS</sub>	Conductive grease applied	=	0.035	-	
Mounting torque		Power terminal screw: M6	2.5 to 5.0		Nm	
		Mounting screw: M6	3.0 to 5.0		INIII	
Weight				300		g





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# Vishay Semiconductors

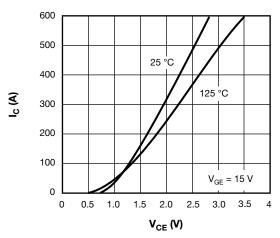


Fig. 1 - IGBT Typical Output Characteristics

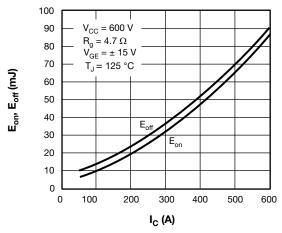


Fig. 3 - IGBT Switching Loss vs. I<sub>C</sub>

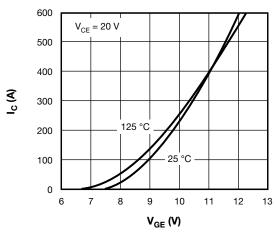


Fig. 2 - IGBT Typical Transfer Characteristics

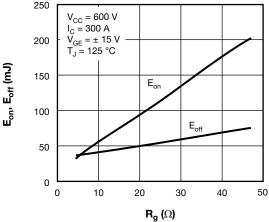
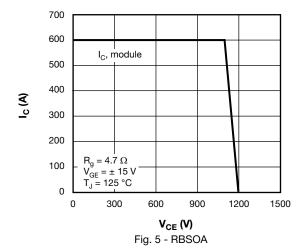


Fig. 4 - IGBT Switching Loss vs. R<sub>a</sub>



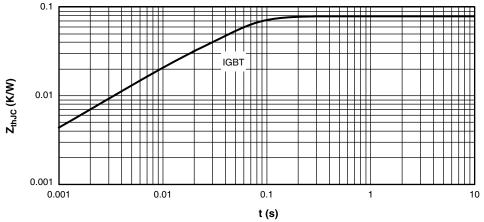


Fig. 6 - IGBT Transient Thermal Impedance

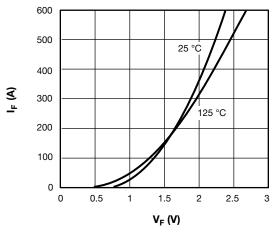


Fig. 7 - Typical Forward Characteristics

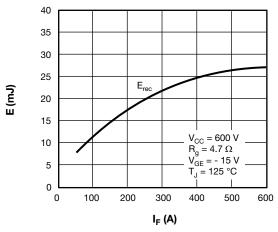


Fig. 8 - Diode Switching Loss vs. I<sub>F</sub>

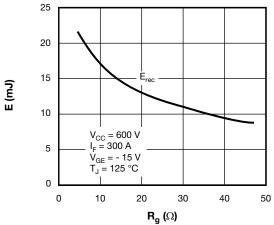


Fig. 9 - Diode Switching Loss vs. Gate Resistance  $R_q$ 



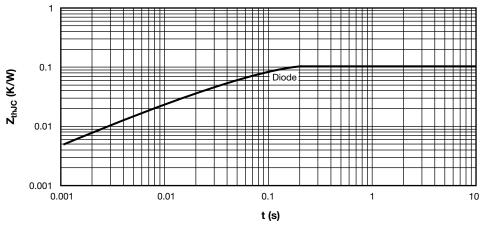
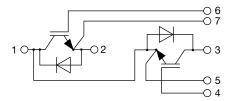


Fig. 10 - Diode Transient Thermal Impedance

### **CIRCUIT CONFIGURATION**

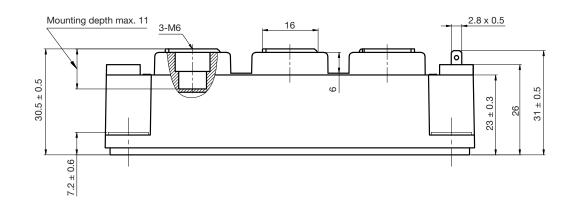


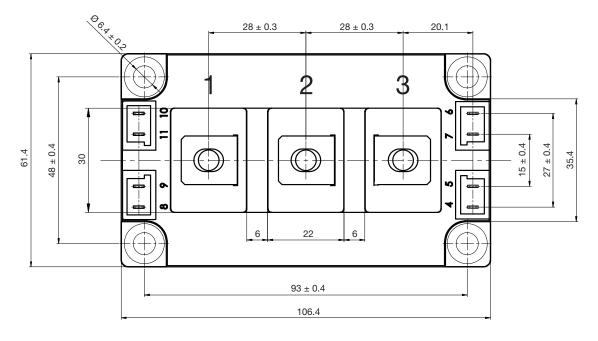
LINKS TO RELATED DOCUMENTS			
Dimensions	www.vishay.com/doc?95525		



# **Double INT-A-PAK**

### **DIMENSIONS** in millimeters (inches)







## **Legal Disclaimer Notice**

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