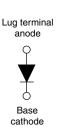
Vishay Semiconductors

# High Performance Schottky Rectifier, 120 A



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HALF-PAK (D-67)

| PRIMARY CHARACTERISTICS  |                 |  |  |
|--------------------------|-----------------|--|--|
| I <sub>F(AV)</sub> 120 A |                 |  |  |
| V <sub>R</sub>           | 45 V            |  |  |
| Package                  | HALF-PAK (D-67) |  |  |
| Circuit configuration    | Single          |  |  |

### **FEATURES**

- 175 °C T<sub>J</sub> operation
- Low forward voltage drop
- High frequency operation
- Guard ring for enhanced ruggedness and long term reliability
- · Designed and qualified for industrial level
- UL approved file E222165
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

#### DESCRIPTION

The VS-121NQ.. high current Schottky rectifier module series has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 175 °C junction temperature. Typical applications are in high current switching power supplies, plating power supplies, UPS systems, converters, freewheeling diodes, welding, and reverse battery protection.

| MAJOR RATINGS AND CHARACTERISTICS |   |             |       |  |  |
|-----------------------------------|---|-------------|-------|--|--|
| SYMBOL                            | CHARACTERISTICS                               | VALUES      | UNITS |  |  |
| I <sub>F(AV)</sub>                | Rectangular waveform                          | 120         | A     |  |  |
| V <sub>RRM</sub>                  |   | 45          | V     |  |  |
| I <sub>FSM</sub>                  | t <sub>p</sub> = 5 μs sine                    | 16 000      | A     |  |  |
| V <sub>F</sub>                    | 120 A <sub>pk</sub> , T <sub>J</sub> = 125 °C | 0.6         | V     |  |  |
| TJ                                | Range   | -55 to +175 | °C    |  |  |

| VOLTAGE RATINGS                      |                  |                |       |  |
|--------------------------------------|------------------|----------------|-------|--|
| PARAMETER                            | SYMBOL           | VS-121NQ045PbF | UNITS |  |
| Maximum DC reverse voltage           | V <sub>R</sub>   | 45             | V     |  |
| Maximum working peak reverse voltage | V <sub>RWM</sub> | 45             | v     |  |

| ABSOLUTE MAXIMUM RATINGS                               |                    |   |   |        |       |
|--|--------------------|---|---|--------|-------|
| PARAMETER  | SYMBOL             | TEST CONDITIONS   |   | VALUES | UNITS |
| Maximum average forward current<br>See fig. 5          | I <sub>F(AV)</sub> | 50 % duty cycle at $T_{C}$ = 137 °C, rectangular waveform   |   | 120    | A     |
| Maximum peak one cycle<br>non-repetitive surge current |                    | 5 µs sine or 3 µs rect. pulse   | Following any rated load condition and with rated | 16 000 | А     |
| See fig. 7   | IFSM               | 10 ms sine or 6 ms rect. pulse  | V <sub>RRM</sub> applied                          | 2000   | ~     |
| Non-repetitive avalanche energy                        | E <sub>AS</sub>    | T <sub>J</sub> = 25 °C, I <sub>AS</sub> = 13 A, L = 1 mH  |   | 81     | mJ    |
| Repetitive avalanche current                           | I <sub>AR</sub>    | Current decaying linearly to zero in 1 $\mu$ s<br>Frequency limited by T <sub>J</sub> maximum V <sub>A</sub> = 1.5 x V <sub>R</sub> typical |   | 13     | A     |

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| ELECTRICAL SPECIFICATIONS                     |                                |   |                           |        |       |
|---|--------------------------------|---|---------------------------|--------|-------|
| PARAMETER                                     | SYMBOL                         | TEST CONDITIONS   |                           | VALUES | UNITS |
| Maximum forward voltage drop                  | V <sub>EM</sub> <sup>(1)</sup> | 120 A   | - T <sub>J</sub> = 25 °C  | 0.65   | V     |
|   |                                | 240 A   |                           | 0.82   |       |
| See fig. 1                                    | VFM (**                        | 120 A   | - T <sub>J</sub> = 125 °C | 0.6    |       |
|   |                                | 240 A   |                           | 0.76   |       |
| Maximum reverse leakage current<br>See fig. 2 | I <sub>RM</sub>                | $T_J = 25 \ ^{\circ}C$  | V Deted V                 | 10     | mA    |
|   |                                | T <sub>J</sub> = 125 °C                                       | $V_R = Rated V_R$         | 90     |       |
| Maximum junction capacitance                  | CT                             | $V_R$ = 5 $V_{DC}$ (test signal range 100 kHz to 1 MHz) 25 °C |                           | 5200   | pF    |
| Typical series inductance                     | L <sub>S</sub>                 | From top of terminal hole to mounting plane                   |                           | 7.0    | nH    |
| Maximum voltage rate of change                | dV/dt                          | Rated V <sub>R</sub>  |                           | 10 000 | V/µs  |

#### Note

<sup>(1)</sup> Pulse width = 500  $\mu$ s

| THERMAL - MECHANICAL SPECIFICATIONS          |                                     |                                   |                                      |                 |                     |  |
|--|-------------------------------------|-----------------------------------|--------------------------------------|-----------------|---------------------|--|
| PARAMETER                                    |                                     | SYMBOL                            | TEST CONDITIONS                      | VALUES          | UNITS               |  |
| Maximum junction and storage ter             | mperature range                     | T <sub>J</sub> , T <sub>Stg</sub> |                                      | -55 to 175      | °C                  |  |
| Maximum thermal resistance, junction to case |                                     | R <sub>thJC</sub>                 | DC operation<br>See fig. 4           | 0.38            | °C/W                |  |
| Typical thermal resistance, case to          | hermal resistance, case to heatsink |                                   | Mounting surface, smooth and greased | 0.05            |                     |  |
| Approvingets weight                          |                                     |                                   |                                      | 30              | g                   |  |
| Approximate weight                           | Approximate weight                  |                                   |                                      | 1.06            | oz.                 |  |
|  | minimum                             |                                   | Non-lubricated threads               | 3 (26.5)        | N ⋅ m<br>(lbf ⋅ in) |  |
| Mounting torque                              | maximum                             |                                   |                                      | 4 (35.4)        |                     |  |
| Terminal torque -                            | minimum                             |                                   |                                      | 3.4 (30)        |                     |  |
|  | maximum                             |                                   | 5 (4                                 |                 |                     |  |
| Case style                                   |                                     |                                   |                                      | HALF-PAK module |                     |  |

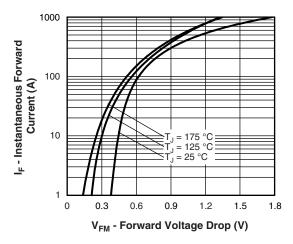
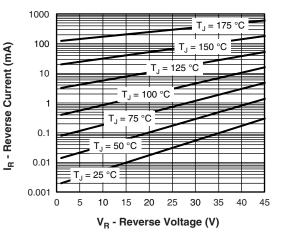
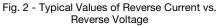


Fig. 1 - Maximum Forward Voltage Drop Characteristics





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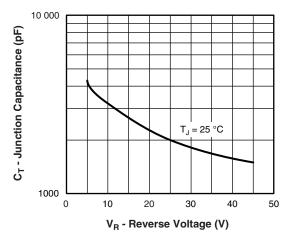


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

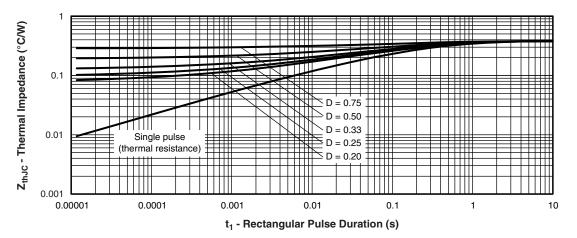
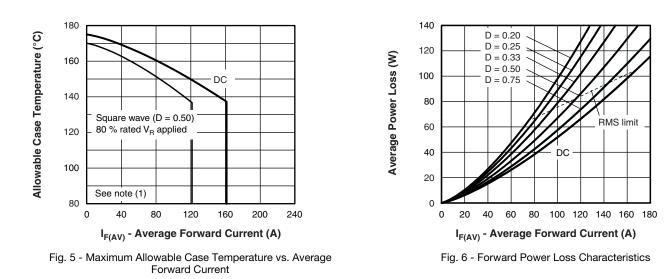


Fig. 4 - Maximum Thermal Impedance Z<sub>thJC</sub> Characteristics



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## VS-121NQ045PbF

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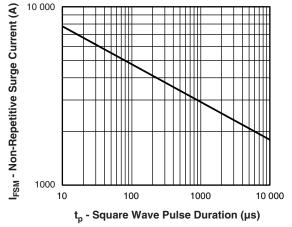


Fig. 7 - Maximum Non-Repetitive Surge Current

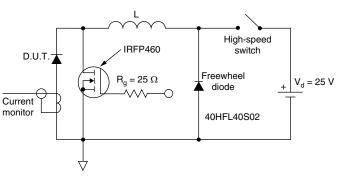


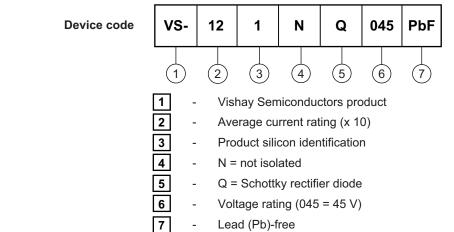
Fig. 8 - Unclamped Inductive Test Circuit

#### Note

<sup>(1)</sup> Formula used:  $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$ ; Pd = forward power loss =  $I_{F(AV)} \times V_{FM}$  at ( $I_{F(AV)}/D$ ) (see fig. 6);

 $Pd_{REV}$  = inverse power loss =  $V_{R1} \times I_R (1 - D)$ ;  $I_R$  at  $V_{R1}$  = rated  $V_R$ 

#### **ORDERING INFORMATION TABLE**



| LINKS TO RELATED DOCUMENTS          |  |  |  |
|-------------------------------------|--|--|--|
| Dimensions www.vishay.com/doc?95020 |  |  |  |
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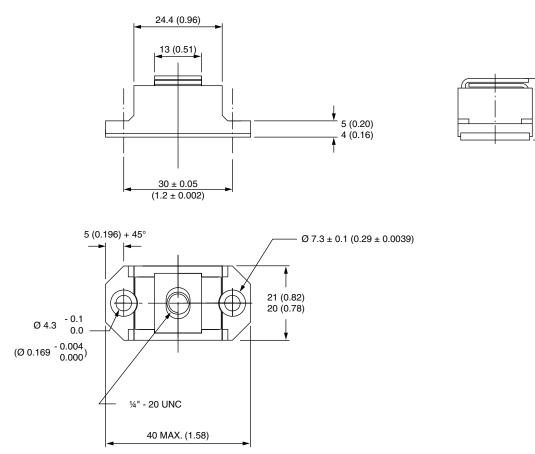
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17.5 (0.69) 16.5 (0.65)



## **DIMENSIONS** in millimeters (inches)

SHAY





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