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## LIMITED DATASHEET

Email [Analog.Switch@fairchildsemi.com](mailto:Analog.Switch@fairchildsemi.com)  
to request the full datasheet.

May 2014



# FSA9485 — USB Port Multimedia Switch Featuring Automatic Switching and Accessory Detection

## Features

- Detection:
  - Headsets with MIC and Remote
  - USB Data Cable
  - UART Serial Link
  - USB Chargers (Car, CDP, DCP)
  - Factory-Mode Cables
  - Teletype (TTY) Converter
  - USB OTG
  - CEA-936-A Car Kit
  - Video
- Audio, USB, UART, Video, Charging
- Full-Speed and High-Speed 2.0 Compliant
- Automatic Switching with Available Interrupt
- Integrated FET, Charger Detect, OCP (1.5 A), OVP (28 V), 7 V OVP
- Left, Right, MIC (Negative Swing)
- UART: Rx/D & Tx/D
- Video: Composite
- $V_{BAT}$ : 3 to 4.4 V
- Programmability: I<sup>2</sup>C
- 15 kV IEC 61000-4-2 Air Gap

## Description

The FSA9485 is a high-performance multimedia switch featuring automatic switching and accessory detection for the USB port. The FSA9485 allows the sharing of a common USB port to pass audio, video, USB data / charging / On-The-Go (OTG), as well as factory programmability.

In addition, the FSA9485 integrates detection for accessories such as headphones, headsets (MIC / Button), CEA-936-A Car Kit, USB chargers, and UART data cables to use a common USB connector. The FSA9485 can be programmed for manual switching or automatic switching of data paths based on the accessory detected.

With integrated 28 V over-voltage protection and 1.5 A over-current protected FET, the FSA9485 integrates common USB protection devices for  $V_{BUS}$ .

## Applications

- Mobile Phones & Portable Media Players

## Additional Information

- For additional performance information, please contact [analogswitch@fairchildsemi.com](mailto:analogswitch@fairchildsemi.com).

## Ordering Information

Part Number	Operating Temperature Range	Top Mark	Package
FSA9485UCX <sup>(1)</sup>	-40 to +85°C	N6	25-Lead, 2.1 x 2.1 x 0.625 mm WLCSP Package

### Note:

1. Includes backside lamination.

## Block Diagram

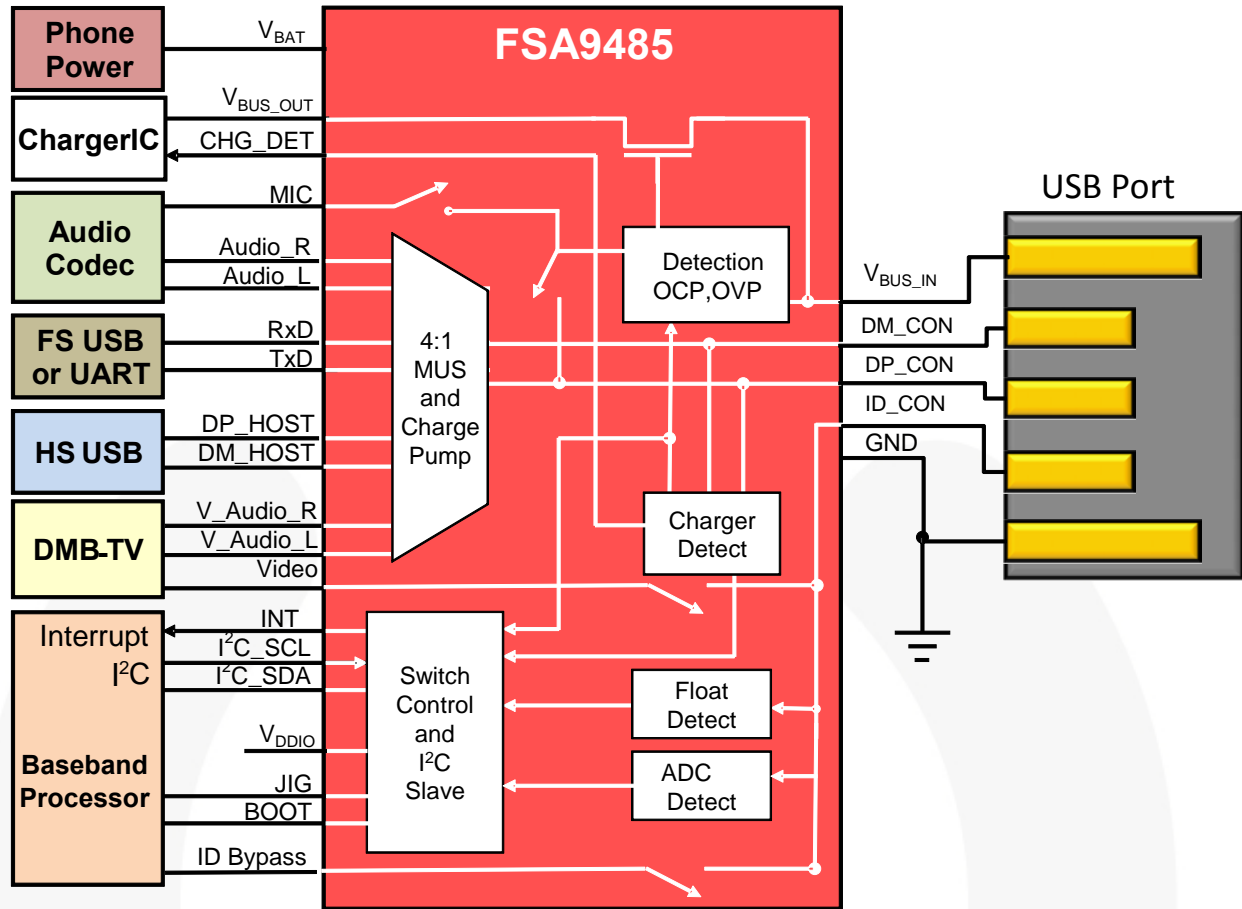


Figure 2. Block Diagram

## Pin Configuration

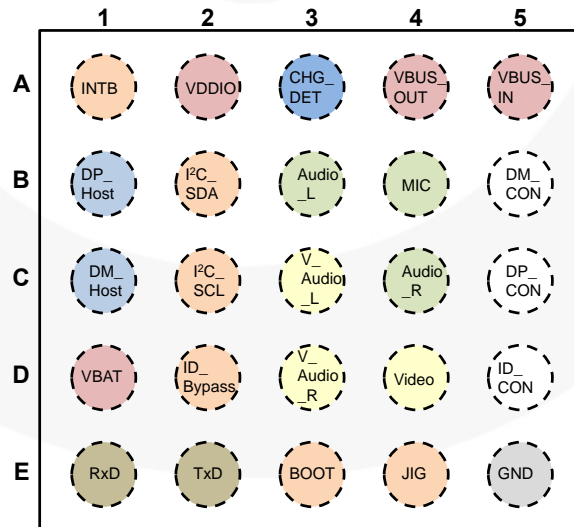
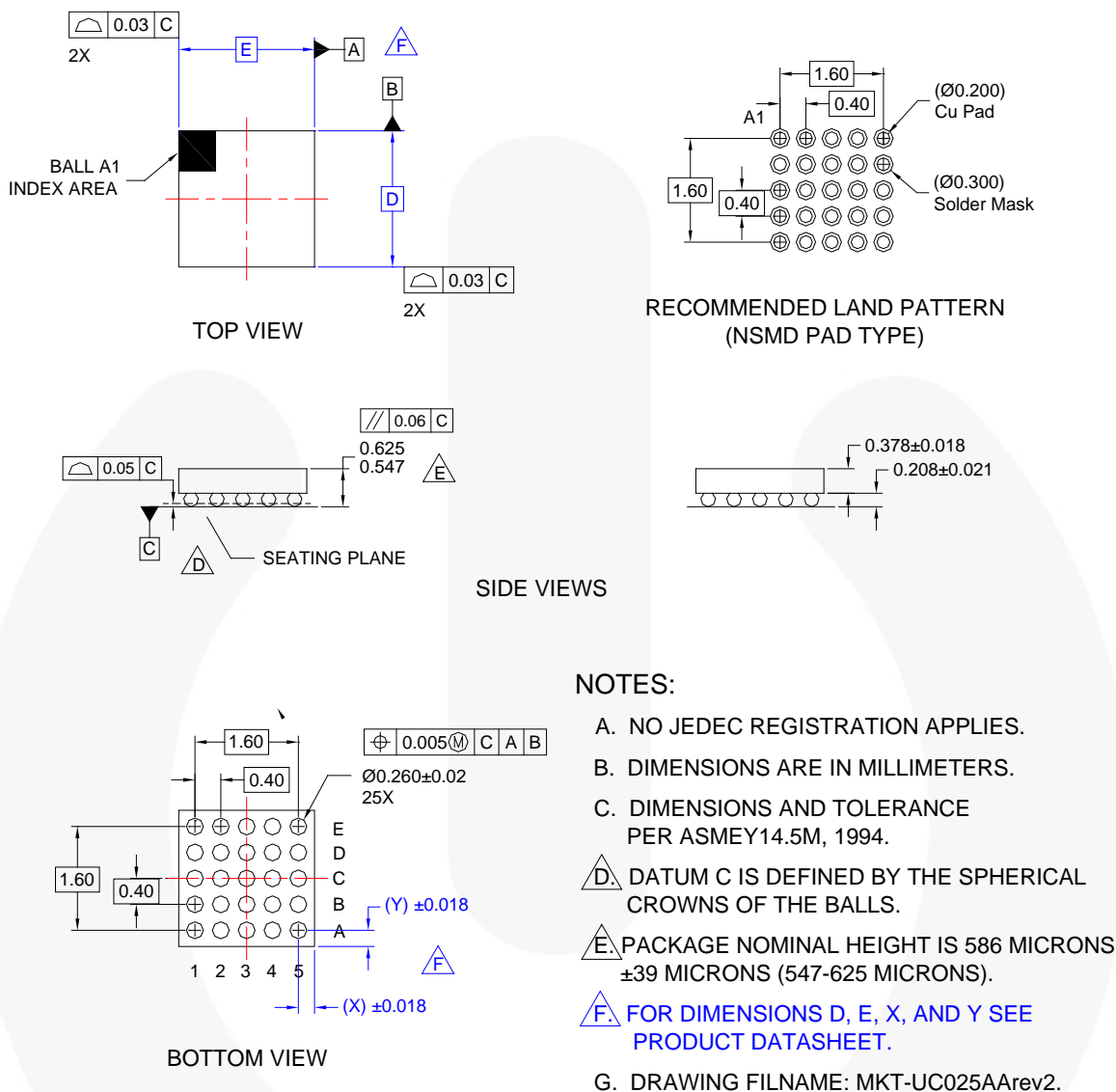


Figure 3. Pin Configuration (Top-Through View)

## Physical Dimensions



**Figure 25. 25-Lead, Wafer-Level Chip-Scale Package (WLCSP)**

## Product-Specific Dimensions

Product	D	E	X	Y
FSA9485UCX	2.10	2.10	0.25	0.25

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