

RoHS

COMPLIANT

INT-A-PAK Half Bridge IGBT (Standard Speed IGBT), 200 A



INT-A-PAK

PRIMARY CHARACTERISTICS				
V _{CES}	600 V			
I _C DC	480 A			
V _{CE(on)} at 200 A, 25 °C	1.13 V			
Speed	DC to 1 kHz			
Package	INT-A-PAK			
Circuit configuration	Half bridge			

FEATURES

- Gen 4 IGBT technology
- Standard: optimized for hard switching speed
- Very low conduction losses
- Industry standard package
- UL approved file E78996
- · Designed and qualified for industrial level
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

BENEFITS

- · Increased operating efficiency
- Direct mounting to heatsink
- Performance optimized as output inverter stage for TIG welding machines

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS	
Collector to emitter voltage	V _{CES}		600	V	
Continuous collector current		T _C = 25 °C	480		
Continuous collector current	I _C	T _C = 116 °C	200	_	
Pulsed collector current	I _{CM}		800	А	
Peak switching current	I _{LM}		800		
Gate to emitter voltage	V_{GE}		± 20	V	
RMS isolation voltage	V _{ISOL}	Any terminal to case, t = 1 min	2500	V	
Maximum power dissipation	P _D	T _C = 25 °C	830	w	
		T _C = 85 °C	430		
Operating junction temperature range	TJ		-40 to +150	°C	
Storage temperature range	T _{Stg}		-40 to +125	1	

ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise specified)						
Collector to emitter breakdown voltage	V _{BR(CES)}	$V_{GE} = 0 \text{ V}, I_{C} = 1 \text{ mA}$	600	-	-	
Collector to emitter voltage	V _{CE(on)}	V _{GE} = 15 V, I _C = 200 A	-	1.13	1.21	V
		V _{GE} = 15 V, I _C = 200 A, T _J = 125 °C	-	1.08	1.18	V
Gate threshold voltage	$V_{GE(th)}$	$I_C = 0.25 \text{ mA}$	3	4.5	6	
Collector to emitter leakage current	I _{CES}	V _{GE} = 0 V, V _{CE} = 600 V	-	0.025	1	mA
		V _{GE} = 0 V, V _{CE} = 600 V, T _J = 125 °C	-	-	10	IIIA
Gate to emitter leakage current	I _{GES}	V _{GE} = ± 20 V	-	-	± 250	nA



SWITCHING CHARACTERISTICS (T _J = 25 °C unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Total gate charge	Qg	I _C = 200 A	-	1600	1700	
Gate to emitter charge	Q_{ge}	V _{CC} = 400 V	=	260	340	nC
Gate to collector charge	Q _{gc}	V _{GE} = 15 V	=	580	670	
Turn-on switching loss	E _{on}	I _C = 200 A, V _{CC} = 480 V, V _{GF} = 15 V	-	30	-	
Turn-off switching loss	E _{off}	$R_g = 10 \Omega$	=	50	-	mJ
Total switching loss	E _{ts}	Freewheeling diode: 30EPH06, T _J = 25 °C	-	80	-	
Turn-on switching loss	E _{on}	I _C = 200 A, V _{CC} = 480 V, V _{GE} = 15 V	-	34	-	
Turn-off switching loss	E _{off}	$R_g = 10 \Omega$	=	104	-	mJ
Total switching loss	E _{ts}	Freewheeling diode: 30EPH06, T _J = 125 °C	=	138	151	
Input capacitance	C _{ies}	V _{GF} = 0 V	-	32 500	-	
Output capacitance	C _{oes}	$V_{CC} = 30 \text{ V}$	-	2080	-	pF
Reverse transfer capacitance	C _{res}	f = 1.0 MHz	-	380	-	

THERMAL AND MECHANICAL SPECIFICATIONS						
PARAMETER		SYMBOL	MIN.	TYP.	MAX.	UNITS
Operating junction temperature ra	T _J	-40	-	150	• °C	
Storage temperature range	T _{Stg}	-40	-	125		
Junction to case per leg		R _{thJC}	-		0.15	°C/W
Case to sink		R _{thCS}	-	0.1	-	C/VV
Mounting torque	case to heatsink		-	-	4	Nm
	case to terminal 1, 2, 3		-		3	INITI
Weight			-	185	-	g

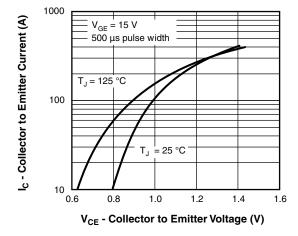


Fig. 1 - Typical Output Characteristics

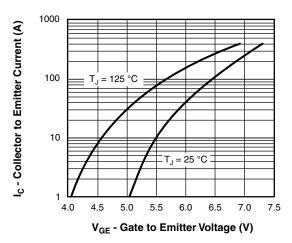


Fig. 2 - Typical Transfer Characteristics

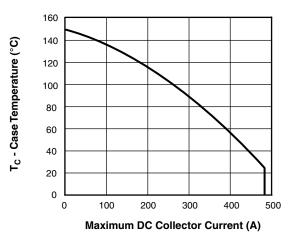


Fig. 3 - Case Temperature vs. Maximum Collector Current

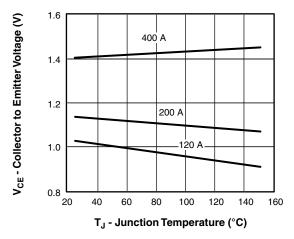


Fig. 4 - Typical Collector to Emitter Voltage vs.
Junction Temperature

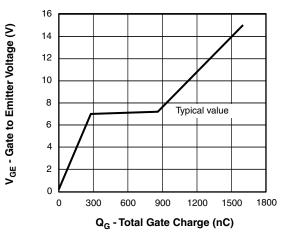


Fig. 5 - Typical Gate Charge vs. Gate to Emitter Voltage

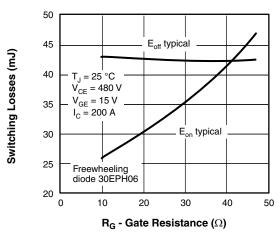


Fig. 6 - Typical Switching Losses vs. Gate Resistance

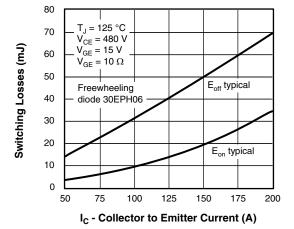
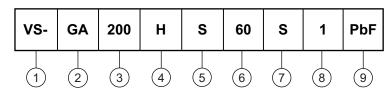


Fig. 7 - Typical Switching Losses vs. Collector to Emitter Current



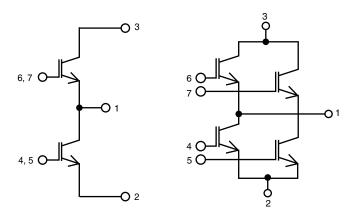
ORDERING INFORMATION TABLE

Device code



- 1 Vishay Semiconductors product
- 2 Essential part number IGBT modules
- 3 Current rating (200 = 200 A)
- Circuit configuration (H = half bridge without f/w diode)
- 5 INT-A-PAK
- Voltage code (60 = 600 V)
- 7 Speed/type (S = standard speed IGBT)
- 8 Assy location Italy
- 9 None = standard production; PbF = lead (Pb)-free

CIRCUIT CONFIGURATION



Functional Diagram

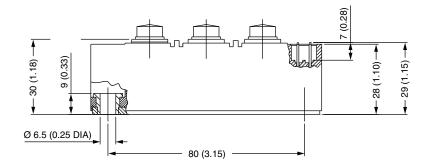
Electrical Diagram

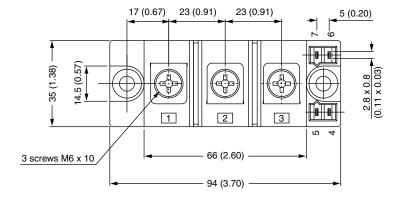
LINKS TO RELATED DOCUMENTS					
Dimensions	www.vishay.com/doc?95173				

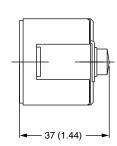


INT-A-PAK IGBT/Thyristor

DIMENSIONS in millimeters (inches)



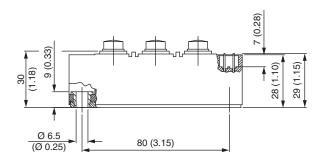


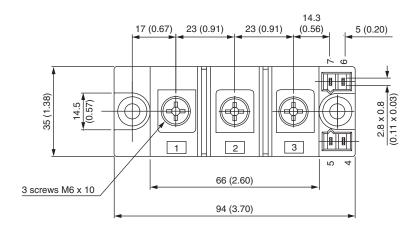


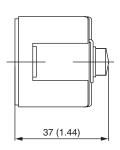


INT-A-PAK IGBT

DIMENSIONS in millimeters (inches)









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