VS-GB150TH120N

Vishay Semiconductors



Molding Type Module IGBT, 2-in-1 Package, 1200 V and 150 A



PRIMARY CHARACTERISTICS				
V _{CES} 1200 V				
I _C at T _C = 80 °C	150 A			
V _{CE(on)} (typical) at I _C = 150 A, 25 °C	1.9 V			
Speed 8 kHz to 30 kHz				
Package Dual INT-A-PAK				
Circuit configuration Half bridge				

FEATURES

- Low V_{CE(on)} SPT + IGBT technology
- 10 µs short circuit capability
- V_{CE(on)} with positive temperature coefficient
- Maximum junction temperature 150 °C
- Low inductance case
- Fast and soft reverse recovery antiparallel FWD
- Isolated copper baseplate using DCB (Direct Copper Bonding) technology
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

TYPICAL APPLICATIONS

- Inverter for motor drive
- AC and DC servo drive amplifier
- Uninterruptible power supply (UPS)

DESCRIPTION

Vishay's IGBT power module provides ultra low conduction loss as well as short circuit ruggedness. It is designed for applications such as general inverters and UPS.

ABSOLUTE MAXIMUM RATINGS (T _C = 25 °C unless otherwise noted)				
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS
Collector to emitter voltage	V _{CES}		1200	V
Gate to emitter voltage	V _{GES}		± 20	v
Collector current I _C	T _C = 25 °C	300		
	IC	T _C = 80 °C	150	
Pulsed collector current	I _{CM} ⁽¹⁾	t _p = 1 ms	300	А
Diode continuous forward current	١ _F	T _C = 80 °C	150	
Diode maximum forward current	I _{FM}	t _p = 1 ms	300	
Maximum power dissipation	PD	$T_J = 150 \ ^\circ C$	1008	W
Short circuit withstand time	t _{SC}	T _J = 125 °C	10	μs
RMS isolation voltage	V _{ISOL}	f = 50 Hz, t = 1 min	2500	V

Note

⁽¹⁾ Repetitive rating: pulse width limited by maximum junction temperature

IGBT ELECTRICAL SPECIFICATIONS ($T_c = 25$ °C unless otherwise noted)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Collector to emitter breakdown voltage	V _{(BR)CES}	T _J = 25 °C	1200	-	-	
Collector to emitter voltage	V	V_{GE} = 15 V, I_{C} = 150 A, T_{J} = 25 °C	-	1.90	2.35	v
Collector to enlitter voltage	V _{CE(on)}	V_{GE} = 15 V, I_{C} = 150 A, T_{J} = 125 °C	-	2.10	-	v
Gate to emitter threshold voltage	V _{GE(th)}	$V_{CE} = V_{GE}$, $I_C = 6$ mA, $T_J = 25 \text{ °C}$	5.0	6.2	7.0	
Collector cut-off current	I _{CES}	$V_{CE} = V_{CES}, V_{GE} = 0 \text{ V}, \text{ T}_{J} = 25 \text{ °C}$	-	-	5.0	mA
Gate to emitter leakage current	I _{GES}	$V_{GE} = V_{GES}$, $V_{CE} = 0$ V, $T_J = 25$ °C	-	-	400	nA

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COMPLIANT



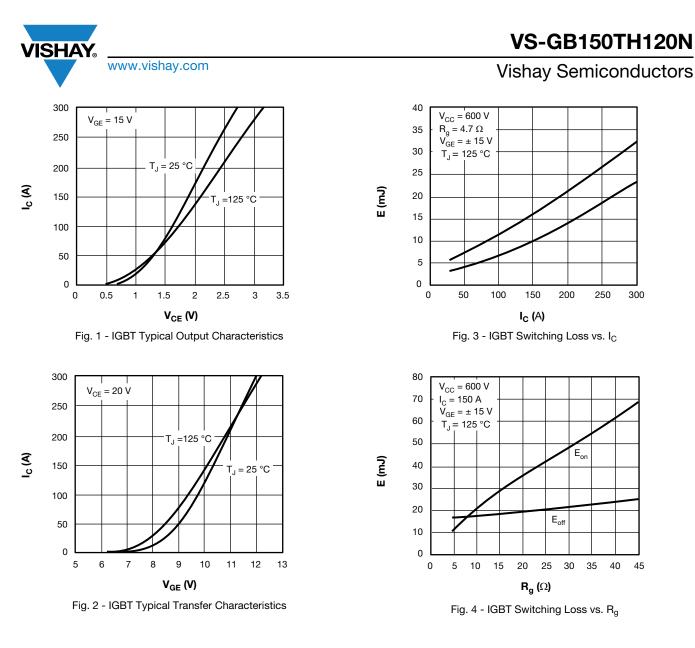


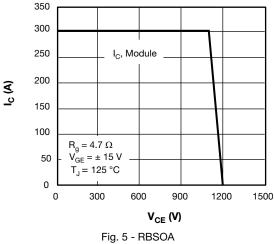
SWITCHING CHARACTERISTICS	S					
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Turn-on delay time	t _{d(on)}		-	336	-	
Rise time	t _r		-	75	-	
Turn-off delay time	t _{d(off)}	$V_{CC} = 600 \text{ V}, \text{ I}_{C} = 150 \text{ A}, \text{ R}_{g} = 4.7 \Omega,$	-	346	-	ns
Fall time	t _f	V _{GE} = ± 15 V, T _J = 25 °C	-	182	-	
Turn-on switching loss	E _{on}		-	7.25	-	
Turn-off switching loss	E _{off}		-	9.30	-	- mJ
Turn-on delay time	t _{d(on)}		-	346	-	ns
Rise time	t _r		-	77	-	
Turn-off delay time	t _{d(off)}	$V_{CC} = 600 \text{ V}, \text{ I}_{C} = 150 \text{ A}, \text{ R}_{g} = 4.7 \Omega,$	-	389	-	
Fall time	t _f	V _{GE} = ± 15 V, T _J = 125 °C	-	322	-	
Turn-on switching loss	E _{on}		-	9.95	-	ml
Turn-off switching loss	E _{off}		-	16.0	-	mJ
Input capacitance	Cies		-	11.0	-	
Output capacitance	C _{oes}	$V_{GE} = 0 \text{ V}, \text{ V}_{CE} = 25 \text{ V}, \text{ f} = 1.0 \text{ MHz}$	-	0.80	-	nF
Reverse transfer capacitance	C _{res}		-	0.52	-	
SC data	I _{SC}	$ \begin{split} t_{sc} &\leq 10 \; \mu s, V_{GE} = 15 \; V, T_J = 125 \; ^{\circ}C, \\ V_{CC} &= 900 \; V, V_{CEM} \leq 1200 \; V \end{split} $	-	890	-	A
Internal gate resistance	R _{GINT}		-	1.5	-	Ω
Stray inductance	L _{CE}		-	-	20	nH
Module lead resistance, terminal to chip	R _{CC'+EE'}	T _C = 25 °C	-	0.35	-	mΩ

DIODE ELECTRICAL SPECIFICATIONS ($T_c = 25$ °C unless otherwise noted)								
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS	
Diode forward voltage	V _F	I _F = 150 A	T _J = 25 °C	-	1.80	2.20	v	
Didde forward voltage			T _J = 125 °C	-	1.85	-		
Diada rayarga ragayany aharga	Q _{rr}		T _J = 25 °C	-	16.2	-	μC	
Diode reverse recovery charge			T _J = 125 °C	-	26.6	-	μΟ	
Diada paak rayaraa raaayany ayrrant	I _{rr}	$I_F = 150 \text{ A}, \text{ V}_R = 600 \text{ V},$ I_{rr} $dI/dt = -2360 \text{ A}/\mu\text{s},$ $V_{GF} = -15 \text{ V}$		T _J = 25 °C	-	138	-	А
Diode peak reverse recovery current			T _J = 125 °C	-	166	-	A	
	E _{rec}		T _J = 25 °C	-	7.48	-	ml	
Diode reverse recovery energy			T _J = 125 °C	-	13.4	-	mJ	

THERMAL AND MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Operating junction temperature range	TJ		-	-	150	°C
Storage temperature range	T _{STG}		-40	-	125	
Junction-to-case	В		-	-	0.124	
Diode	- R _{thJC}		-	-	0.174	K/W
Case to sink	R _{thCS}	Conductive grease applied	-	0.035	-	
		Power terminal screw: M6		2.5 to 5.0		Nima
Mounting torque		Mounting screw: M6		3.0 to 5.0	C	Nm
Weight				300		g

Revision: 22-Sep-17 2 For technical questions within your region: DiodesAmericas@vishay.com, DiodesAsia@vishay.com, DiodesEurope@vishay.com





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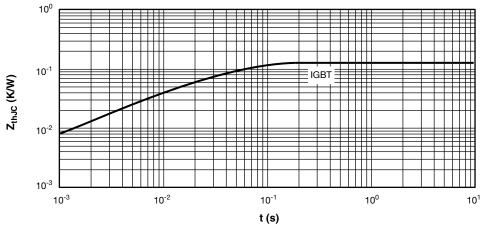
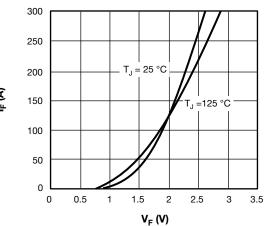


Fig. 6 - IGBT Transient Thermal Impedance



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Fig. 7 - Diode Typical Forward Characteristics

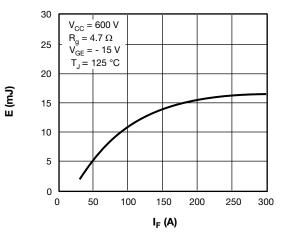


Fig. 8 - Diode Switching Loss vs. ${\sf I}_{\sf F}$

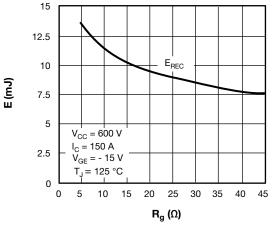


Fig. 9 - Diode Switching Loss vs.Rg

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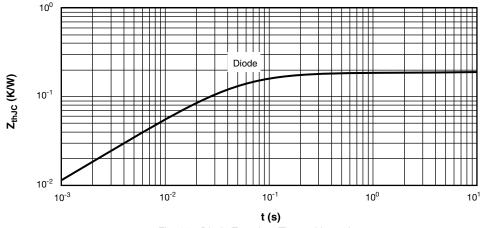
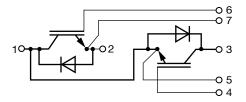


Fig. 10 - Diode Transient Thermal Impedance

CIRCUIT CONFIGURATION

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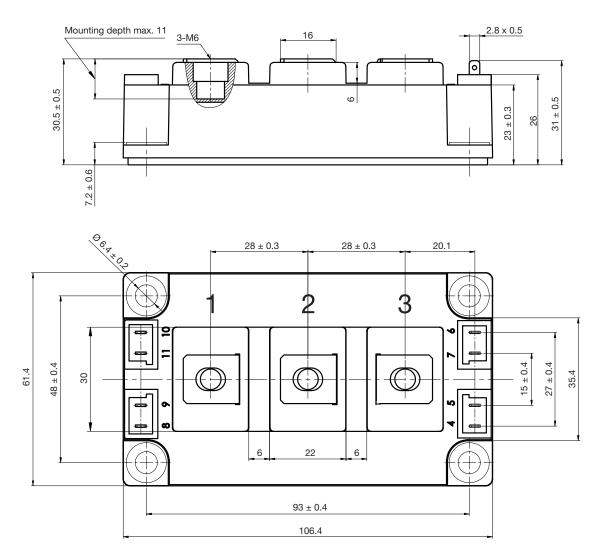


LINKS TO RELATED DOCUMENTS			
Dimensions	www.vishay.com/doc?95525		



Double INT-A-PAK

DIMENSIONS in millimeters (inches)





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