Vishay Semiconductors

Half Bridge IGBT Power Module, 600 V, 100 A



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INT-A-PAK

PRIMARY CHARACTERISTICS					
V _{CES} 600 V					
I _C at T _C = 80 °C	100 A				
V _{CE(on)} (typical) at I _C = 100 A, 25 °C	1.65 V				
Speed 8 kHz to 30 kHz					
Package INT-A-PAK					
Circuit configuration	Half bridge				

FEATURES

- Low V_{CE(on)} trench IGBT technology
- 5 µs short circuit capability
- V_{CE(on)} with positive temperature coefficient
- Maximum junction temperature 175 °C
- Low inductance case
- · Fast and soft reverse recovery antiparallel FWD
- · Isolated copper baseplate using DCB (direct copper bonding) technology
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

TYPICAL APPLICATIONS

- UPS (uninterruptable power supply)
- · Switching mode power supplies
- · Electronic welders

DESCRIPTION

Vishay's IGBT power module provides ultra low conduction loss as well as short circuit ruggedness. It is designed for applications such as UPS and SMPS.

ABSOLUTE MAXIMUM RATINGS (T _C = 25 °C unless otherwise noted)					
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS	
Collector to emitter voltage	V _{CES}		600	V	
Gate to emitter voltage	V _{GES}		± 20	v	
Collector ourrent		T _C = 25 °C	160		
Collector current I _C	T _C = 80 °C	100			
Pulsed collector current	I _{CM} ⁽¹⁾	t _p = 1 ms	200	А	
Diode continuous forward current	١ _F	T _C = 80 °C	100		
Diode maximum forward current	I _{FM} ⁽¹⁾	t _p = 1 ms	200		
Maximum power dissipation	PD	T _J = 175 °C	417	W	
Short circuit withstand time	t _{SC}	T _C = 125 °C	5	μs	
RMS isolation voltage	V _{ISOL}	f = 50 Hz, t = 1 min	4000	V	

Note

⁽¹⁾ Repetitive rating: pulse width limited by maximum junction temperature

IGBT ELECTRICAL SPECIFICATIONS ($T_c = 25 \ ^{\circ}C$ unless otherwise noted)						
PARAMETER	SYMBOL	SYMBOL TEST CONDITIONS		TYP.	MAX.	UNITS
Collector to emitter breakdown voltage	V _{(BR)CES}	T _J = 25 °C	600	-	-	
	V	V_{GE} = 15 V, I_{C} = 100 A, T_{J} = 25 °C	-	1.65	2.10	v
Collector to emitter voltage	V _{CE(on)}	V_{GE} = 15 V, I_{C} = 100 A, T_{J} = 175 °C	-	2.00	-	v
Gate to emitter threshold voltage	V _{GE(th)}	V_{CE} = V_{GE} , I_C = 1.0 mA, T_J = 25 °C	4.0	4.4	6.5	
Collector cut-off current	I _{CES}	$V_{CE} = V_{CES}, V_{GE} = 0 \text{ V}, \text{ T}_{J} = 25 ^{\circ}\text{C}$	-	-	5.0	mA
Gate to emitter leakage current	I _{GES}	$V_{GE} = V_{GES}, V_{CE} = 0 \text{ V}, \text{ T}_{J} = 25 ^{\circ}\text{C}$	-	-	400	nA

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COMPLIANT

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SWITCHING CHARACTERISTICS	5					
PARAMETER	R SYMBOL TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
Turn-on delay time	t _{d(on)}		-	106	-	ns mJ
Rise time	t _r		-	49	-	
Turn-off delay time	t _{d(off)}	$V_{CC} = 300 \text{ V}, \text{ I}_{C} = 100 \text{ A}, \text{ R}_{g} = 2.2 \Omega,$	-	102	-	
Fall time	t _f	$V_{GE} = \pm 15 \text{ V}, \text{ T}_{J} = 25 \text{ °C}$	-	85	-	
Turn-on switching loss	E _{on}		-	0.46	-	
Turn-off switching loss	E _{off}		-	0.95	-	
Turn-on delay time	t _{d(on)}		-	112	-	
Rise time	t _r		-	62	-	ns
Turn-off delay time	t _{d(off)}	V_{CC} = 300 V, I _C = 100 A, R _g = 2.2 Ω, V _{GE} = ± 15 V, T _J = 125 °C	-	126	-	
Fall time	t _f		-	109	-	
Turn-on switching loss	Eon		-	0.78	-	mJ
Turn-off switching loss	E _{off}		-	1.73	-	1115
Input capacitance	Cies		-	7.71	-	
Output capacitance	C _{oes}	V _{GE} = 0 V, V _{CE} = 30 V, f = 1.0 MHz	-	0.53	-	nF
Reverse transfer capacitance	C _{res}		-	0.23	-	
SC data	I _{SC}	$\begin{array}{l} t_p \leq 5 \; \mu s, \; V_{GE} = 15 \; V, \; T_J = 125 \; ^{\circ}C, \\ V_{CC} = 360 \; V, \; V_{CEM} \leq 1200 \; V \end{array}$	-	900	-	А
Stray inductance	L _{CE}		-	-	30	nH
Module lead resistance, terminal to chip	R _{CC'+EE'}		-	0.75	-	mΩ

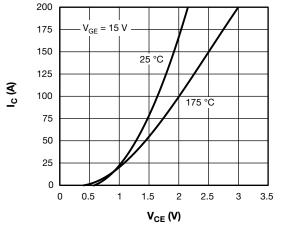
DIODE ELECTRICAL SPECIFICATIONS ($T_c = 25 \text{ °C}$ unless otherwise noted)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
Forward voltage	VF	I _F = 100 A	T _J = 25 °C	-	1.40	1.80	V
Torward voltage	٧F		T _J = 125 °C	-	1.40	-	
	Q _{rr}		T _J = 25 °C	-	5.5	-	
Reverse recovery charge			T _J = 125 °C	-	7.3	-	μC
Poole rougeno rocoveru ourrent	I _{rr}	$I_F = 100 \text{ A}, V_R = 600 \text{ V},$	T _J = 25 °C	-	68	-	А
Peak reverse recovery current		$R_{G} = 5.6 \Omega$ $V_{GE} = -15 V$	T _J = 125 °C	-	88	-	A
	E		T _J = 25 °C	-	0.89	-	ml
Reverse recovery energy	E _{rec}		T _J = 125 °C	-	1.71	-	mJ

THERMAL AND MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Maximum junction temperature	TJ		-	-	175	- °C
Storage temperature range	T _{Stg}		-40	-	125	
Junction to case	Б		-	-	0.36	K/W
Diode	– R _{thJC}		-	-	0.57	
Case to sink (conductive grease applied)	R _{thCS}		-	0.05	-	
Mounting torque		Power terminal screw: M5		2.5 to 5.0		Nime
		Mounting screw: M6		3.0 to 5.0		Nm
Weight			-	150	-	g

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Fig. 1 - IGBT Typical Output Characteristics

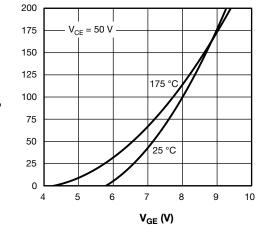


Fig. 2 - IGBT Transfer Characteristics

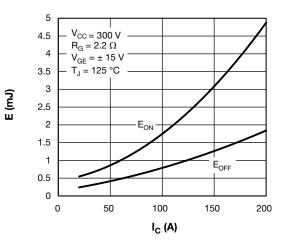


Fig. 3 - IGBT Switching Loss vs. I_C

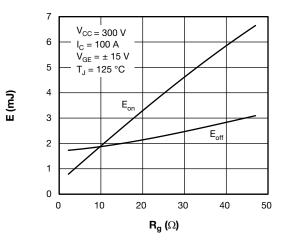


Fig. 4 - IGBT Switching Loss vs. R_G

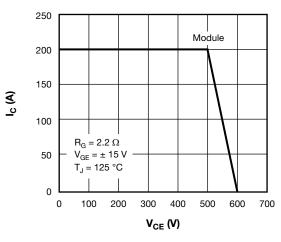


Fig. 5 - RBSOA

I_c (A)

ISHAY

VS-GT100TP60N

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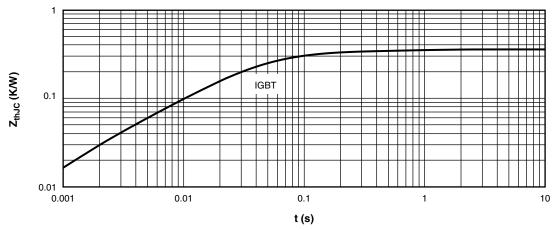


Fig. 6 - IGBT Transient Thermal Impedance

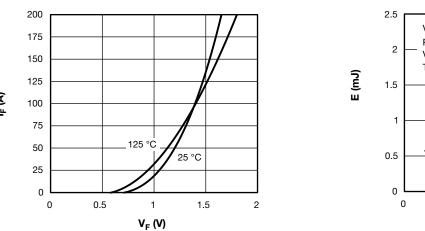
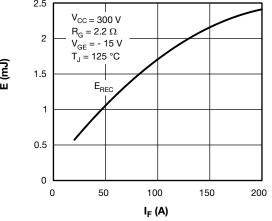
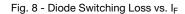


Fig. 7 - Diode Forward Characteristics





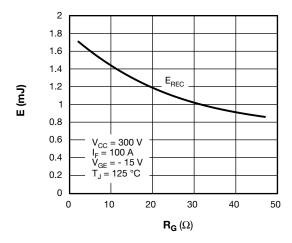


Fig. 9 - Diode Switching Loss vs. R_G

I_F (A)

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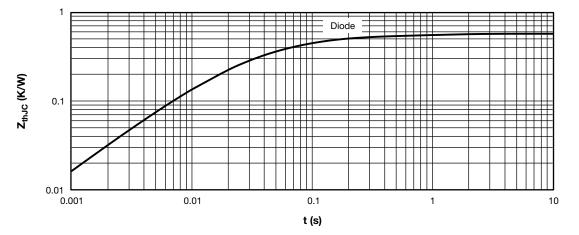
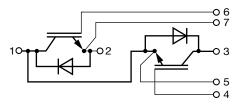


Fig. 10 - Forward Characteristics of Diode

CIRCUIT CONFIGURATION



LINKS TO RELATED DOCUMENTS				
Dimensions	www.vishay.com/doc?95524			

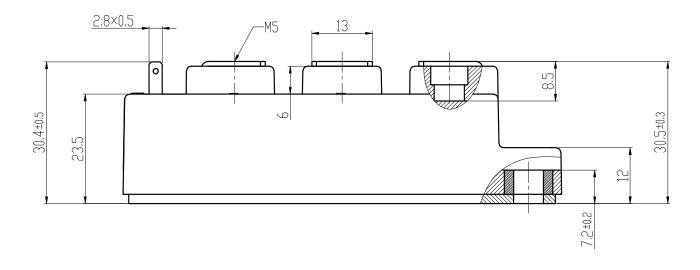
Outline Dimensions

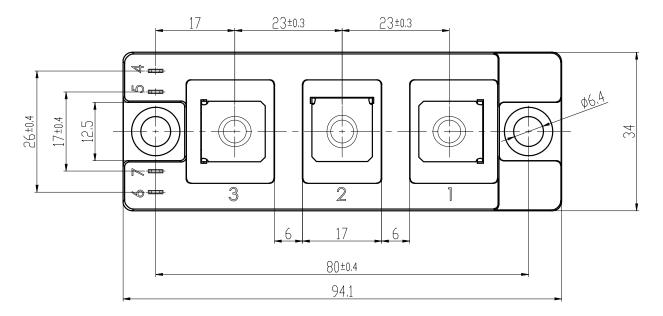


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DIMENSIONS in millimeters (inches)







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