Revision. 2

MOS FET

#### MTM763250LBF

## **Panasonic**

## MTM763250LBF

Silicon N-channel MOSFET (FET1) Silicon P-channel MOSFET (FET2)

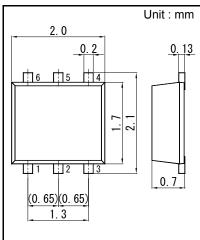
For Switching For DC-DC Converter

#### ■ Features

- Low Drain-source On-state Resistance : RDS(on)typ. N-ch = 95 m $\Omega$ (VGS = 4.0 V) P-ch:300 m $\Omega$  (VGS = -4.0 V)
- Halogen-free / RoHS compliant (EU RoHS / UL-94 V-0 / MSL : Level 1 compliant)
- Marking Symbol : DE
- Basic Part Number Nch+Pch MOS 20V (Individual)

#### ■ Packaging

Embossed type (Thermo-compression sealing) 3 000 pcs / reel (standard)



- 1. Source(FET1) 4. Source(FET2)
- 2. Gate(FET1) 5. Gate(FET2)
- 3. Drain(FET2) 6. Drain(FET1)

Panasonic	WSMini6-F1-B
JEITA	SC-113DA
Code	

#### ■ Absolute Maximum Ratings Ta = 25 °C

Parameter		Symbol	Rating	Unit
(N-ch.)	Drain-source Voltage	VDS	20	V
	Gate-source Voltage	VGS	±10	V
	Drain current *2	ID	1.7	Α
	Peak drain current *1,*2	IDp	6.8	Α
(P-ch.)	Drain-source Voltage	VDS	-20	V
	Gate-source Voltage	VGS	±12	V
	Drain current *2	ID	-1.0	Α
	Peak drain current *1,*2	IDp	-4.0	Α
	Total nower dissination *2	PD	700	mW
	Channel temperature	Tch	150	°C
	Operating ambient temperature	Topr	-40 to +85	°C
	Storage Temperature Range	Tstg	-55 to +150	°C

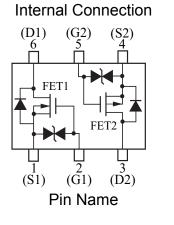
Note:

Established: 2011-11-09

: 2013-10-21

Revised

- \*1 t = 10  $\mu$ s. Duty cycle  $\leq$  1 %.
- \*2 Measuring on ceramic substrate at 40 mm  $\cdot$  38 mm  $\cdot$  0.2 mm. PD absolute maximum rating Non-heat sink: 150 mW.



- 1. Source(FET1) 4. Source(FET2)
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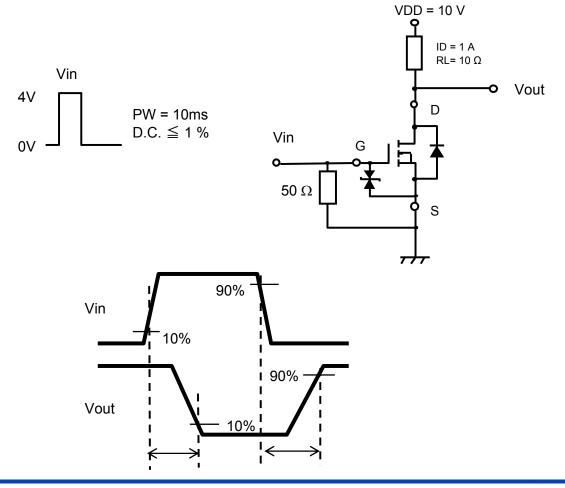
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#### ■ Electrical Characteristics Ta = 25 $^{\circ}$ C $\pm$ 3 $^{\circ}$ C

FET1 (N-ch.) Parameter Symbol Conditions Min Typ Max Unit Drain-source Breakdown Voltage VDSS ID = 1.0 mA, VGS = 0 V20 ٧ VDS = 20 V, VGS = 0 V Zero Gate Voltage Drain Current IDSS 1.0 μΑ Gate-source Leakage Current **IGSS**  $VGS = \pm 8.0 \text{ V}, VDS = 0 \text{ V}$ ±10 μA Gate-source Threshold Voltage Vth ID = 1.0 mA, VDS = 10 V 0.4 ٧ 0.85 1.3 ID = 1.0 A, VGS = 4.0 V RDS(ON)1 95 120 Drain-source ON resistance \*1  $\text{m}\Omega$ RDS(ON)2 ID = 0.5 A, VGS = 2.5 V 115 170 ID = 1.0 A, VDS = 10 V 3.0 |Yfs| Forward transfer admittance S Input Capacitance Ciss 280 **Output Capacitance** Coss VDS = 10 V, VGS = 0, f = 1 MHz 18 pF Reverse Transfer Capacitance Crss 17 VDD = 10 V, VGS = 0 to 4 V, Turn-on time \*2 12 ton ID = 1.0 A ns VDD = 10 V, VGS = 4 to 0 V, Turn-off time \*2 toff 50 ID = 1.0 A

Note: 1. Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7030 Measuring methods for transistors.

- 2. \*1 Pulse measurement
  - \*2 Measurement circuit for Turn-on Time / Turn-off Time



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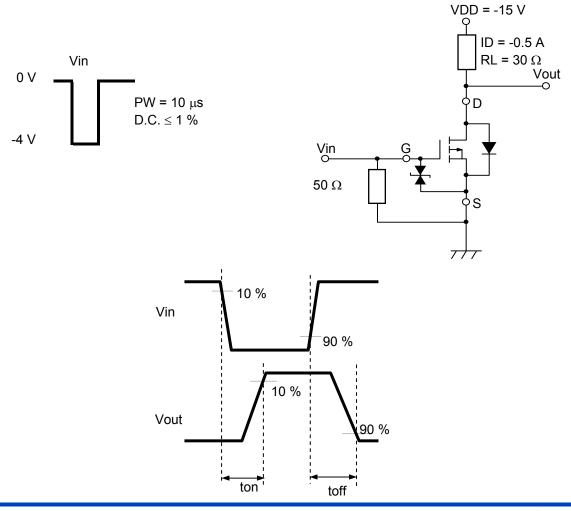
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#### FET2 (P-ch.)

Parameter	Symbol	Conditions	Min	Тур	Max	Unit	
Drain-source Breakdown Voltage	VDSS	ID = -1.0 mA, VGS = 0 V	-20			V	
Zero Gate Voltage Drain Current	IDSS	VDS = -20 V, VGS = 0 V			-1.0	μΑ	
Gate-source Leakage Current	IGSS	VGS = $\pm 10$ V, VDS = 0 V			±10	μΑ	
Gate-source Threshold Voltage	Vth	ID = -1.0 mA, VDS = -10 V	-0.45	-1.0	-1.5	V	
Drain-source On-state Resistance *1	RDS(ON)1	ID = -0.5 A, VGS = -4.0 V		300	420	mΩ	
	RDS(ON)2	ID = -0.5 A, VGS = -2.5 V		420	560		
Forward transfer admittance *1	Yfs	ID = -0.5 A, VDS = -10 V	1.0	2.0		S	
Input Capacitance	Ciss			80		pF	
Output Capacitance	Coss	VDS = -10 V, VGS = 0, f = 1 MHz		12			
Reverse Transfer Capacitance	Crss			12			
Turn-on Time *2	ton	VDD = -15 V, VGS = 0 to -4 V	18				
		ID = -0.5 A		10		ne	
Turn-off Time <sup>*2</sup>	toff	VDD = -15 V, VGS = -4 to 0 V		27		ns	
		ID = -0.5 A		21			

Note: 1. Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7030 Measuring methods for transistors.

- 2. \*1 Pulse measurement
  - \*2 Measurement circuit for Turn-on Time / Turn-off Time

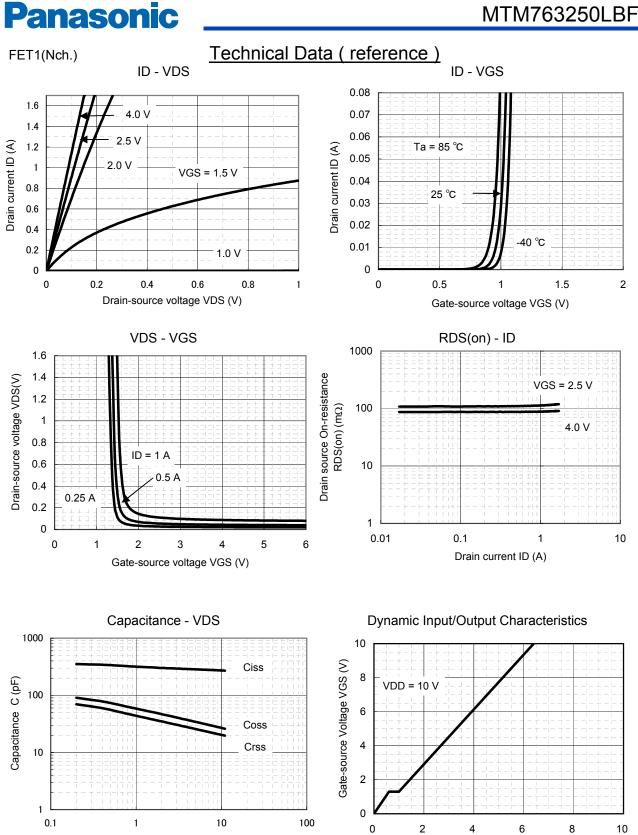


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Total Gate Charge Qg (nC)

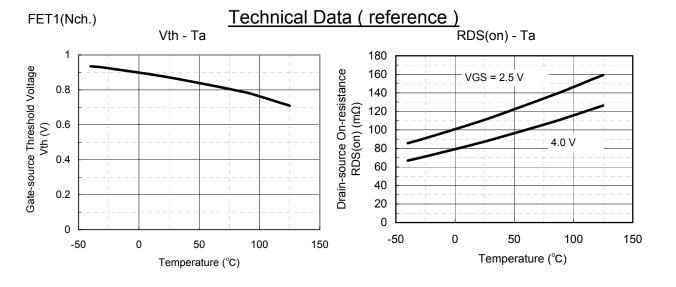
Established: 2011-11-09

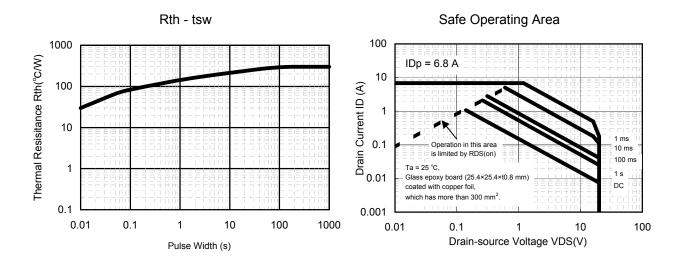
Drain-source voltage (V)

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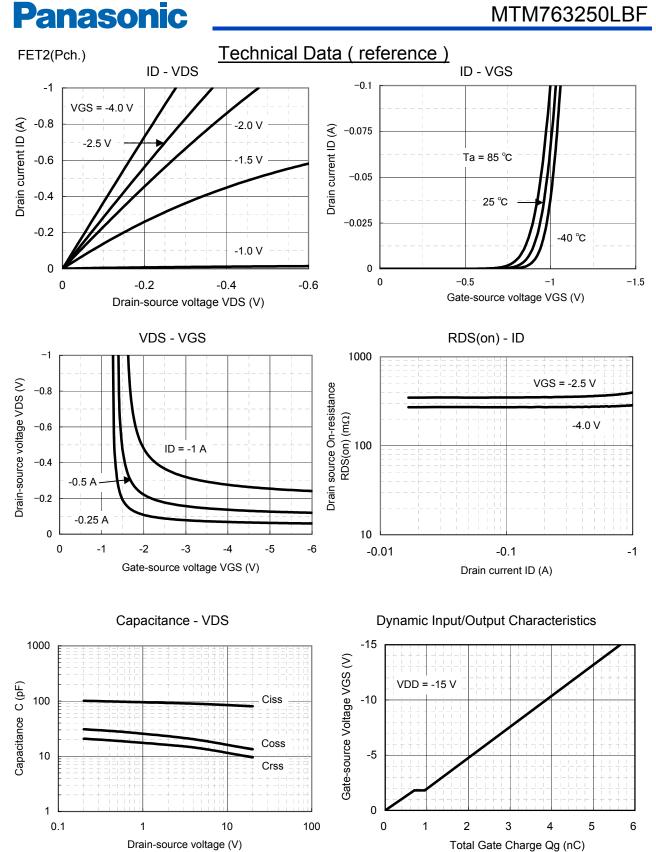
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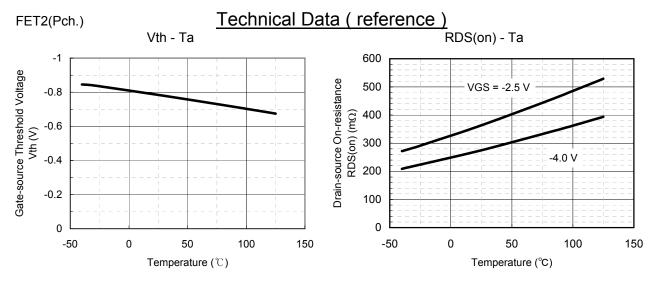
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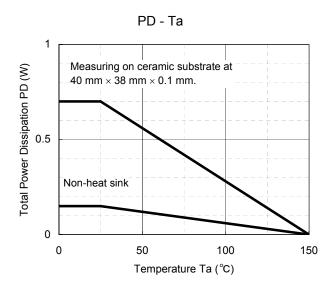


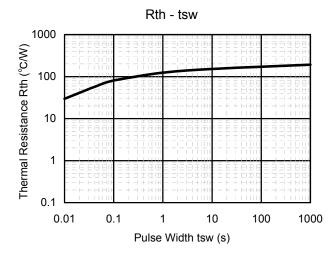
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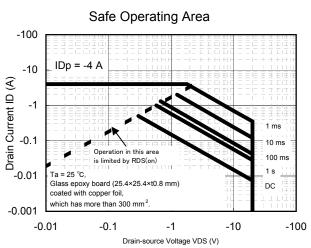
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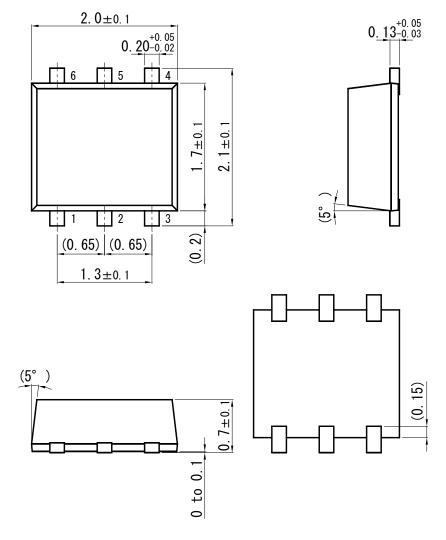


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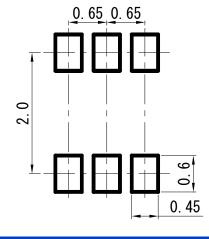
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WSMini6-F1-B

Unit: mm



■ Land Pattern (Reference) (Unit: mm)



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