

Molding Type Module IGBT, Chopper in 1 Package, 1200 V and 300 A



PRIMARY CHARACTERISTICS						
V _{CES}	1200 V					
I _C at T _C = 80 °C	300 A					
$V_{CE(on)}$ (typical) at $I_C = 300 \text{ A}, 25 ^{\circ}\text{C}$	2.0 V					
Speed	8 kHz to 30 kHz					
Package	Dual INT-A-PAK					
Circuit configuration	High side chopper					

FEATURES

- Low V_{CE(on)} SPT and IGBT technology
- 10 µs short circuit capability
- V_{CE(on)} with positive temperature coefficient
- Low inductance case
- · Fast and soft reverse recovery antiparallel FWD
- Isolated copper baseplate using DCB (Direct Copper Bonding) technology
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

TYPICAL APPLICATIONS

- · Inverter for motor drive
- · AC and DC servo drive amplifier
- Uninterruptible power supply (UPS)

DESCRIPTION

Vishay's IGBT power module provides ultra low conduction loss as well as short circuit ruggedness. It is designed for applications such as general inverters and UPS.

ABSOLUTE MAXIMUM RATINGS (T _C = 25 °C unless otherwise noted)						
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS		
Collector to emitter voltage	V _{CES}		1200	V		
Gate to emitter voltage	V_{GES}		± 20	V		
Collector current		T _C = 25 °C	500			
Collector current	I _C	T _C = 80 °C	300			
Pulsed collector current	I _{CM} ⁽¹⁾	$t_p = 1 \text{ ms}$	600	Α		
Diode continuous forward current	I _F	T _C = 80 °C	300			
Diode maximum forward current	I _{FM}	$t_p = 1 \text{ ms}$	600			
Maximum power dissipation	P _D	T _J = 150 °C	1645	W		
Short circuit withstand time	t _{SC}	T _J = 125 °C	10	μs		
RMS isolation voltage	V _{ISOL}	f = 50 Hz, t = 1 min	2500	V		

Note

⁽¹⁾ Repetitive rating: pulse width limited by maximum junction temperature

IGBT ELECTRICAL SPECIFICATIONS (T _C = 25 °C unless otherwise noted)							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS	
Collector to emitter breakdown voltage	V _{(BR)CES}	T _J = 25 °C	1200	-	-		
Callantanta andittana alta a	V	V _{GE} = 15 V, I _C = 300 A, T _J = 25 °C	-	2.0	2.45	V	
Collector to emitter voltage	V _{CE(on)}	V _{GE} = 15 V, I _C = 300 A, T _J = 125 °C	-	2.2	-	V	
Gate to emitter threshold voltage	V _{GE(th)}	$V_{CE} = V_{GE}$, $I_C = 12$ mA, $T_J = 25$ °C	5.0	6.2	7.0		
Collector cut-off current	I _{CES}	$V_{CE} = V_{CES}$, $V_{GE} = 0$ V, $T_{J} = 25$ °C	-	-	5.0	mA	
Gate to emitter leakage current	I _{GES}	$V_{GE} = V_{GES}$, $V_{CE} = 0$ V, $T_{J} = 25$ °C	-	-	400	nA	



SWITCHING CHARACTERISTICS	3					
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Turn-on delay time	t _{d(on)}		-	574	-	ns ns
Rise time	t _r		-	133	-	
Turn-off delay time	t _{d(off)}	$V_{CC} = 600 \text{ V}, I_{C} = 300 \text{ A}, R_{q} = 4.7 \Omega,$	-	563	-	
Fall time	t _f	$V_{GE} = \pm 15 \text{ V}, T_{J} = 25 \text{ °C}$	-	120	-	
Turn-on switching loss	E _{on}		-	23.9	-	- mJ
Turn-off switching loss	E _{off}		-	25.3	-	
Turn-on delay time	t _{d(on)}		-	604	-	- ns
Rise time	t _r		-	137	-	
Turn-off delay time	t _{d(off)}	$V_{CC} = 600 \text{ V}, I_{C} = 300 \text{ A}, R_{q} = 4.7 \Omega,$	-	629	-	
Fall time	t _f	$V_{GE} = \pm 15 \text{ V}, T_{J} = 125 \text{ °C}$	-	167	-	
Turn-on switching loss	E _{on}		-	31.5	-	m l
Turn-off switching loss	E _{off}		-	35.9	-	- mJ
Input capacitance	C _{ies}		-	21.2	-	
Output capacitance	C _{oes}	$V_{GE} = 0 \text{ V}, V_{CE} = 25 \text{ V}, f = 1.0 \text{ MHz}$	-	1.42	-	nF
Reverse transfer capacitance	C _{res}		-	0.94	-	
SC data	I _{SC}	$t_{SC} \le 10~\mu s, V_{GE} = 15~V, T_{J} = 125~^{\circ}C, \ V_{CC} = 900~V, V_{CEM} \le 1200~V$	-	1800	-	Α
Internal gate resistance	R _g		-	1.0	-	Ω
Stray inductance	L _{CE}		-	-	20	nΗ
Module lead resistance, terminal to chip	R _{CC'+EE'}	T _C = 25 °C	-	0.35	-	mΩ

DIODE ELECTRICAL SPECIFICATIONS (T _C = 25 °C unless otherwise noted)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
Diode forward voltage	V _F	I _F = 300 A	$T_J = 25 ^{\circ}C$	-	1.82	2.25	V
Diode forward voltage			T _J = 125 °C	-	1.95	-	
Diada rayaraa raaayany aharga	0		$T_J = 25 ^{\circ}C$	-	20.2	-	
Diode reverse recovery charge	Q _{rr}		T _J = 125 °C	-	40.1	-	μC
Diada paak vayawa vaasyaw ayyant		I_{rr} $I_{F} = 300 \text{ A}, V_{R} = 600 \text{ V},$ $dI_{F}/dt = -2360 \text{ A}/\mu\text{s},$ $V_{GE} = -15 \text{ V}$	T _J = 25 °C	-	170	-	^
Diode peak reverse recovery current	Irr		T _J = 125 °C	-	250		A
Diada variarea vacariam anavari	_		T _J = 25 °C	-	8.2	-	m l
Diode reverse recovery energy	E _{rec}		T _J = 125 °C	-	21.7	-	mJ

THERMAL AND MECHANICAL SPECIFICATIONS									
PARAMETER		SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS	
Operating junction temper	rature	TJ			-	-	150	°C	
Storage temperature rang	je	T _{STG}			-40	-	125		
Junction to case	IGBT	Б			-	-	0.076		
Junction to case	Diode	R _{thJC}			-	-	0.100	K/W	
Case to sink		R _{thCS}	Conductive grease applied		-	0.035	-		
Mounting torque			Power terminal screw: M6		2.5 to 5.0)	Nm	
			Mounting screw: M6		3.0 to 5.0		ווואו		
Weight					300		g		

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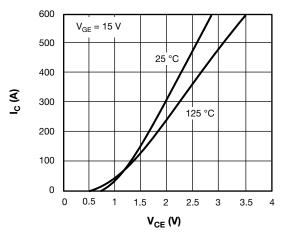
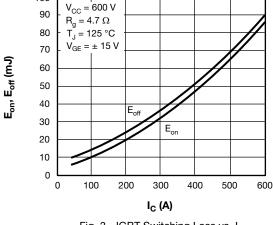


Fig. 1 - IGBT Typical Output Characteristics



100

Fig. 3 - IGBT Switching Loss vs. I_C

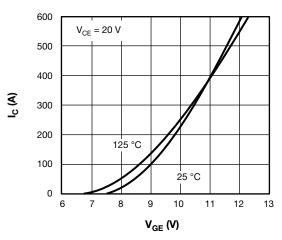


Fig. 2 - IGBT Typical Transfer Characteristics

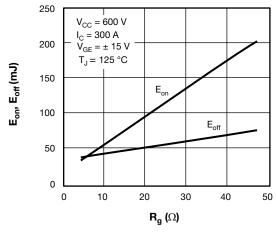
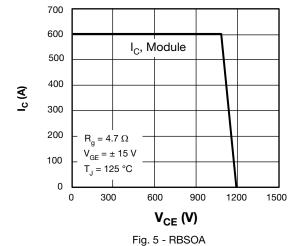


Fig. 4 - IGBT Switching Loss vs. Ra



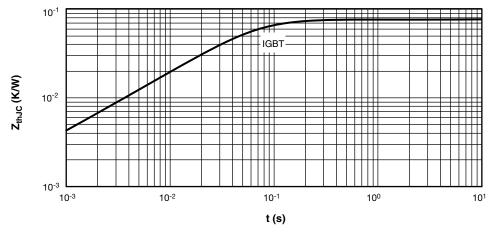


Fig. 6 - IGBT Transient Thermal Impedance

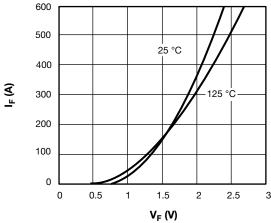


Fig. 7 - Diode Typical Forward Characteristics

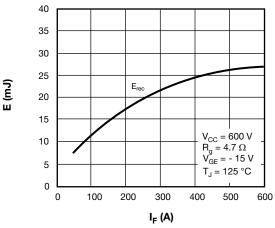


Fig. 8 - Diode Switching Loss vs. I_F

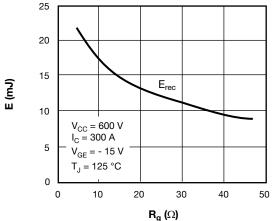


Fig. 9 - Diode Switching Loss vs. R_g

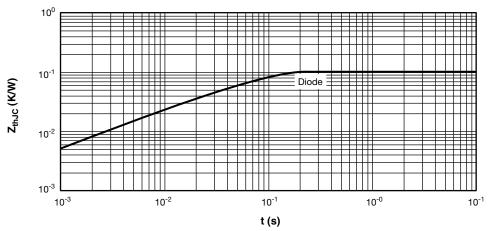
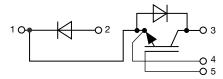


Fig. 10 - Diode Transient Thermal Impedance

CIRCUIT CONFIGURATION

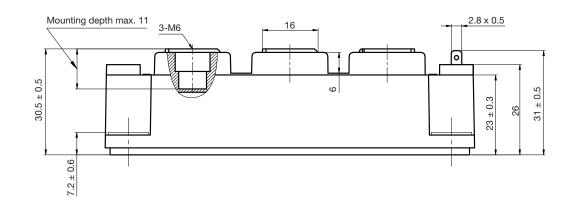


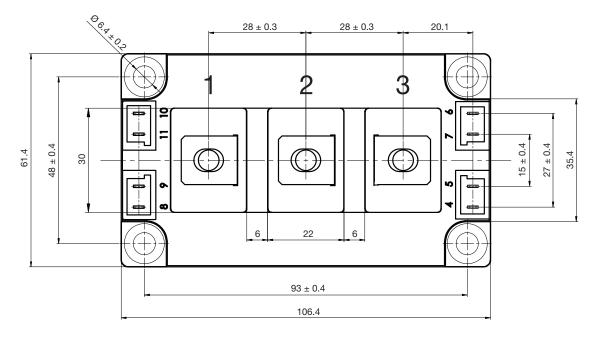
LINKS TO RELATED DOCUMENTS				
Dimensions	www.vishay.com/doc?95525			



Double INT-A-PAK

DIMENSIONS in millimeters (inches)







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