

Molding Type Module IGBT, Chopper in 1 Package, 1200 V and 100 A



PRIMARY CHARACTERISTICS					
V _{CES} 1200 V					
I _C at T _C = 80 °C 100 A					
$V_{CE(on)}$ (typical) at $I_C = 100$ A, 25 °C	1.90 V				
Speed 8 kHz to 30 kHz					
Package Dual INT-A-PAK					
Circuit configuration	High side chopper				

FEATURES

- 10 µs short circuit capability
- V_{CE(on)} with positive temperature coefficient



- Maximum junction temperature 150 °C
- · Low inductance case
- · Fast and soft reverse recovery antiparallel FWD
- Isolated copper baseplate using DCB (Direct Copper Bonding) technology
- Material categorization: for definitions of compliance please see www.vishav.com/doc?99912

TYPICAL APPLICATIONS

- UPS
- · Inverter for motor drive
- AC and DC servo drive amplifier

DESCRIPTION

Vishay's IGBT power module provides ultra low conduction loss as well as short circuit ruggedness. It is designed for applications such as general inverters and UPS.

ABSOLUTE MAXIMUM RATINGS (T _C = 25 °C unless otherwise noted)					
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS	
Collector to emitter voltage	V _{CES}		1200	V	
Gate to emitter voltage	V_{GES}		± 20	V	
Collector current	,	T _C = 25 °C	200		
Collector current	I _C	T _C = 80 °C	100]	
Pulsed collector current	I _{CM} ⁽¹⁾	t _p = 1 ms	200	Α	
Diode continuous forward current	I _F	T _C = 80 °C	100]	
Diode maximum forward current	I _{FM}	t _p = 1 ms	200		
Maximum power dissipation	P _D	T _J = 150 °C	833	W	
Short circuit withstand time	t _{SC}	T _J = 125 °C	10	μs	
RMS isolation voltage	V _{ISOL}	f = 50 Hz, t = 1 min	2500	V	

Note

(1) Repetitive rating: pulse width limited by maximum junction temperature

IGBT ELECTRICAL SPECIFICATIONS (T _C = 25 °C unless otherwise noted)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Collector to emitter breakdown voltage	V _{(BR)CES}	T _J = 25 °C	1200	-	-	
Collector to emitter voltage	V	$V_{GE} = 15 \text{ V}, I_{C} = 100 \text{ A}, T_{J} = 25 \text{ °C}$ - 1	1.90	2.35	v	
Collector to enfitter voltage	V _{CE(on)}	V _{GE} = 15 V, I _C = 100 A, T _J = 125 °C	-	2.10	-	\ \
Gate to emitter threshold voltage	V _{GE(th)}	$V_{CE} = V_{GE}$, $I_C = 4$ mA, $T_J = 25$ °C	5.0	6.2	7.0	
Collector cut-off current	I _{CES}	$V_{CE} = V_{CES}$, $V_{GE} = 0$ V, $T_{J} = 25$ °C	-	-	5.0	mA
Gate to emitter leakage current	I _{GES}	$V_{GE} = V_{GES}$, $V_{CE} = 0$ V, $T_{J} = 25$ °C	-	-	400	nA



SWITCHING CHARACTERISTICS	3					
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Turn-on delay time	t _{d(on)}		-	279	-	- ns
Rise time	t _r		-	61	-	
Turn-off delay time	t _{d(off)}	$V_{CC} = 600 \text{ V}, I_{C} = 100 \text{ A}, R_{g} = 5.6 \Omega,$	-	308	-	
Fall time	t _f	$V_{GE} = \pm 15 \text{ V}, T_{J} = 25 \text{ °C}$	-	205	-	
Turn-on switching loss	E _{on}		-	5.56	-	- mJ
Turn-off switching loss	E _{off}		-	6.95	-	
Turn-on delay time	t _{d(on)}		-	287	-	ns
Rise time	t _r		-	63	-	
Turn-off delay time	t _{d(off)}	$V_{CC} = 600 \text{ V}, I_{C} = 100 \text{ A}, R_{g} = 5.6 \Omega,$	-	328	-	
Fall time	t _f	V _{GE} = ± 15 V, T _J = 125 °C	-	360	-	
Turn-on switching loss	E _{on}		-	7.85	-	I
Turn-off switching loss	E _{off}		-	10.55	-	- mJ
Input capacitance	C _{ies}		-	8.58	-	
Output capacitance	C _{oes}	$V_{GE} = 0 \text{ V}, V_{CE} = 25 \text{ V}, f = 1.0 \text{ MHz}$	-	0.60	-	nF
Reverse transfer capacitance	C _{res}		-	0.40	-	
SC data	I _{SC}	$t_{\text{SC}} \leq 10 \; \mu\text{s}, V_{\text{GE}} = 15 \; \text{V}, T_{\text{J}} = 125 \; ^{\circ}\text{C}, \\ V_{\text{CC}} = 900 \; \text{V}, V_{\text{CEM}} \leq 1200 \; \text{V}$	-	600	-	А
Internal gate resistance	R _{gint}		-	5.0	-	Ω
Stray inductance	L _{CE}		-	-	20	nΗ
Module lead resistance, terminal to chip	R _{CC'+EE'}	T _C = 25 °C	-	0.35	-	mΩ

DIODE ELECTRICAL SPECIFICATIONS (T _C = 25 °C unless otherwise noted)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
Diada famusard valtaga		1 100 4	T _J = 25 °C	-	1.82	2.22	V
Diode forward voltage	V_F $I_F = 100$	I _F = 100 A	T _J = 125 °C	-	1.95	-	
Diode reverse recovery charge	Q _{rr}		T _J = 25 °C	-	5.5	-	
Diode reverse recovery charge			T _J = 125 °C	-	11.9	-	μC
Diada pask vayawa vasayaw ayawant	I _{rr}	I_{rr} $I_{F} = 100 \text{ A}, V_{R} = 600 \text{ V},$ $dI/dt = -2000 \text{ A}/\mu\text{s},$ $V_{GF} = -15 \text{ V}$	T _J = 25 °C	-	85		^
Diode peak reverse recovery current			T _J = 125 °C	-	103		Α
Diada waxaa waxaa waxaa waxaa a	Г	E _{rec}	T _J = 25 °C	-	2.07	-	I
Diode reverse recovery energy	⊏ _{rec}		T _J = 125 °C	-	5.56	-	mJ

THERMAL AND MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Operating junction temperature range	TJ		-	-	150	°C
Storage temperature range	T _{STG}		-40	-	125	
Junction to case	D		-	-	0.150	
Diode	R _{thJC}		-	-	0.225	K/W
Case to sink	R _{thCS}	Conductive grease applied	=	0.035	-	
Mounting toward		Power terminal screw: M6	2.5 to 5.0		Nm	
Mounting torque		Mounting screw: M6	,	3.0 to 5.0)	INIII
Weight				300		g

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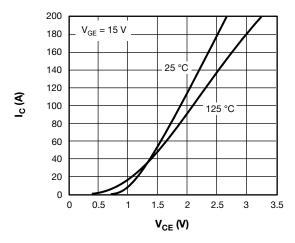


Fig. 1 - IGBT Typical Output Characteristics

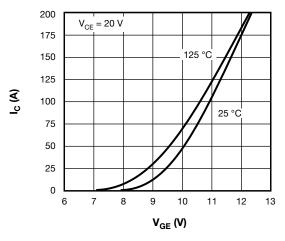


Fig. 2 - IGBT Typical Transfer Characteristics

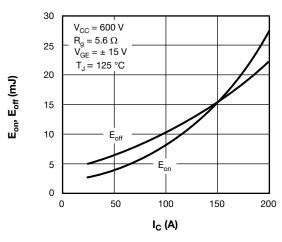


Fig. 3 - IGBT Switching Loss vs. I_C

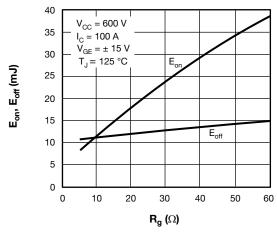
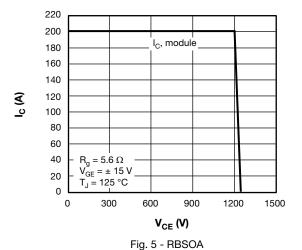


Fig. 4 - IGBT Switching Loss vs. R_q



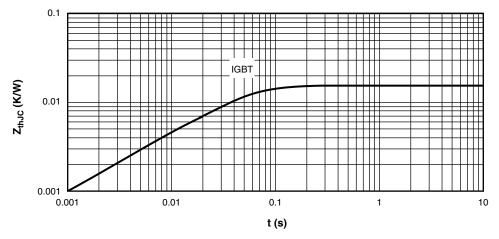
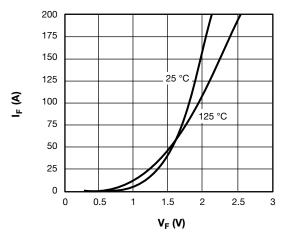


Fig. 6 - IGBT Transient Thermal Impedance



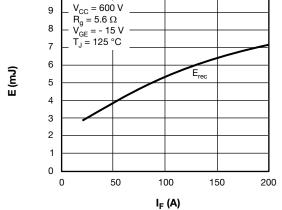


Fig. 7 - Typical Forward Characteristics

Fig. 8 - Diode Switching Loss vs. I_F

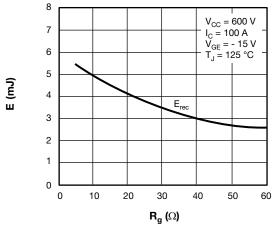


Fig. 9 - Diode Switching Loss vs. Gate Resistance

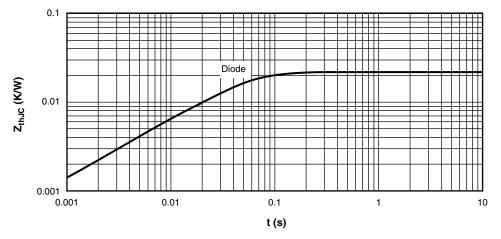
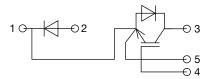


Fig. 10 - Diode Transient Thermal Impedance

CIRCUIT CONFIGURATION

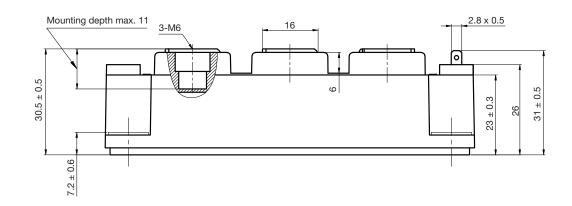


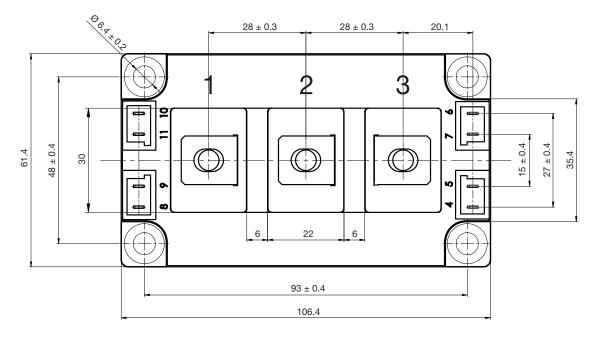
LINKS TO RELATED DOCUMENTS					
Dimensions	www.vishay.com/doc?95525				



Double INT-A-PAK

DIMENSIONS in millimeters (inches)







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