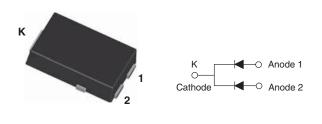


**SMPC (TO-277A)** 

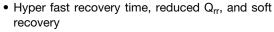
### Vishay Semiconductors

# Hyper Fast Rectifier, 2 x 2 A FRED Pt®



PRODUCT SUMMARY				
Package	SMPC (TO-277A)			
I <sub>F(AV)</sub>	2 x 2 A			
$V_{R}$	200 V			
V <sub>F</sub> at I <sub>F</sub>	0.75 V			
t <sub>rr (typ.)</sub>	24 ns			
T <sub>J</sub> max.	175 °C			
Diode variation	Dual die			

#### **FEATURES**





RoHS

COMPLIANT **HALOGEN** 

FREE

- 175 °C maximum operating junction temperature
- Specified for output and snubber operation
- Low forward voltage drop
- Low leakage current
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- AEC-Q101 qualified, meets JESD 201 class 2 whisker test
- Material categorization: for definitions of compliance please see www.vishav.com/doc?99912

#### **DESCRIPTION / APPLICATIONS**

State of the art hyper fast recovery rectifiers specifically designed with optimized performance of forward voltage drop and hyper fast recovery time.

The planar structure and the platinum doped life time control guarantee the best overall performance, ruggedness, and reliability characteristics.

These devices are intended for use in snubber, boost, lighting, piezo-injection, as high frequency rectifiers, and freewheeling diodes.

The extremely optimized stored charge and low recovery current minimize the switching losses and reduce power dissipation in the switching element.

ABSOLUTE MAXIMUM RATINGS					
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS
Peak repetitive reverse voltage		$V_{RRM}$		200	V
A	per device	I <sub>F(AV)</sub>	T <sub>Sp</sub> = 165 °C	4	^
Average rectified forward current	per diode			2	
Non-repetitive peak surge current	per device		T 05 °C	90	Α
	per diode	IFSM	T <sub>J</sub> = 25 °C	50	
Operating junction and storage temperatures		T <sub>J</sub> , T <sub>Stg</sub>		-65 to +175	°C

<b>ELECTRICAL SPECIFICATIONS</b> (T <sub>J</sub> = 25 °C unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Breakdown voltage, blocking voltage	V <sub>BR</sub> , V <sub>R</sub>	I <sub>R</sub> = 100 μA	200	-	-	
Forward voltage, per diode V <sub>F</sub>	M	I <sub>F</sub> = 2 A	-	0.88	0.95	V
	I <sub>F</sub> = 2 A, T <sub>J</sub> = 125 °C	-	0.75	0.82		
Reverse leakage current, per diode	I <sub>R</sub>	V <sub>R</sub> = V <sub>R</sub> rated	-	-	2	
		T <sub>J</sub> = 125 °C, V <sub>R</sub> = V <sub>R</sub> rated	-	1	8	μA
Junction capacitance	C <sub>T</sub>	V <sub>R</sub> = 200 V	-	8	-	pF



# Vishay Semiconductors

<b>DYNAMIC RECOVERY CHARACTERISTICS</b> (T <sub>J</sub> = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
		$I_F = 1.0 \text{ A}, dI_F/dt = 50 \text{ A/}\mu\text{s}, V_R = 30 \text{ V}$		-	24	-	
Poveree receivery time		I <sub>F</sub> = 0.5 A, I <sub>R</sub> = 1 A, I <sub>rr</sub> = 0.25 A		-	-	25	
Reverse recovery time t <sub>rr</sub>	L <sub>rr</sub>	T <sub>J</sub> = 25 °C		-	16	-	ns
		T <sub>J</sub> = 125 °C		-	22	-	
Peak recovery current I <sub>RRM</sub>	T <sub>J</sub> = 25 °C	$I_F = 2 A$ $dI_F/dt = 200 A/\mu s$ $V_B = 160 V$	-	2	-	Α	
	T <sub>J</sub> = 125 °C		-	3	-		
Reverse recovery charge Q <sub>rr</sub>	0	T <sub>J</sub> = 25 °C	***	=	16	-	nC
	T <sub>J</sub> = 125 °C		-	30	-	iic	

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Maximum junction and storage temperature range	T <sub>J</sub> , T <sub>Stg</sub>		-65	-	175	°C
Thermal resistance, junction to solder pad, per diode	R <sub>thJ-Sp</sub>		-	4.5	5.5	°C/W
Approximate weight				0.1		g
Approximate weight				0.0035		oz.
Marking device		Case style SMPC (TO-277A)		JC	H2	

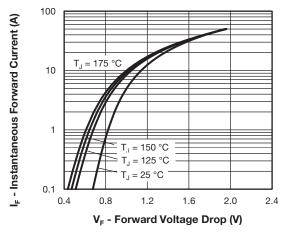


Fig. 1 - Typical Forward Voltage Drop Characteristics

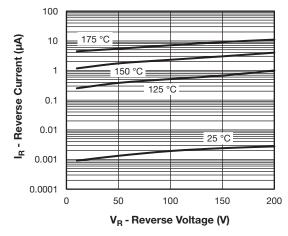


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

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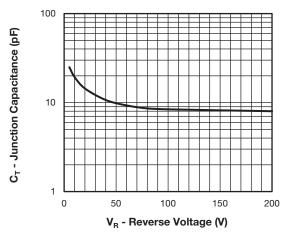


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

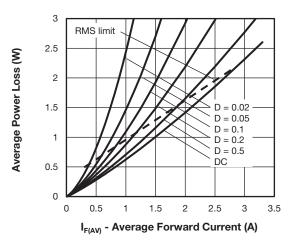


Fig. 5 - Forward Power Loss Characteristics

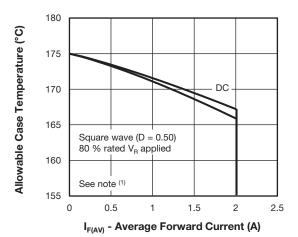


Fig. 4 - Maximum Allowable Case Temperature vs. Average Forward Current

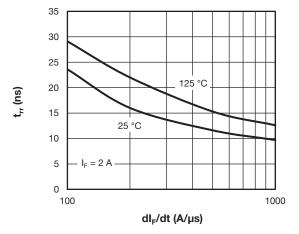


Fig. 6 - Typical Reverse Recovery Time vs. dl<sub>F</sub>/dt

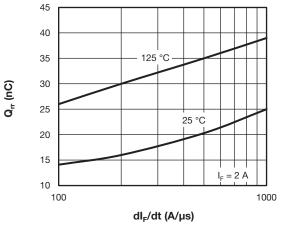
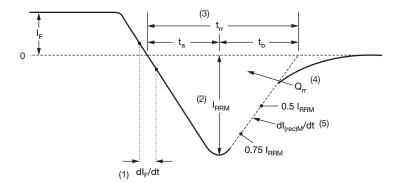


Fig. 7 - Typical Stored Charge vs. dl<sub>F</sub>/dt

#### Note

 $\begin{array}{ll} \text{(1)} \ \ \text{Formula used:} \ T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}; \\ Pd = \text{forward power loss} = I_{F(AV)} \times V_{FM} \ \text{at} \ (I_{F(AV)}/D) \ \text{(see fig. 5)}; \\ Pd_{REV} = \text{inverse power loss} = V_{R1} \times I_R \ \text{(1 - D)}; \ I_R \ \text{at} \ V_{R1} = \text{rated} \ V_R \end{aligned}$ 

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- (1) dl<sub>F</sub>/dt rate of change of current through zero crossing
- (2) I<sub>RRM</sub> peak reverse recovery current
- (3)  $\rm t_{rr}$  reverse recovery time measured from zero crossing point of negative going  $\rm I_{r}$  to point where a line passing through 0.75  $\rm I_{RRM}$  and 0.50  $\rm I_{RRM}$  extrapolated to zero current.
- (4)  $\mathbf{Q}_{\rm rr}$  area under curve defined by  $\mathbf{t}_{\rm rr}$  and  $\mathbf{I}_{\rm RRM}$

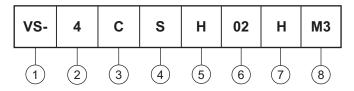
$$Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$$

(5) dl<sub>(rec)M</sub>/dt - peak rate of change of current during t<sub>b</sub> portion of t<sub>rr</sub>

Fig. 8 - Reverse Recovery Waveform and Definitions

#### **ORDERING INFORMATION TABLE**

**Device code** 



- 1 Vishay Semiconductors product
- 2 Current rating (4 = 4 A)
- 3 Circuit configuration:

C = common cathode

4 - S = SMPC package

5 - Process type,

H = hyper fast recovery

6 - Voltage code (02 = 200 V)

7 - H = AEC-Q101 qualified

8 - M3 = halogen-free, RoHS-compliant, and terminations lead (Pb)-free

ORDERING INFORMATION (Example)				
PREFERRED P/N	QUANTITY PER REEL	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION	
VS-4CSH02HM3/86A	1500	1500	7" diameter plastic tape and reel	
VS-4CSH02HM3/87A	6500	6500	13" diameter plastic tape and reel	

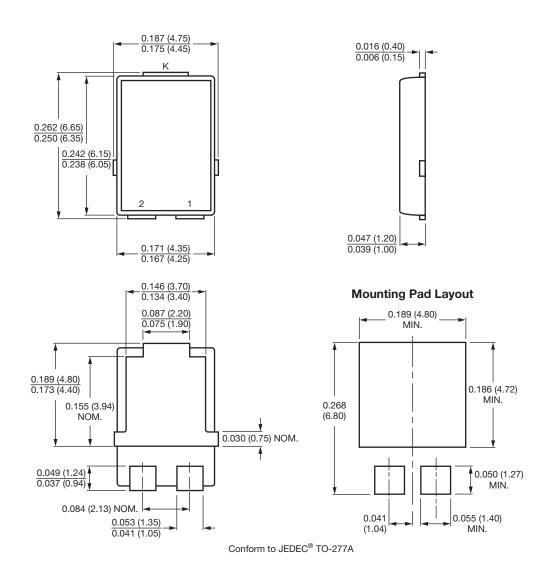
LINKS TO RELATED DOCUMENTS			
Dimensions <u>www.vishay.com/doc?95570</u>			
Part marking information	www.vishay.com/doc?95565		
Packaging information	www.vishay.com/doc?88869		



Vishay Semiconductors

# **TO-277A (SMPC)**

### **DIMENSIONS** in inches (millimeters)





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