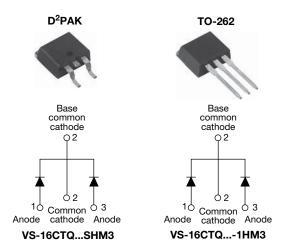
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ISHA'

Vishay Semiconductors

High Performance Schottky Rectifier, 2 x 8 A



PRODUCT SUMMARY						
I _{F(AV)}	2 x 8 A					
V _R	60 V to 100 V					
V _F at I _F	0.58 V					
I _{RM}	7.0 mA at 125 °C					
T _J max.	175 °C					
E _{AS}	7.5 mJ					
Package	TO-263AB (D ² PAK), TO-262AA					
Diode variation	Common cathode					

FEATURES

- 175 °C T_J operation
- Center tap configuration
- Low forward voltage drop
- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance



- High frequency operation
- Guard ring for enhanced ruggedness and long term reliability
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- AEC-Q101 qualified, meets JESD 201 class 1 whisker test
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

DESCRIPTION

This center tap Schottky rectifier series has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 175 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS							
SYMBOL	CHARACTERISTICS	CHARACTERISTICS VALUES					
I _{F(AV)}	Rectangular waveform	16	A				
V _{RRM}		60 to 100	V				
I _{FSM}	t _p = 5 μs sine	850	A				
V _F	8 A _{pk} , T _J = 125 °C (per leg)	0.58	V				
TJ	Range	-55 to +175	°C				

VOLTAGE RATINGS								
PARAMETER	SYMBOL			VS-16CTQ100SHM3 VS-16CTQ100-1HM3				
Maximum DC reverse voltage	V _R	60	80	100	V			
Maximum working peak reverse voltage	V _{RWM}	00	80	100	v			

ABSOLUTE MAXIMUM RATINGS								
PARAMETER	SYMBOL	TEST COND	ITIONS	VALUES	UNITS			
Maximum average forward current per leg		50 % duty cycle at T _C = 148 °C		8	А			
See fig. 5 per device	I _{F(AV)}	50% duty cycle at $T_C = 148$ C	16	A				
Maximum peak one cycle non-repetitive surge		5 µs sine or 3 µs rect. pulse	Following any rated load	850				
current per leg	I _{FSM}	10 ms sine or 6 ms rect. pulse	condition and with rated	275	А			
See fig. 7		To this sine of o this fect, pulse	V _{RRM} applied	275				
Non-repetitive avalanche energy per leg	Non-repetitive avalanche energy per leg E_{AS} $T_J = 25 \text{ °C}, I_{AS} = 0.50 \text{ A}, L = 60 \text{ mH}$) mH	7.50	mJ			
Repetitive avalanche current per leg	lun.	Current decaying linearly to zero in 1 µs		0.50	А			
hopenine avalanche current per leg	I _{AR}	Frequency limited by T_J maximum $V_A = 1.5 \times V_R$ typical		0.00	A			

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ELECTRICAL SPECIFICATIONS							
PARAMETER	SYMBOL	TEST CO	NDITIONS	VALUES	UNITS		
		8 A	T _{.1} = 25 °C	0.72			
Maximum forward voltage drop per leg	V _{FM} ⁽¹⁾	16 A	1j=23 0	0.88	v		
See fig. 1	VFM ("	8 A	T _{.1} = 125 °C	0.58			
		16 A	1j = 125 C	0.69			
Maximum reverse leakage current per leg	I _{BM} ⁽¹⁾	T _J = 25 °C	$V_{\rm B}$ = Rated $V_{\rm B}$	0.55	mA		
See fig. 2	IRM (")	T _J = 125 °C	$v_{\rm R} = naleu v_{\rm R}$	7.0	ШA		
Threshold voltage	V _{F(TO)}			0.415	V		
Forward slope resistance	r _t	$T_J = T_J maximum$		11.07	mΩ		
Maximum junction capacitance per leg	CT	$V_R = 5 V_{DC}$ (test signal ran	500	pF			
Typical series inductance per leg	L _S	Measured lead to lead 5 mm from package body 8.0			nH		
Maximum voltage rate of change	dV/dt	Rated V _R 10 000					

Note

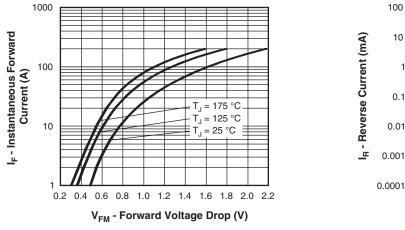
 $^{(1)}\,$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

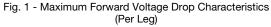
THERMAL - MECHANICAL SPECIFICATIONS							
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS		
Maximum junction and storage temperature range	, ,			-55 to 175	°C		
Maximum thermal resistance, junction to case per leg		Б	DC aparation	3.25			
Maximum thermal resistance, junction to case per package		R _{thJC} DC operation	1.63	°C/W			
Typical thermal resistance, case to heatsink		R _{thCS}	Mounting surface, smooth and greased	0.50			
Approximate weight				2	g		
Approximate weight				0.07	oz.		
minim				6 (5)	kgf · cm		
Mounting torque	maximum			12 (10)	(lbf · in)		
Marking davias			Case style D ² PAK	16CTC	QSH		
	Marking device		Case style TO-262	16CTC)1H		



VS-16CTQ...SHM3, VS-16CTQ...-1HM3 Series

Vishay Semiconductors





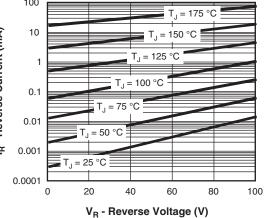


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

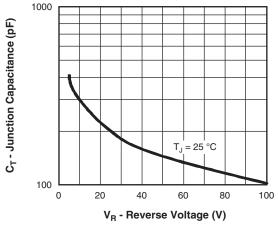


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

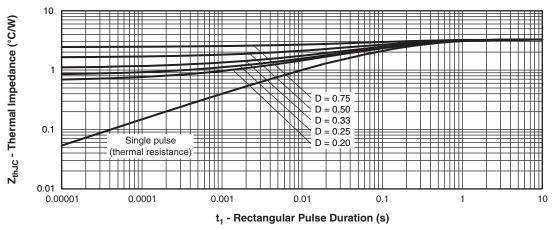
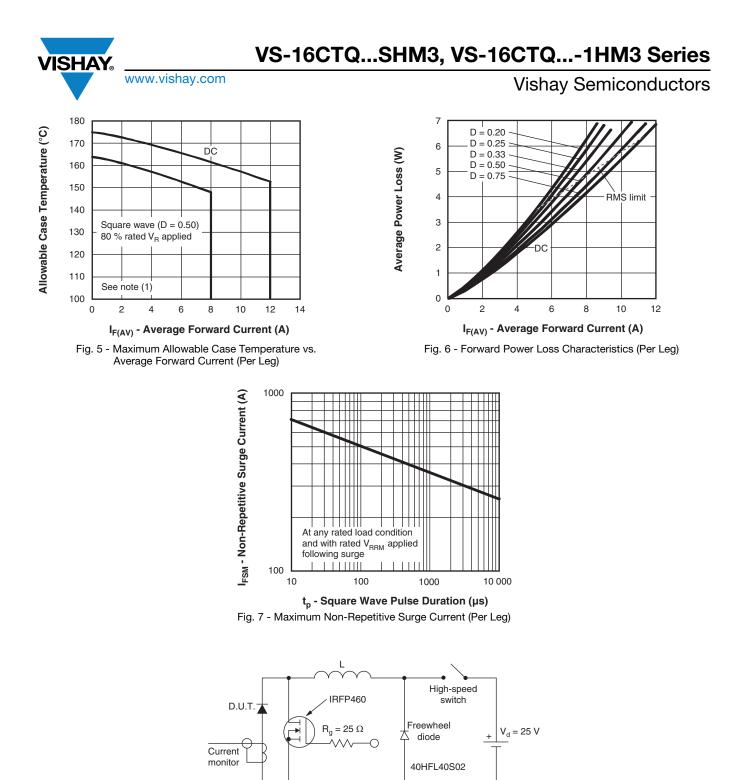


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics (Per Leg)



☆ Fig. 8 - Unclamped Inductive Test Circuit

Note

- ⁽¹⁾ Formula used: $T_C = T_J (Pd + Pd_{REV}) \times R_{thJC}$;
- Pd = Forward power loss = $I_{F(AV)} \times V_{FM}$ at $(I_{F(AV)}/D)$ (see fig. 6);
- Pd_{REV} = Inverse power loss = $V_{R1} \times I_R (1 D)$; I_R at V_{R1} = 80 % rated V_R applied

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Vishay Semiconductors

ORDERING INFORMATION TABLE

www.vishay.com

VISHAY

Device code	VS-	16	с	т	Q	100	S	TRL	Н	М3
	1	2	3	4	5	6	7	8	9	10
	H	- Cur - Circ - T =	Vishay Semiconductors product Current rating (16 A) Circuit configuration: C = Common cathode T = TO-220							
	6 7	- Voli - • S	Schottky "Q" series $060 = 60 V$ Voltage ratings $080 = 80 V$ • S = D ² PAK $100 = 100 V$ • -1 = TO-262							
	8	• T • T	 None = tube TRL = tape and reel (left oriented - for D²PAK only) TRR = tape and reel (right oriented - for D²PAK only) H = AEC-Q101 qualified 							
	10			•		complia	ant and	terminat	tion lead	d (Pb)-fr

ORDERING INFORMATION							
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION				
VS-16CTQ060SHM3	50	1000	Antistatic plastic tubes				
VS-16CTQ060STRRHM3	800	800	13" diameter reel				
VS-16CTQ060STRLHM3	800	800	13" diameter reel				
VS-16CTQ060-1HM3	50	1000	Antistatic plastic tubes				
VS-16CTQ080SHM3	50	1000	Antistatic plastic tubes				
VS-16CTQ080STRRHM3	800	800	13" diameter reel				
VS-16CTQ080STRLHM3	800	800	13" diameter reel				
VS-16CTQ080-1HM3	50	1000	Antistatic plastic tubes				
VS-16CTQ100SHM3	50	1000	Antistatic plastic tubes				
VS-16CTQ100STRRHM3	800	800	13" diameter reel				
VS-16CTQ100STRLHM3	800	800	13" diameter reel				
VS-16CTQ100-1HM3	50	1000	Antistatic plastic tubes				

	LINKS TO RELATED DOCUMENTS							
Dimensions	TO-263AB (D ² PAK)	www.vishay.com/doc?95046						
Dimensions	TO-262AA	www.vishay.com/doc?95419						
Part marking information	TO-263AB (D ² PAK)	www.vishay.com/doc?95444						
Part marking information	TO-262AA	www.vishay.com/doc?95443						
Packaging information		www.vishay.com/doc?95032						
SPICE model		www.vishay.com/doc?95279						

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Document Number: 95861

Outline Dimensions



D²PAK

DIMENSIONS in millimeters and inches

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SHA



SYMBOL	MILLIM	IETERS	INC	HES	NOTES	SYMBOL	MILLIM	IETERS	INC	HES	NOTES
STMBOL	MIN.	MAX.	MIN.	MAX.	NOTES	STWDUL	MIN.	MAX.	MIN.	MAX.	NOTES
А	4.06	4.83	0.160	0.190		D1	6.86	8.00	0.270	0.315	3
A1	0.00	0.254	0.000	0.010		E	9.65	10.67	0.380	0.420	2, 3
b	0.51	0.99	0.020	0.039		E1	7.90	8.80	0.311	0.346	3
b1	0.51	0.89	0.020	0.035	4	е	2.54	BSC	0.100	BSC	
b2	1.14	1.78	0.045	0.070		Н	14.61	15.88	0.575	0.625	
b3	1.14	1.73	0.045	0.068	4	L	1.78	2.79	0.070	0.110	
С	0.38	0.74	0.015	0.029		L1	-	1.65	-	0.066	3
c1	0.38	0.58	0.015	0.023	4	L2	1.27	1.78	0.050	0.070	
c2	1.14	1.65	0.045	0.065		L3	0.25	BSC	0.010	BSC	
D	8.51	9.65	0.335	0.380	2	L4	4.78	5.28	0.188	0.208	

Notes

⁽¹⁾ Dimensioning and tolerancing per ASME Y14.5 M-1994

⁽²⁾ Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body

⁽³⁾ Thermal pad contour optional within dimension E, L1, D1 and E1

⁽⁴⁾ Dimension b1 and c1 apply to base metal only

⁽⁵⁾ Datum A and B to be determined at datum plane H

⁽⁶⁾ Controlling dimension: inch

⁽⁷⁾ Outline conforms to JEDEC[®] outline TO-263AB

Revision: 08-Jul-15

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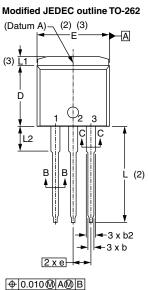


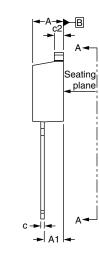
Outline Dimensions

Vishay Semiconductors

TO-262

DIMENSIONS in millimeters and inches

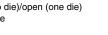


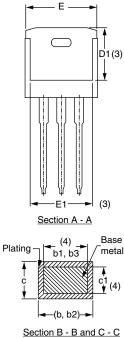


Lead assignments



Diodes 1. - Anode (two die)/open (one die) 2., 4. - Cathode 3. - Anode





Scale: None

CVMDOI	MILLIMETERS		INC	INCHES			
SYMBOL	MIN.	MAX.	MIN.	MAX.	NOTES		
А	4.06	4.83	0.160	0.190			
A1	2.03	3.02	0.080	0.119			
b	0.51	0.99	0.020	0.039			
b1	0.51	0.89	0.020	0.035	4		
b2	1.14	1.78	0.045	0.070			
b3	1.14	1.73	0.045	0.068	4		
С	0.38	0.74	0.015	0.029			
c1	0.38	0.58	0.015	0.023	4		
c2	1.14	1.65	0.045	0.065			
D	8.51	9.65	0.335	0.380	2		
D1	6.86	8.00	0.270	0.315	3		
E	9.65	10.67	0.380	0.420	2, 3		
E1	7.90	8.80	0.311	0.346	3		
е	2.54	2.54 BSC		0 BSC			
L	13.46	14.10	0.530	0.555			
L1	-	1.65	-	0.065	3		
L2	3.56	3.71	0.140	0.146			

Notes

Revision: 04-Oct-10

⁽¹⁾ Dimensioning and tolerancing as per ASME Y14.5M-1994

⁽²⁾ Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body

⁽⁴⁾ Dimension b1 and c1 apply to base metal only

(5) Controlling dimension: inches

(6) Outline conform to JEDEC TO-262 except A1 (maximum), b (minimum) and D1 (minimum) where dimensions derived the actual package outline

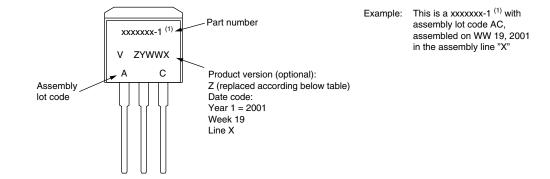
⁽³⁾ Thermal pad contour optional within dimension E, L1, D1 and E1

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Vishay Semiconductors

TO-262



Note

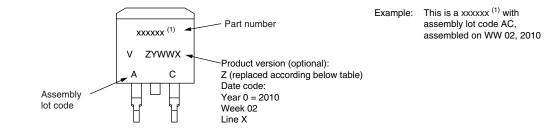
⁽¹⁾ If part number contain "H" as last digit, product is AEC-Q101 qualified

ENVIRONMENTAL NAMING CODE (Z)	PRODUCT DEFINITION		
A	Termination lead (Pb)-free		
В	Totally lead (Pb)-free		
E	RoHS-compliant and termination lead (Pb)-free		
F	RoHS-compliant and totally lead (Pb)-free		
М	Halogen-free, RoHS-compliant and termination lead (Pb)-free		
Ν	Halogen-free, RoHS-compliant and totally lead (Pb)-free		
G	Green		



Vishay Semiconductors

D²PAK



Note

⁽¹⁾ If part number contain "H" as last digit, product is AEC-Q101 qualified

ENVIRONMENTAL NAMING CODE (Z)	PRODUCT DEFINITION
A	Termination lead (Pb)-free
В	Totally lead (Pb)-free
E	RoHS-compliant and termination lead (Pb)-free
F	RoHS-compliant and totally lead (Pb)-free
М	Halogen-free, RoHS-compliant, and termination lead (Pb)-free
N	Halogen-free, RoHS-compliant, and totally lead (Pb)-free
G	Green



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