

**Micro Commercial Components** 



Micro Commercial Components 20736 Marilla Street Chatsworth CA 91311

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## **SI3139KL**

### **Features**

- Lead Free Product is Acquired
- Halogen free available upon request by adding suffix "-HF"
- Epoxy meets UL 94 V-0 flammability rating
- Moisture Sensitivity Level 1
- Operated at Low Logic Level Gate Drive
- P-Channel Switch with Low RDS(on)
- Surface Mount Package
- Marking:39



**Equivalent Circuit** 

### **Absolute maximum ratings @ 25 ℃**

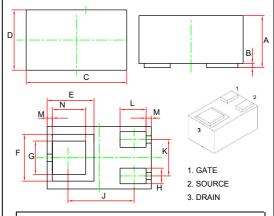
Symbol	Parameter	Value	Unit
$V_{DS}$	Drain-source Voltage	-20	V
$V_{GS}$	Gate-source Voltage	±12	V
$I_D$	Continuous Drain Current <sup>(1)</sup> -0.68		Α
I <sub>DM</sub>	Pulsed Drain Current	-1.2	Α
P <sub>D</sub>	Total Power Dissipation <sup>(2)</sup>	100	mW
TJ	Operating Junction Temperature	perature -55 to +150 °C	
$T_{STG}$	Storage Temperature	-55 to +150	$^{\circ}$
TL	Lead Temperature for Soldering Purposes	260	$^{\circ}\!\mathbb{C}$
RthJA	Thermal Resistance fromJunction to Ambient	1250	°C/W

### Electrical Characteristics @ 25°C Unless Otherwise Specified

Symbol	Para	Min	Тур	Max	Units	
STATIC PARAMETERS						
$V_{(BR)DSS}$	Drain-Source Breal (V <sub>GS</sub> =0Vdc, I <sub>D</sub> =-250	-20			Vdc	
$V_{GS(th)}$	Gate-Threshold Voltage (VDS=VGS, ID= -250µAdc)		-0.35		-1.1	Vdc
I <sub>GSS</sub>	Gate-body Leakage (V <sub>GS</sub> =±10V, V <sub>DS</sub> = 0V)				±20	μA
I <sub>DSS</sub>	Zero Gate Voltage Drain Current (V <sub>DS</sub> =-20Vdc, V <sub>GS</sub> =0Vdc)				-1	μAdc
$V_{SD}$	Diode Forward Voltage (I <sub>S</sub> =-0.5A,V <sub>GS</sub> =0V				-1.2	Vdc
r <sub>DS(on)</sub>	$\begin{array}{l} \text{Drain-Source On-Resistance} \\ (\text{V}_{\text{GS}}\text{=-}4.5\text{Vdc}, \text{I}_{\text{D}}\text{=-}1.0\text{Adc}) \\ (\text{V}_{\text{GS}}\text{=-}2.5\text{Vdc}, \text{I}_{\text{D}}\text{=-}0.8\text{Adc}) \\ (\text{V}_{\text{GS}}\text{=-}1.8\text{Vdc}, \text{I}_{\text{D}}\text{=-}0.5\text{Adc}) \end{array}$			  0.95	0.52 0.70 	Ω
<b>g</b> FS	Forward transconductance (V <sub>DS</sub> =-10Vdc, I <sub>D</sub> =-0.54Adc)			1.2		S
DYNAMIC PARAMETERS <sup>(4)</sup>						
C <sub>iss</sub>	Input Capacitance	\/= 16\/do		113	170	
Coss	Output Capacitance	$V_{DS}$ =-16Vdc, $V_{GS}$ =0Vdc		15	25	pF
C <sub>rSS</sub>	Reverse Transfer capacitance	f=1MHz		9	15	I-
SWITCHIN	G PARAMETERS(4)					
t <sub>d(on)</sub>	Turn-on Time <sup>(3)</sup>	V <sub>DD</sub> =-4.5V,		9.0		
$t_{d(off)}$	Turn-off Time(3)	V <sub>GS</sub> =-10V,		32.7		ns
t <sub>r</sub>	Rise Time(3)	I <sub>D</sub> =-200mA		5.8		115
t <sub>r</sub>	Fall Time(3)	$R_{GEN}$ =10 $\Omega$		20.3		

# **P-Channel MOSFET**





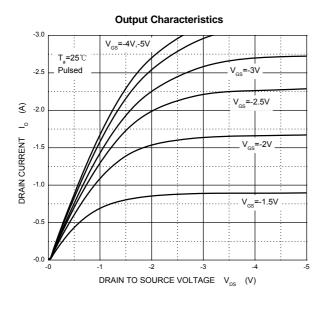
DIMENSIONS					
	INCHES		MM		
DIM	MIN	MAX	MIN	MAX	NOTE
A	0.018	0.022	0.450	0.550	
В	0.000	0.004	0.010	0.100	
С	0.037	0.041	0. 950	1.050	
D	0.022	0.026	0.550	0.650	
Е	0. 018REF.		0. 45	OREF.	
F	0. 018REF.		0. 450REF.		
G	0.011	0.015	0.270	0.370	
Н	0.004	0.008	0.100	0.200	
J	0. 025REF.		0.635REF.		
K	0.012	0.016	0.300	0.400	
L	0.008	0.012	0.200	0.300	
M	0.002REF.		0.05	50REF.	
N	0.011	0.015	0.270	0.370	

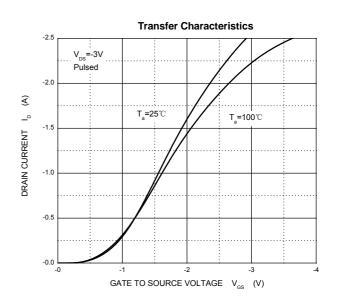
- 1.Surface mounted on FR4 board using the minimum recommended pad size.
- 2.Pulse est : Pulse width=300µs, duty cycle≤2%.
- 3. Switching characteristics are independent of operating junction temperatures.
- 4. Graranted y design, not subject to producting.

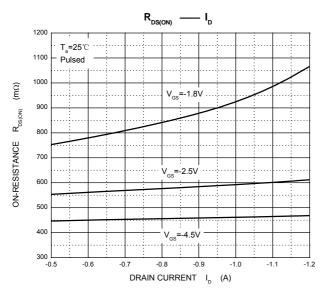
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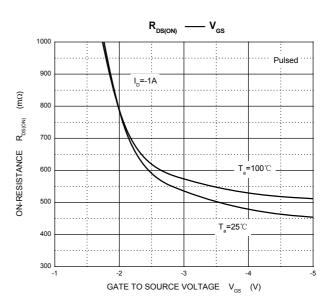
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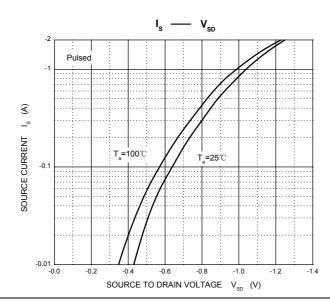
### **Typical Characteristics**

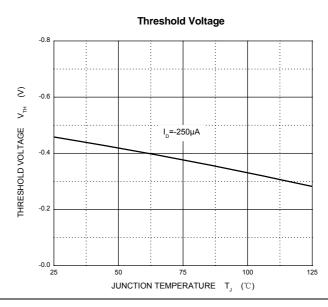














### **Ordering Information:**

Device	Packing
Part Number-TP	Tape&Reel: 10Kpcs/Reel

Note: Adding "-HF" suffix for halogen free, eg. Part Number-TP-HF

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