Vishay Semiconductors

High Performance Schottky Rectifier, 240 A



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PRIMARY CHARACTERISTICS					
I _{F(AV)}	240 A				
V _R	30 V				
Package	HALF-PAK (D-67)				
Circuit configuration	Single				

FEATURES

- 150 °C T_{.1} operation
- Low forward voltage drop
- High frequency operation
- Guard ring for enhanced ruggedness and long term reliability
- Designed and qualified for industrial level
- UL approved file E222165
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

DESCRIPTION

The VS-242NQ.. high current Schottky rectifier module series has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 150 °C junction temperature. Typical applications are in high current switching power supplies, plating power supplies, UPS systems, converters, freewheeling diodes, welding, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS						
SYMBOL	CHARACTERISTICS	VALUES	UNITS			
I _{F(AV)}	Rectangular waveform	240	А			
V _{RRM}		30	V			
I _{FSM}	t _p = 5 μs sine	27 000	А			
V _F	220 A _{pk} , T _J = 125 °C	0.45	V			
TJ	Range	-55 to +150	°C			

VOLTAGE RATINGS					
PARAMETER	SYMBOL	VS-242NQ030PbF	UNITS		
Maximum DC reverse voltage	VR	30	V		
Maximum working peak reverse voltage	V _{RWM}	30	v		

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average forward current See fig. 5	I _{F(AV)}	50 % duty cycle at $T_C = 118$ °C, rectangular waveform 240		240	
Maximum peak one cycle non-repetitive surge current	1	5 μs sine or 3 μs rect. pulse	Following any rated load condition and with	27 000	A
See fig. 7	IFSM	10 ms sine or 6 ms rect. pulse	rated V_{RRM} applied	3000	
Non-repetitive avalanche energy	E _{AS}	T _J = 25 °C, I _{AS} = 21 A, L = 1 mH		216	mJ
Repetitive avalanche current	I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T _J maximum V _A = 1.5 x V _R typical 48		А	

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COMPLIANT



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ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CO	VALUES	UNITS	
Maximum forward voltage drop See fig. 1	V _{FM} ⁽¹⁾	240 A	T _{.1} = 25 °C	0.54	V
		480 A	1j=25 0	0.73	
		240 A	T.I = 125 °C	0.47	
		480 A	1j = 125 0	0.7	
Maximum reverse leakage current	I _{RM}	T _J = 25 °C	$V_{\rm B}$ = Rated $V_{\rm B}$	20	mA
See fig. 2		T _J = 125 °C	V _R = naleu V _R	1120	
Maximum junction capacitance	CT	$V_{R} = 5 V_{DC}$ (test signal range 100 kHz to 1 MHz) 25 °C		14 800	pF
Typical series inductance	L _S	From top of terminal hole to mounting plane		5.0	nH
Maximum voltage rate of change	dV/dt	Rated V _R 10 000 V/ _H			V/µs

Note

⁽¹⁾ Pulse width = 500 μ s

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum junction and storage T _J temperature range		T _J , T _{Stg}		-55 to 150	°C	
Maximum thermal resistance, junction to case		R _{thJC}	DC operation See fig. 4	0.19	20.44	
Typical thermal resistance, case to heatsink		R _{thCS}	Mounting surface, smooth and greased	0.05	°C/W	
A				30	g	
Approximate weight	Approximate weight			1.06	oz.	
Mounting torque	minimum			3 (26.5)		
Mounting torque maximum Terminal torque maximum			Non-lubricated threads	4 (35.4)	N ⋅ m	
			Non-lubricated trireads	3.4 (30)	(lbf · in)	
				5 (44.2)		
Case style				HALF-PAI	< module	

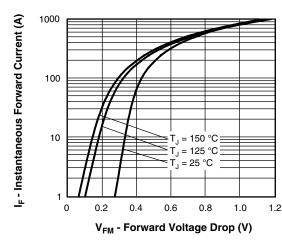


Fig. 1 - Maximum Forward Voltage Drop Characteristics

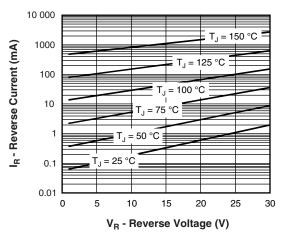


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

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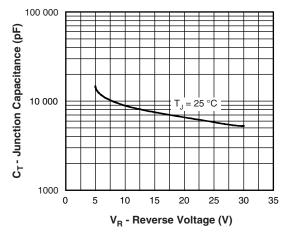


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

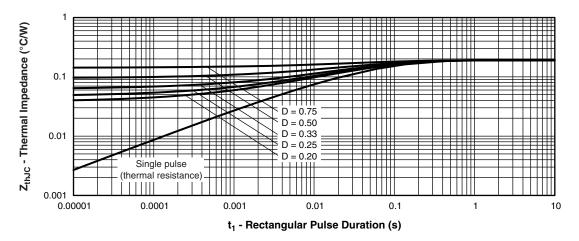
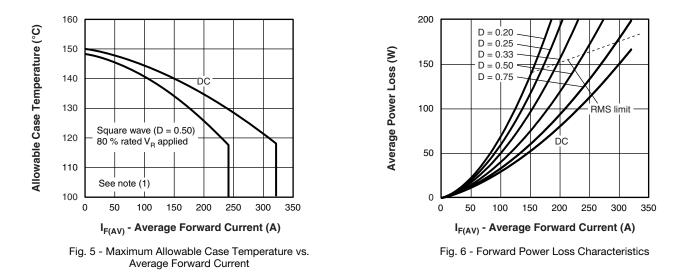


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics



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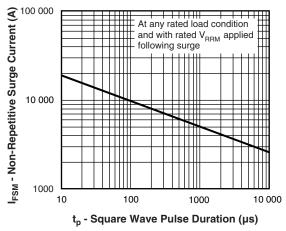


Fig. 7 - Maximum Non-Repetitive Surge Current

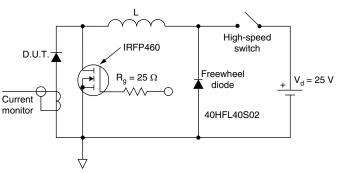


Fig. 8 - Unclamped Inductive Test Circuit

Note

- ⁽¹⁾ Formula used: $T_C = T_J (Pd + Pd_{REV}) \times R_{thJC}$; $\begin{array}{l} \mathsf{Pd} = \mathsf{forward} \ \mathsf{power} \ \mathsf{loss} = \mathsf{I}_{\mathsf{F}(\mathsf{AV})} \times \mathsf{V}_{\mathsf{FM}} \ \mathsf{at} \ (\mathsf{I}_{\mathsf{F}(\mathsf{AV})}/\mathsf{D}) \ (\mathsf{see} \ \mathsf{fig.} \ \mathsf{6}); \\ \mathsf{Pd}_{\mathsf{REV}} = \mathsf{inverse} \ \mathsf{power} \ \mathsf{loss} = \mathsf{V}_{\mathsf{R1}} \times \mathsf{I}_{\mathsf{R}} \ (\mathsf{1} - \mathsf{D}); \ \mathsf{I}_{\mathsf{R}} \ \mathsf{at} \ \mathsf{V}_{\mathsf{R1}} = \mathsf{rated} \ \mathsf{V}_{\mathsf{R}} \end{array}$

ORDERING INFORMATION TABLE

Device code	VS-	24	2	N	Q	030	PbF
	1	2	3	4	5	6	7
	1 - 2 - 3 - 4 - 5 - 6 - 7 -	Ave Pro N = Q = Vol	erage cu duct sili Not iso Schottl	ky rectifi ing (030	ting (x 1 htification er diode	0) n	

LINKS TO RELATED DOCUMENTS					
Dimensions		www.vishay.com/doc?95020			
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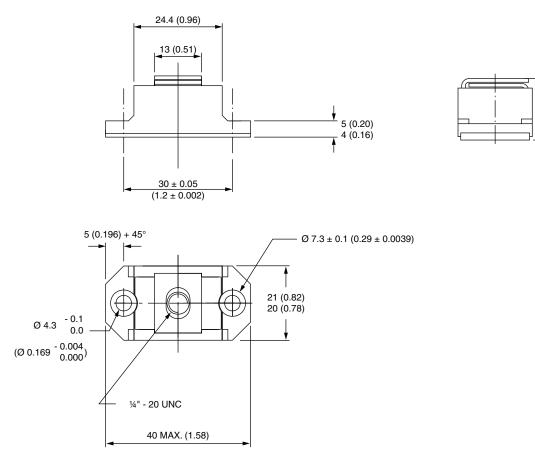
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17.5 (0.69) 16.5 (0.65)



DIMENSIONS in millimeters (inches)

SHAY





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