

Molding Type Module IGBT, Chopper in1 Package, 1200 V and 100 A



Dual	INT	A D	٨K

PRIMARY CHARACTERISTICS						
V _{CES}	1200 V					
I _C at T _C = 80 °C	100 A					
$V_{CE(on)}$ (typical) at $I_C = 100$ A, 25 °C	1.77 V					
Speed	8 kHz to 30 kHz					
Package	Dual INT-A-PAK					
Circuit configuration	Low side chopper					

FEATURES





- 10 µs short circuit capability
- V_{CE(on)} with positive temperature coefficient
- Maximum junction temperature 150 °C
- Low inductance case
- · Fast and soft reverse recovery antiparallel FWD
- Isolated copper baseplate using DCB (Direct Copper Bonding) technology
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

TYPICAL APPLICATIONS

- · AC inverter drives
- Switching mode power supplies
- Electronic welders

DESCRIPTION

Vishay's IGBT power module provides ultra low conduction loss as well as short circuit ruggedness. It is designed for applications such as general inverters and UPS.

ABSOLUTE MAXIMUM RATINGS (T _C = 25 °C unless otherwise noted)						
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS		
Collector to emitter voltage	V _{CES}		1200	V		
Gate to emitter voltage	V_{GES}		± 20	V		
O-llaston compat		T _C = 25 °C	200			
Collector current	Ic	T _C = 80 °C	100			
Pulsed collector current	I _{CM} ⁽¹⁾	$t_p = 1 \text{ ms}$	200	Α		
Diode continuous forward current	I _F	T _C = 80 °C	100			
Diode maximum forward current	I _{FM}	t _p = 1 ms	200			
Maximum power dissipation	P _D	T _J = 150 °C	833	W		
Short circuit withstand time	t _{SC}	T _J = 125 °C	10	μs		
RMS isolation voltage	V _{ISOL}	f = 50 Hz, t = 1 min	2500	V		
I ² t-value, diode	l ² t	V _R = 0 V, t = 10 ms, T _J = 125 °C	1700	A ² s		

Note

⁽¹⁾ Repetitive rating: pulse width limited by maximum junction temperature

IGBT ELECTRICAL SPECIFICATIONS (T _C = 25 °C unless otherwise noted)							
PARAMETER	SYMBOL	SYMBOL TEST CONDITIONS MIN. TYP. MA		MAX.	UNITS		
Collector to emitter breakdown voltage	V _{(BR)CES}	T _J = 25 °C	1200	-	-		
Collector to emitter voltage	V	$V_{GE} = 15 \text{ V}, I_{C} = 100 \text{ A}, T_{J} = 25 ^{\circ}\text{C}$	-	1.77	-	V	
Collector to enlitter voltage	V _{CE(on)}	V _{GE} = 15 V, I _C = 100 A, T _J = 125 °C	-	2.0	-	\ \	
Gate to emitter threshold voltage	V _{GE(th)}	$V_{CE} = V_{GE}$, $I_C = 2.0$ mA, $T_J = 25$ °C	5.0	6.2	7.0		
Collector cut-off current	I _{CES}	$V_{CE} = V_{CES}$, $V_{GE} = 0$ V, $T_{J} = 25$ °C	-	1	1.0	mA	
Gate to emitter leakage current	I _{GES}	$V_{GE} = V_{GES}$, $V_{CE} = 0$ V, $T_{J} = 25$ °C	-	-	400	nA	



PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Turn-on delay time	t _{d(on)}		-	75	-	
Rise time	t _r		-	40	-	- ns
Turn-off delay time	t _{d(off)}	$V_{CC} = 600 \text{ V}, I_{C} = 100 \text{ A}, R_{g} = 8 \Omega,$	-	400	-	
Fall time	t _f	$V_{GE} = \pm 15 \text{ V}, T_{J} = 25 \text{ °C}$	-	60	-	
Turn-on switching loss	E _{on}		-	6.0	-	mJ
Turn-off switching loss	E _{off}		-	3.7	-	IIIJ
Turn-on delay time	t _{d(on)}		-	80	-	
Rise time	t _r		-	50	-	ns
Turn-off delay time	t _{d(off)}	$V_{CC} = 600 \text{ V}, I_{C} = 100 \text{ A}, R_{g} = 8 \Omega,$	-	420	-	
Fall time	t _f	V _{GE} = ± 15 V, T _J = 125 °C	-	65	-	
Turn-on switching loss	E _{on}		-	8.4	-	m l
Turn-off switching loss	E _{off}		-	5.8	-	mJ
Input capacitance	C _{ies}		-	8.96	-	
Output capacitance	C _{oes}	$V_{GE} = 0 \text{ V}, V_{CE} = 25 \text{ V}, f = 1.0 \text{ MHz}$	-	0.96	-	nF
Reverse transfer capacitance	C _{res}		-	0.45	-	
SC data	I _{SC}	$t_{sc} \leq 10 \; \mu s, V_{GE} = 15 \; V, T_J = 125 \; ^{\circ}C,$ $V_{CC} = 900 \; V, V_{CEM} \leq 1200 \; V$	-	540	-	А
Internal gate resistance	R _{GINT}		-	5	-	Ω
Stray inductance	L _{CE}		-	-	20	nΗ
Module lead resistance, terminal to chip	R _{CC'+EE'}	T _C = 25 °C	-	0.35	-	mΩ

DIODE ELECTRICAL SPECIFICATIONS (T _C = 25 °C unless otherwise noted)								
PARAMETER	SYMBOL	TEST CONDIT	ONS	MIN.	TYP.	MAX.	UNITS	
Diede femueral veltere		I _E = 100 A	T _J = 25 °C	-	1.98	-	V	
Diode forward voltage	V _F	I _F = 100 A	T _J = 125 °C	-	2.21	-	ľ	
Diode reverse recovery charge	Q _{rr}	0	T _J = 25 °C	-	10	-		
blode reverse recovery charge			T _J = 125 °C	-	16	-	μC	
Diada mask variana vasariani arimont		$I_F = 100 \text{ A}, V_R = 600 \text{ V},$ $dI/dt = -3600 \text{ A/}\mu\text{s},$	T _J = 25 °C	-	90	-	^	
Diode peak reverse recovery current	Irr	$V_{GF} = -15 \text{ V}$	T _J = 125 °C	-	120	-	Α	
Diada waxaa waxaa waxaa waxaa	Г		T _J = 25 °C	-	3.5	-	I	
Diode reverse recovery energy	E _{rec}		T _J = 125 °C	-	6.0	-	mJ	

THERMAL AND MECHANICAL SPECIFICATIONS							
PARAMETER		SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Operating junction	temperature range	TJ		-40	-	150	- °C
Storage temperatu	re range	T _{STG}		-40	-	125	
Junction to case	IGBT part, per 1/2 module	В		-	-	0.15	
Junction to case	Diode part, per 1/2 module	R _{thJC}			-	0.29	
Case to sink		R _{thCS}	Conductive grease applied	-	0.035	-	
Mounting torque			Power terminal screw: M6	al screw: M6 2.5 t		.0 Nm	
			Mounting screw: M6	;	3.0 to 6.0)	INIII
Weight				300		g	

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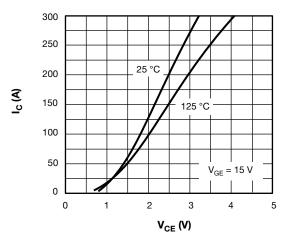


Fig. 1 - IGBT Typical Output Characteristics

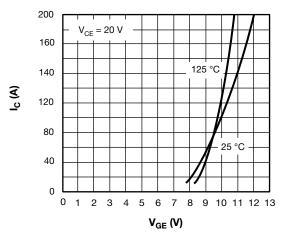


Fig. 2 - IGBT Typical Transfer Characteristics

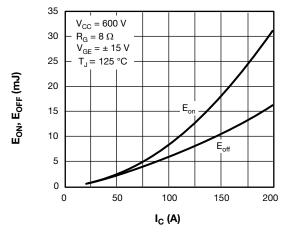


Fig. 3 - IGBT Switching Loss vs. I_C

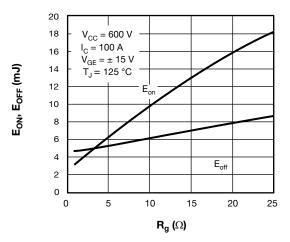


Fig. 4 - IGBT Switching Loss vs. Rq

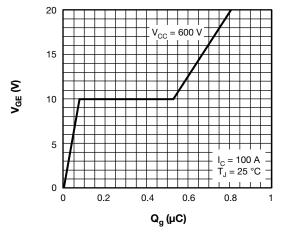


Fig. 5 - Gate Charge Characteristics

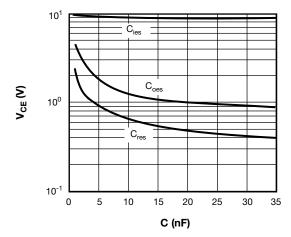
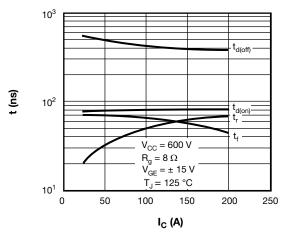


Fig. 6 - Typical Capacitance vs. Collector to Emitter Voltage







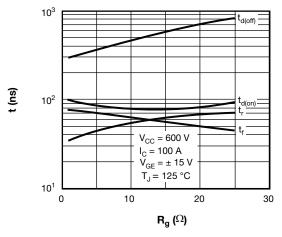


Fig. 8 - Typical Switching Times vs. Gate Resistance R_q

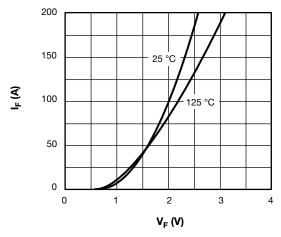


Fig. 9 - Typical Forward Characteristics (Diode)

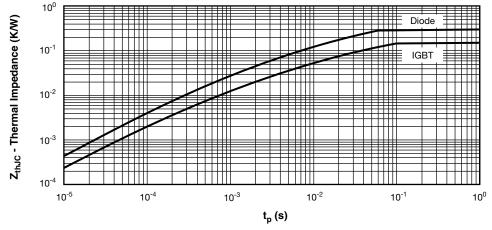
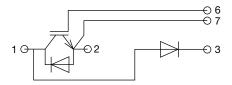


Fig. 10 - Transient Thermal Impedance



CIRCUIT CONFIGURATION

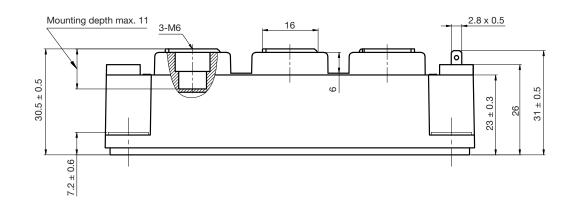


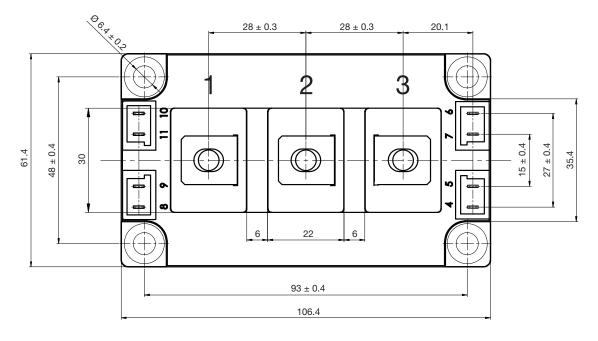
LINKS TO RELAT	ED DOCUMENTS
Dimensions	www.vishay.com/doc?95525



Double INT-A-PAK

DIMENSIONS in millimeters (inches)







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