VS-HFA140FA60

Vishay Semiconductors



HEXFRED[®] Ultrafast Soft Recovery Diode, 140 A



| PRIMARY CHARACTERISTICS | | | | | | |
|-------------------------------------|-----------------|--|--|--|--|--|
| V _R | 600 V | | | | | |
| V _F (typical) | 1.33 V | | | | | |
| t _{rr} (typical) | 43 ns | | | | | |
| $I_{F(DC)}$ at T_C , per module | 140 A at 110 °C | | | | | |
| $I_{F(AV)}$ at T_{C} , per module | 140 A at 96 °C | | | | | |
| Package | SOT-227 | | | | | |

FEATURES

- · Fast recovery time characteristic
- · Electrically isolated base plate
- Large creepage distance between terminal
- · Simplified mechanical designs, rapid assembly
- · Designed and qualified for industrial level
- UL approved file E78996
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

DESCRIPTION / APPLICATIONS

The dual diode series configuration VS-HFA140FA60 is used for output rectification or freewheeling/clamping operation and high voltage application.

The semiconductor in the SOT-227 package is isolated from the copper base plate, allowing for common heatsinks and compact assemblies to be built.

These modules are intended for general applications such as power supplies, battery chargers electronic welders, motor control and inverters.

| ABSOLUTE MAXIMUM RATINGS | | | | | | |
|--|-----------------------------------|--------------------------------------|-------------|-------|--|--|
| PARAMETER | SYMBOL | TEST CONDITIONS | MAX. | UNITS | | |
| Cathode to anode voltage | V _R | | 600 | V | | |
| Continuous forward current per leg | I_ | T 110 °C | 70 | | | |
| per module | lF | T _C = 110 °C | 140 | А | | |
| Single pulse forward current | I _{FSM} | T _J = 25 °C | 600 | | | |
| Maximum power dissipation, per leg | D- | T _C = 25 °C | 357 | w | | |
| Maximum power dissipation, per leg | PD | T _C = 110 °C | 114 | vv | | |
| RMS isolation voltage | VISOL | Any terminal to case, $t = 1$ minute | 2500 | V | | |
| Operating junction and storage temperature range | T _J , T _{Stg} | | -55 to +150 | °C | | |

| ELECTRICAL SPECIFICATIONS ($T_J = 25 \text{ °C}$ unless otherwise specified) | | | | | | | |
|--|-----------------|---|------|------|------|-------|--|
| PARAMETER | SYMBOL | TEST CONDITIONS | MIN. | TYP. | MAX. | UNITS | |
| Cathode to anode breakdown voltage | V_{BR} | I _R = 100 μA | 600 | - | - | | |
| Forward voltage, per leg V _{FM} | | I _F = 60 A | - | 1.33 | 1.70 | V | |
| | N/ | I _F = 120 A | - | 1.56 | 2.04 | | |
| | VFM | I _F = 60 A, T _J = 125 °C | - | 1.24 | - | | |
| | | I _F = 60 A, T _J = 150 °C | - | 1.19 | - | | |
| | | V _R = V _R rated | - | 2.5 | 20 | μA | |
| Reverse leakage current, per leg | I _{RM} | $T_J = 125 \text{ °C}, V_R = V_R \text{ rated}$ | - | 0.8 | 2 | 0 | |
| | | $T_J = 150 \text{ °C}, V_R = V_R \text{ rated}$ | - | 3 | 9 | - mA | |

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| DYNAMIC RECOVERY CHARACTERISTICS ($T_J = 25$ °C unless otherwise specified) | | | | | | | |
|---|------------------|--|---|------|------|-------|----|
| PARAMETER | SYMBOL | TEST CO | MIN. | TYP. | MAX. | UNITS | |
| | | $I_F = 1 \text{ A}; \text{ d}I_F/\text{d}t = 20$ | 00 A/µs; V _R = 30 V | - | 43 | - | ns |
| Reverse recovery time, per leg | t _{rr} | T _J = 25 °C | I _F = 50 A dI _F /dt = - 200 A/μs V _B = 200 V | - | 90 | - | |
| | | T _J = 125 °C | | - | 150 | - | |
| Back receiver ourrent per les | I _{RRM} | T _J = 25 °C | | - | 9.5 | - | А |
| Peak recovery current, per leg | | T _J = 125 °C | | - | 17 | - | ~ |
| Reverse recovery charge, per leg | Q _{rr} | T _J = 25 °C | | - | 400 | - | nC |
| neverse recovery charge, per leg | | T _J = 125 °C | | - | 1180 | - | no |
| Junction capacitance, per leg | CT | V _R = 600 V | | - | 67 | - | pF |

| THERMAL - MECHANICAL SPECIFICATIONS | | | | | | | |
|---|-------------------|-----------------------|------|------|------------|-------------|--|
| PARAMETER | SYMBOL | TEST CONDITIONS | MIN. | TYP. | MAX. | UNITS | |
| Junction to case, single leg conducting | D | | - | - | 0.35 | | |
| Junction to case, both legs conducting | R _{thJC} | | - | - | 0.175 | °C/W | |
| Case to heatsink | R _{thCS} | Flat, greased surface | - | 0.05 | - | | |
| Weight | | | - | 30 | - | g | |
| Mounting torque | | Torque to terminal | - | - | 1.1 (9.7) | Nm (lbf.in) | |
| Mounting torque | | Torque to heatsink | - | - | 1.8 (15.9) | Nm (lbf.in) | |
| Case style | | | | SC | DT-227 | | |

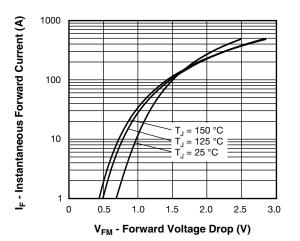


Fig. 1 - Typical Forward Voltage Drop Characteristics (Per Leg)

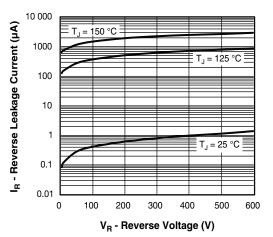
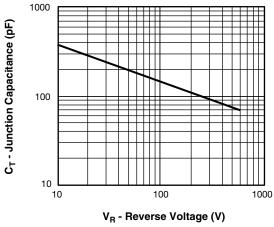
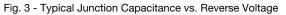


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

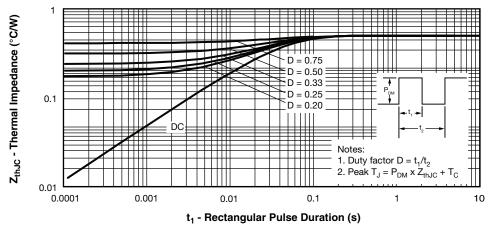




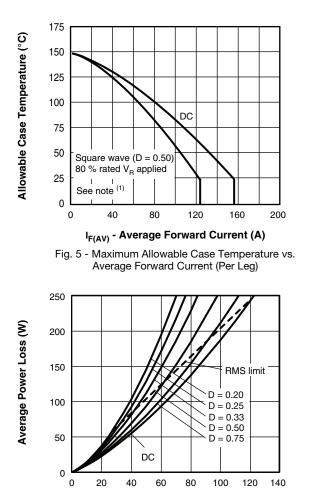
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VS-HFA140FA60

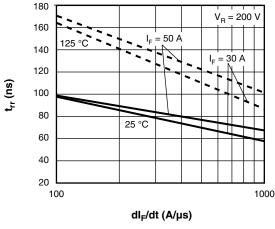
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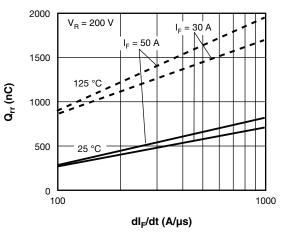


Fig. 8 - Typical Stored Charge vs. dl_F/dt

Note

⁽¹⁾ Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$;

 $\begin{array}{l} Pd = \textit{forward power loss} = I_{F(AV)} \times V_{FM} \textit{ at } (I_{F(AV)}/D) \textit{ (see fig. 5);} \\ Pd_{REV} = \textit{inverse power loss} = V_{R1} \times I_{R} \textit{ (1 - D); } I_{R} \textit{ at } V_{R1} = \textit{rated } V_{R} \end{array}$

IF(AV) - Average Forward Current (A)

Fig. 6 - Forward Power Loss Characteristics (Per Leg)

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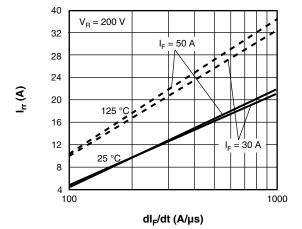


Fig. 9 - Typical Peak Recovery Current vs. dl_F/dt

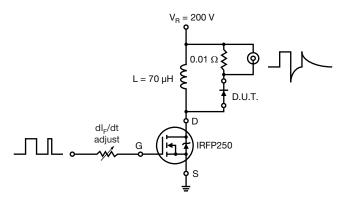
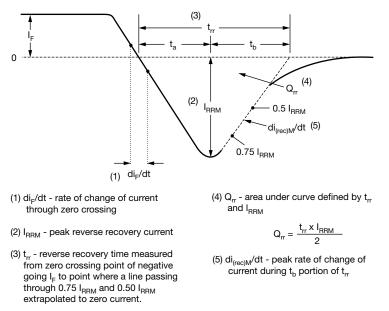
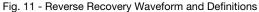


Fig. 10 - Reverse Recovery Parameter Test Circuit





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ORDERING INFORMATION TABLE

| Device code | VS- | HF | Α | 140 | F | Α | 60 |
|-------------|-----|--------|----------|---------------------|-----------|-----------|---------|
| | 1 | 2 | 3 | 4 | 5 | 6 | 7 |
| | 1 - | - Visł | nay Sen | niconduc | ctors pro | oduct | |
| | 2 - | HEX | KFRED® | [®] family | | | |
| | 3 - | Pro | cess de | signator | (A = ele | ectron ir | radiate |
| | 4 - | Ave | rage cu | rrent (14 | 40 = 140 |) A) | |
| | 5 - | Circ | uit conf | iguratior | n (two se | eparate | diodes |
| | 6 - | Pac | kage in | dicator (| SOT-22 | 7 stand | ard ins |
| | 7 - | · Volt | age rati | ng (60 = | = 600 V) |) | |

| CIRCUIT CONFIGURATION | | | | | | |
|--|-------------------------------|-----------------|--|--|--|--|
| CIRCUIT | CIRCUIT CONFIGURATION CODE | CIRCUIT DRAWING | | | | |
| Two separate diodes, parallel pin-out | F | Lead Assignment | | | | |

| LINKS TO RELATED DOCUMENTS | | | | | |
|----------------------------|--------------------------|--|--|--|--|
| Dimensions | www.vishay.com/doc?95423 | | | | |
| Part marking information | www.vishay.com/doc?95425 | | | | |



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