

Vishay Siliconix

# N-Channel Reduced $Q_g$ , Fast Switching MOSFET

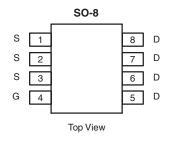
PRODUCT SUMMARY					
V <sub>DS</sub> (V)	<b>R<sub>DS(on)</sub> (</b> Ω <b>)</b>	I <sub>D</sub> (A)			
30	0.010 at V <sub>GS</sub> = 10 V	13			
	0.0135 at V <sub>GS</sub> = 4.5 V	11			

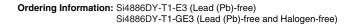
#### FEATURES

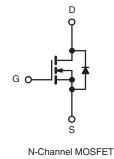
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- Halogen-free According to IEC 61249-2-21
  Definition
- TrenchFET<sup>®</sup> Power MOSFETs
  - High-Efficiency PWM Optimized
- Compliant to RoHS Directive 2002/95/EC









<b>ABSOLUTE MAXIMUM RATINGS</b>	$T_A = 25 \ ^{\circ}C$ , unless	ss otherwise n	oted			
Parameter		Symbol	10 s	Steady State	Unit	
Drain-Source Voltage		V <sub>DS</sub>	30		V	
Gate-Source Voltage		V <sub>GS</sub>	± 20			
Continuous Drain Current $(T_J = 150 \ ^{\circ}C)^a$	T <sub>A</sub> = 25 °C	– I <sub>D</sub>	13	9.5		
	T <sub>A</sub> = 70 °C		10.5	7.6	•	
Pulsed Drain Current		I <sub>DM</sub>	± 50		A	
Continuous Source Current (Diode Conduction) <sup>a</sup>		۱ <sub>S</sub>	2.60	1.40		
Maximum Power Dissipation <sup>a</sup>	T <sub>A</sub> = 25 °C	– P <sub>D</sub>	2.95	1.56	W	
	T <sub>A</sub> = 70 °C		1.90	1.0	vv	
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	- 55 to 150		°C	

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
Maximum Junction-to-Ambient (MOSFET) <sup>a</sup>	t ≤ 10 s	- R <sub>thJA</sub> R <sub>thJF</sub>	35	42	
	Steady State		68	80	°C/W
Maximum Junction-to-Foot (Drain)	Steady State		18	23	

Notes:

a. Surface Mounted on 1" x 1" FR4 board.



Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit
Static				•		
Gate Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}$ , $I_D = 250 \ \mu A$	0.80			V
Gate-Body Leakage	I <sub>GSS</sub>	$V_{DS} = 0 V, V_{GS} = \pm 20 V$			± 100	nA
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	$V_{DS} = 24 V, V_{GS} = 0 V$			1	μΑ
		$V_{DS} = 24 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 70 ^{\circ}\text{C}$			5	
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \ge 5 \text{ V}, \text{ V}_{GS} = 10 \text{ V}$	40			А
Drain-Source On-State Resistance <sup>a</sup>	Б	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 13 A		0.0078	0.010	0
	R <sub>DS(on)</sub>	V <sub>GS</sub> = 4.5 V, I <sub>D</sub> = 11 A 0.010		0.0105	0.0135	Ω
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	$V_{DS} = 15 \text{ V}, \text{ I}_{D} = 13 \text{ A}$		38		S
Diode Forward Voltage <sup>a</sup>	V <sub>SD</sub>	$I_{S} = 2.6 \text{ A}, V_{GS} = 0 \text{ V}$		0.74	1.1	V
Dynamic <sup>b</sup>			•	•		
Total Gate Charge	Qg			14.5	20	
Gate-Source Charge	Q <sub>gs</sub>	$V_{DS}$ = 15 V, $V_{GS}$ = 5.0 V, $I_{D}$ = 13 A		3.2		nC
Gate-Drain Charge	Q <sub>gd</sub>			4.3		
Turn-On Delay Time	t <sub>d(on)</sub>			14	20	
Rise Time	t <sub>r</sub>	$V_{DD}$ = 15 V, $R_L$ = 15 $\Omega$		5	10	L
Turn-Off Delay Time	t <sub>d(off)</sub>	$\text{I}_\text{D}\cong \text{1}$ A, $\text{V}_\text{GEN}$ = 10 V, $\text{R}_\text{g}$ = 6 $\Omega$		42	80	ns
Fall Time	t <sub>f</sub>			18	30	
Source-Drain Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> = 2.6 A, dl/dt = 100 A/μs		40	70	

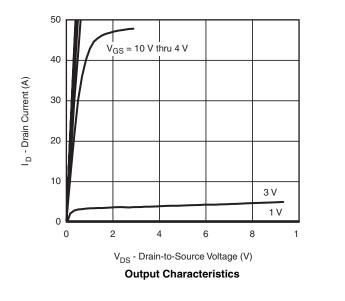
Notes:

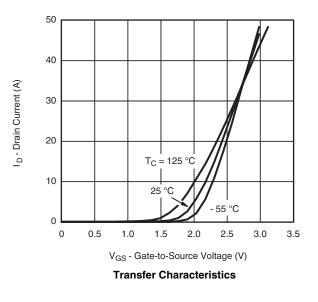
a. Pulse test; pulse width  $\leq$  300 µs, duty cycle  $\leq$  2 %.

b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

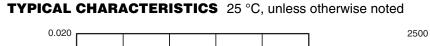
#### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



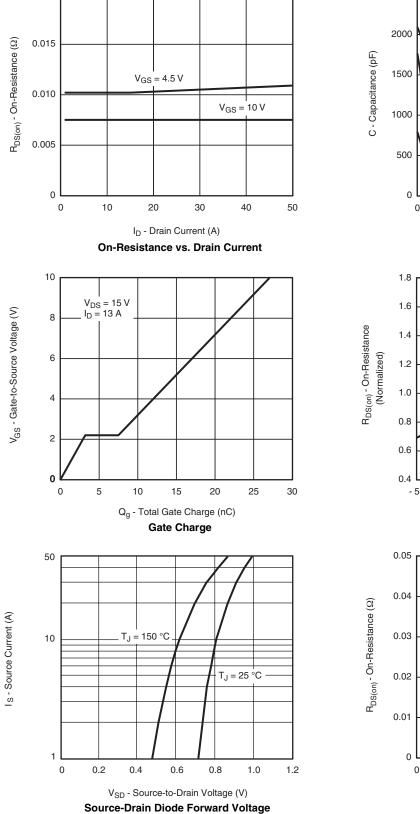


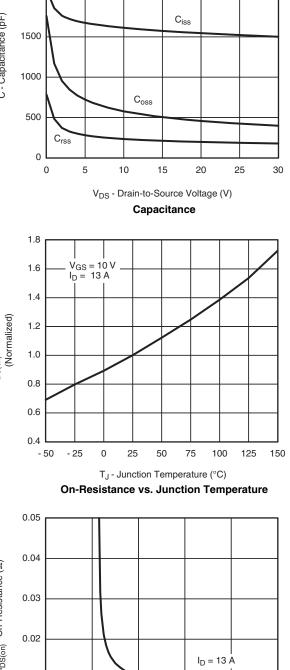
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V<sub>GS</sub> - Gate-to-Source Voltage (V)

On-Resistance vs. Gate-to-Source Voltage

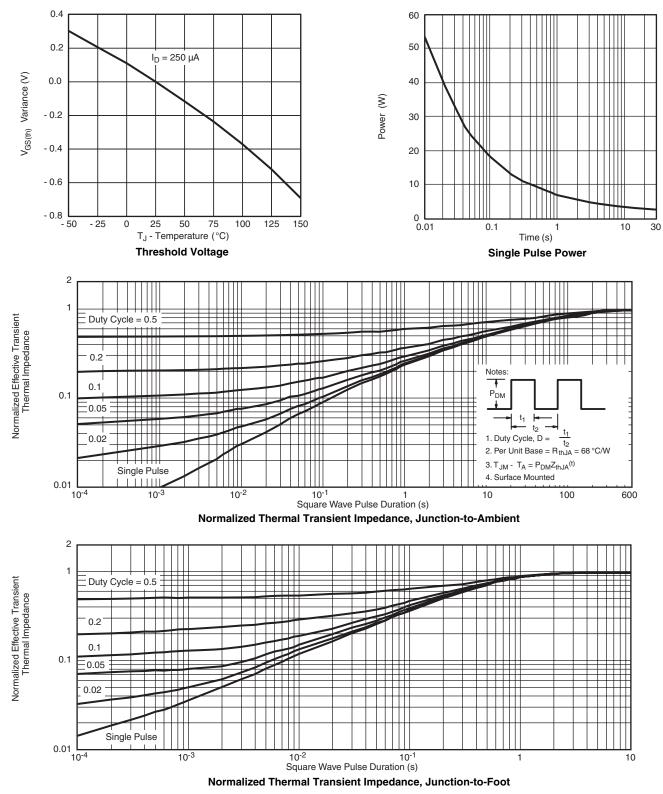
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