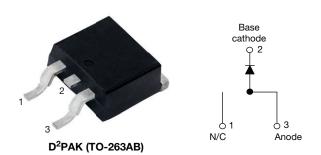
VS-6TQ35S-M3, VS-6TQ40S-M3, VS-6TQ45S-M3

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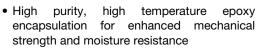
High Performance Schottky Rectifier, 6 A



PRIMARY CHARACTERISTICS				
I _{F(AV)} 6 A				
V _R	35 V, 40 V, 45 V			
V _F at I _F	0.53 V			
I _{RM}	7 mA at 125 °C			
T _J max.	175 °C			
E _{AS}	8 mJ			
Package	D ² PAK (TO-263AB)			
Circuit configuration	Single			

FEATURES

- 175 °C T_J operation
- High frequency operation
- Low forward voltage drop





- Guard ring for enhanced ruggedness and long term reliability
- Meets MSL level 1, per J-STD-020, LF maximum peak of 245 °C
- Designed and qualified according to JEDEC®-JESD 47
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

DESCRIPTION

The VS-6TQ... Schottky rectifier series has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 175 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS						
SYMBOL	CHARACTERISTICS	CHARACTERISTICS VALUES UNITS				
I _{F(AV)}	Rectangular waveform	6	Α			
V_{RRM}	Range	35 to 45	V			
I _{FSM}	t _p = 5 µs sine	690	Α			
V _F	6 A _{pk} , T _J = 125 °C	0.53	V			
TJ	Range	-55 to +175	°C			

VOLTAGE RATINGS					
PARAMETER	SYMBOL	VS-6TQ035S-M3	VS-6TQ040S-M3	VS-6TQ045S-M3	UNITS
Maximum DC reverse voltage	V_R	35	40	45	W
Maximum working peak reverse voltage	V_{RWM}	33	40	45	V

ABSOLUTE MAXIMUM RATINGS							
PARAMETER	SYMBOL	TEST COND	ITIONS	VALUES	UNITS		
Maximum average forward current See fig. 5	I _{F(AV)}	50 % duty cycle at T _C = 164 °C	6				
Maximum peak one cycle		5 μs sine or 3 μs rect. pulse	Following any rated load	690	Α		
non-repetitive surge current See fig. 7	I _{FSM}	10 ms sine or 6 ms rect. pulse	condition and with rated V _{RRM} applied	140			
Non-repetitive avalanche energy	E _{AS}	T _J = 25 °C, I _{AS} = 1.20 A, L = 11.10 mH		8	mJ		
Repetitive avalanche current	I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T_J maximum $V_A = 1.5 \times V_R$ typical		1.20	А		

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ELECTRICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST C	ONDITIONS	VALUES	UNITS	
Maximum forward voltage drop		6 A	T _{.1} = 25 °C	0.60		
	V (1)	12 A	- IJ = 25 C	0.73	V	
See fig. 1	V _{FM} ⁽¹⁾	6 A	T 105 °C	0.53		
		12 A	T _J = 125 °C	0.64		
Maximum reverse leakage current	1 (1)	T _J = 25 °C	V Datad V	0.8	mA	
See fig. 2	I _{RM} ⁽¹⁾	T _J = 125 °C	V _R = Rated V _R	7		
Threshold voltage	V _{F(TO)}	T T manyimayana		0.35	V	
Forward slope resistance	r _t	I j = I j I laxii lulli		18.23	mΩ	
Maximum junction capacitance	C _T	V _R = 5 V _{DC} (test signal ra	400	pF		
Typical series inductance	L _S	Measured lead to lead 5 mm from package body 8.0			nΗ	
Maximum voltage rate of change	dV/dt	Rated V _R 10 000 V				

Note

 $^{^{(1)}\,}$ Pulse width $<300~\mu s,$ duty cycle <2~%

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum junction and storage temperature range		T _J , T _{Stg}		-55 to 175	°C	
Maximum thermal resistance, junction to case		R _{thJC}	DC operation See fig. 4	2.2	°C/W	
Typical thermal resistance, case to heatsink		R _{thCS}	Mounting surface, smooth, and greased	0.50	C/VV	
Approximate weight				2	g	
Approximate weight				0.07	OZ.	
Maunting targue	minimum			6 (5)	kgf · cm	
Mounting torque	maximum			12 (10)	(lbf ⋅ in)	
				6TQ	035S	
Marking device			Case style D ² PAK (TO-263AB)	6TQ	040S	
				6TQ	045S	

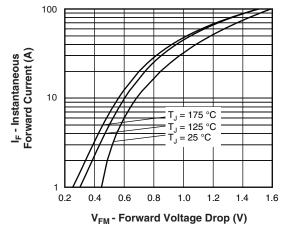


Fig. 1 - Maximum Forward Voltage Drop Characteristics

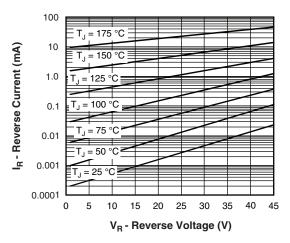


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

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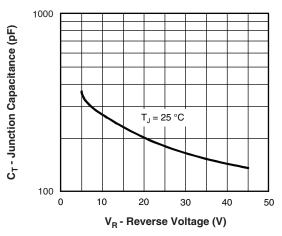


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

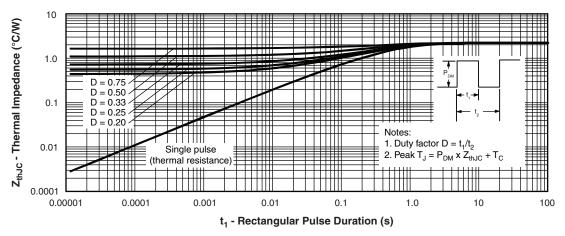


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

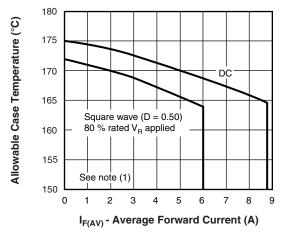


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

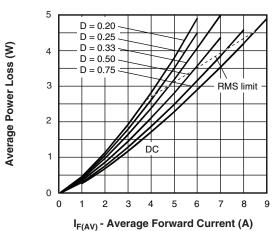
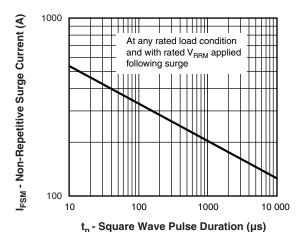


Fig. 6 - Forward Power Loss Characteristics

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7 M : N D ::: 0 0

Fig. 7 - Maximum Non-Repetitive Surge Current

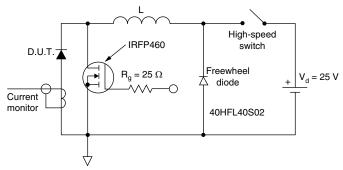
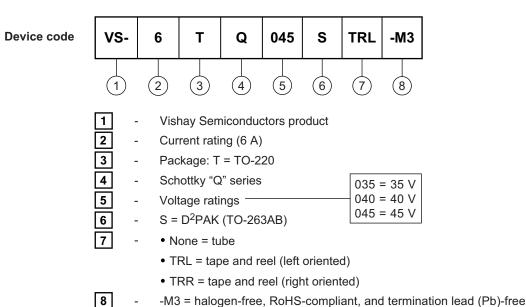


Fig. 8 - Unclamped Inductive Test Circuit

Note

⁽¹⁾ Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{th,JC}$; $Pd = forward power loss = I_{F(AV)} \times V_{FM} at (I_{F(AV)}/D)$ (see fig. 6); $Pd_{REV} = inverse power loss = V_{R1} \times I_R (1 - D)$; I_R at $V_{R1} = 80 \%$ rated V_R

ORDERING INFORMATION TABLE





VS-6TQ35S-M3, VS-6TQ40S-M3, VS-6TQ45S-M3

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ORDERING INFORMAT	ORDERING INFORMATION						
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION				
VS-6TQ035S-M3	50	1000	Antistatic plastic tubes				
VS-6TQ035STRR-M3	800	800	13" diameter reel				
VS-6TQ035STRL-M3	800	800	13" diameter reel				
VS-6TQ040S-M3	50	1000	Antistatic plastic tubes				
VS-6TQ040STRR-M3	800	800	13" diameter reel				
VS-6TQ040STRL-M3	800	800	13" diameter reel				
VS-6TQ045S-M3	50	1000	Antistatic plastic tubes				
VS-6TQ045STRR-M3	800	800	13" diameter reel				
VS-6TQ045STRL-M3	800	800	13" diameter reel				

LINKS TO RELATED DOCUMENTS				
Dimensions	www.vishay.com/doc?96164			
Part marking information	www.vishay.com/doc?95444			
Packaging information	www.vishay.com/doc?96424			



Vishay Semiconductors

D²PAK

DIMENSIONS in millimeters and inches



SYMBOL	MILLIMETERS		INCHES		NOTES	SYMBOL	MILLIM	ETERS	INC	HES	NOTES
STIVIBUL	MIN.	MAX.	MIN.	MAX.	NOIES	STWIDOL	MIN.	MAX.	MIN.	MAX.	NOTES
Α	4.06	4.83	0.160	0.190		D1	6.86	8.00	0.270	0.315	3
A1	0.00	0.254	0.000	0.010		Е	9.65	10.67	0.380	0.420	2, 3
b	0.51	0.99	0.020	0.039		E1	7.90	8.80	0.311	0.346	3
b1	0.51	0.89	0.020	0.035	4	е	2.54	BSC	0.100) BSC	
b2	1.14	1.78	0.045	0.070		Н	14.61	15.88	0.575	0.625	
b3	1.14	1.73	0.045	0.068	4	L	1.78	2.79	0.070	0.110	
С	0.38	0.74	0.015	0.029		L1	-	1.65	-	0.066	3
c1	0.38	0.58	0.015	0.023	4	L2	1.27	1.78	0.050	0.070	
c2	1.14	1.65	0.045	0.065		L3	0.25	BSC	0.010	BSC	
D	8.51	9.65	0.335	0.380	2	L4	4.78	5.28	0.188	0.208	

Notes

- (1) Dimensioning and tolerancing per ASME Y14.5 M-1994
- (2) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body
- (3) Thermal pad contour optional within dimension E, L1, D1 and E1
- (4) Dimension b1 and c1 apply to base metal only
- (5) Datum A and B to be determined at datum plane H
- (6) Controlling dimension: inch
- (7) Outline conforms to JEDEC® outline TO-263AB

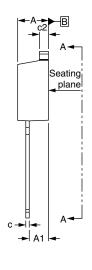


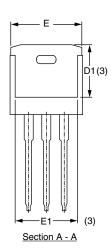
Vishay Semiconductors

TO-262

DIMENSIONS in millimeters and inches

Modified JEDEC outline TO-262 (Datum A) (2) (3) (3) L1 D D D C C C C A (2) A (2) A (3) L2 B B B B C C C A (2) A (2)



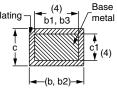


⊕ 0.010**⋒**|A**⋒**|B

Lead assignments



<u>Diodes</u>
1. - Anode (two die)/open (one die)
2., 4. - Cathode
3. - Anode



Section B - B and C - C Scale: None

CYMPOL	MILLIN	METERS	INCH	IES	NOTES
SYMBOL	MIN.	MAX.	MIN.	MAX.	NOTES
Α	4.06	4.83	0.160	0.190	
A1	2.03	3.02	0.080	0.119	
b	0.51	0.99	0.020	0.039	
b1	0.51	0.89	0.020	0.035	4
b2	1.14	1.78	0.045	0.070	
b3	1.14	1.73	0.045	0.068	4
С	0.38	0.74	0.015	0.029	
c1	0.38	0.58	0.015	0.023	4
c2	1.14	1.65	0.045	0.065	
D	8.51	9.65	0.335	0.380	2
D1	6.86	8.00	0.270	0.315	3
E	9.65	10.67	0.380	0.420	2, 3
E1	7.90	8.80	0.311	0.346	3
е	2.54	BSC	0.100	BSC	
L	13.46	14.10	0.530	0.555	
L1	-	1.65	-	0.065	3
L2	3.56	3.71	0.140	0.146	

Notes

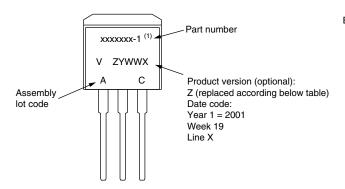
- (1) Dimensioning and tolerancing as per ASME Y14.5M-1994
- (2) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body
- $^{(3)}$ Thermal pad contour optional within dimension E, L1, D1 and E1
- (4) Dimension b1 and c1 apply to base metal only
- (5) Controlling dimension: inches
- (6) Outline conform to JEDEC TO-262 except A1 (maximum), b (minimum) and D1 (minimum) where dimensions derived the actual package outline



Part Marking Information

Vishay Semiconductors

TO-262



Example: This is a xxxxxxx-1 ⁽¹⁾ with assembly lot code AC, assembled on WW 19, 2001

in the assembly line "X"

Note

(1) If part number contain "H" as last digit, product is AEC-Q101 qualified

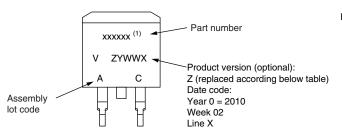
ENVIRONMENTAL NAMING CODE (Z)	PRODUCT DEFINITION		
A	Termination lead (Pb)-free		
В	Totally lead (Pb)-free		
E	RoHS-compliant and termination lead (Pb)-free		
F	RoHS-compliant and totally lead (Pb)-free		
М	Halogen-free, RoHS-compliant and termination lead (Pb)-free		
N	Halogen-free, RoHS-compliant and totally lead (Pb)-free		
G Green			



Part Marking Information

Vishay Semiconductors

D²PAK



Example: This is a xxxxxx ⁽¹⁾ with assembly lot code AC, assembled on WW 02, 2010

Note

(1) If part number contain "H" as last digit, product is AEC-Q101 qualified

ENVIRONMENTAL NAMING CODE (Z) PRODUCT DEFINITION			
А	Termination lead (Pb)-free		
В	Totally lead (Pb)-free		
E	RoHS-compliant and termination lead (Pb)-free		
F	RoHS-compliant and totally lead (Pb)-free		
M	Halogen-free, RoHS-compliant, and termination lead (Pb)-free		
N	Halogen-free, RoHS-compliant, and totally lead (Pb)-free		
G	Green		



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