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Please note: As part of the Fairchild Semiconductor integration, some of the Fairchild orderable part numbers will need to change in order to meet ON Semiconductor's system requirements. Since the ON Semiconductor product management systems do not have the ability to manage part nomenclature that utilizes an underscore (_), the underscore (_) in the Fairchild part numbers will be changed to a dash (-). This document may contain device numbers with an underscore (_). Please check the ON Semiconductor website to verify the updated device numbers. The most current and up-to-date ordering information can be found at www.onsemi.com. Please email any questions regarding the system integration to Fairchild_questions@onsemi.com.

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FAIRCHILD

SEMICONDUCTOR®

FDH5500_F085

N-Channel UltraFET Power MOSFET

55V, 75A, 7m Ω

Features

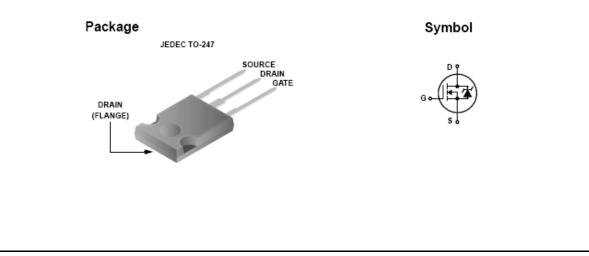
- Typ r_{DS(on)} = 5.2mΩ at V_{GS} = 10V, I_D = 75A
- Typ Q_{g(10)} = 118nC at V_{GS} = 10V
- Simulation Models
 -Temperature Compensated PSPICE and SABERTM Models
- Peak Current vs Pulse Width Curve
- UIS Rating Curve
- Related Literature
 - -TB334, "Guidelines for Soldering Surface Mount Componets to PC Boards"
- Qualified to AEC Q101
- RoHS Compliant

Applications

- DC Linear Mode Control
- Solenoid and Motor Control
- Switching Regulators
- Automotive Systems

October 2008





MOSFET Maximum Rating	S $T_C = 25^{\circ}C$ unless otherwise noted
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Symbol	Parameter		Ratings	Units
V _{DSS}	Drain to Source Voltage	(Note 1)	55	V
V _{DGR}	Drain to Gate Voltage (R_{GS} = 20k Ω)	(Note 1)	55	V
V _{GS}	Gate to Source Voltage	±20	V	
	Drain Current Continuous (T _C < 135 ^o C, V _{GS} = 10V)		75	Α
D	Pulsed		See Figure 4	A
E _{AS}	Single Pulse Avalanche Energy	(Note 2)	864	mJ
П	Power Dissipation		375	W
P _D	Dreate above 25°C		2.5	W/ºC
T _J , T _{STG}	Operating and Storage Temperature		-55 to + 175	
ΤL	Max. Lead Temp. for Soldering (at 1.6mm from case for 10sec)		300	°C
T _{pkg}	Max. Package Temp. for Soldering (Package Body for 10sec)		260	

Thermal Characteristics

$R_{ ext{ heta}JC}$	Thermal Resistance Junction to Case	0.4	°C/W
R_{\thetaJA}	Thermal Resistance Junction to Ambient TO-247, 1in ² copper pad area	30	°C/W

Package Marking and Ordering Information

Device Marking	Device	Package	Reel Size	Tape Width	Quantity
FDH5500	FDH5500_F085	TO-247	Tube	N/A	30 units

Electrical Characteristics T_{C} = 25°C unless otherwise noted

Symbol	Parameter	Test Conditions	Min	Тур	Max	Units

Off Characteristics

B _{VDSS}	Drain to Source Breakdown Voltage	$I_D = 250 \mu A, V_{GS} =$	$I_{D} = 250 \mu A, V_{GS} = 0V$		-	-	V
1	Zara Cata Valtaga Drain Current	V_{DS} = 50V, V_{GS} =	0V	-	-	1	
DSS	I _{DSS} Zero Gate Voltage Drain Current	V _{DS} = 45V	$T_{\rm C} = 150^{\rm o}{\rm C}$	-	-	250	μΑ
I _{GSS}	Gate to Source Leakage Current	V _{GS} = ±20V		-	-	±100	nA

On Characteristics

V _{GS(th)}	Gate to Source Threshold Voltage	$V_{GS} = V_{DS}, I_D = 250 \mu A$	2	2.9	4	V
r _{DS(on)}	Drain to Source On Resistance	I _D = 75A, V _{GS} = 10V	-	5.2	7	mΩ

Dynamic Characteristics

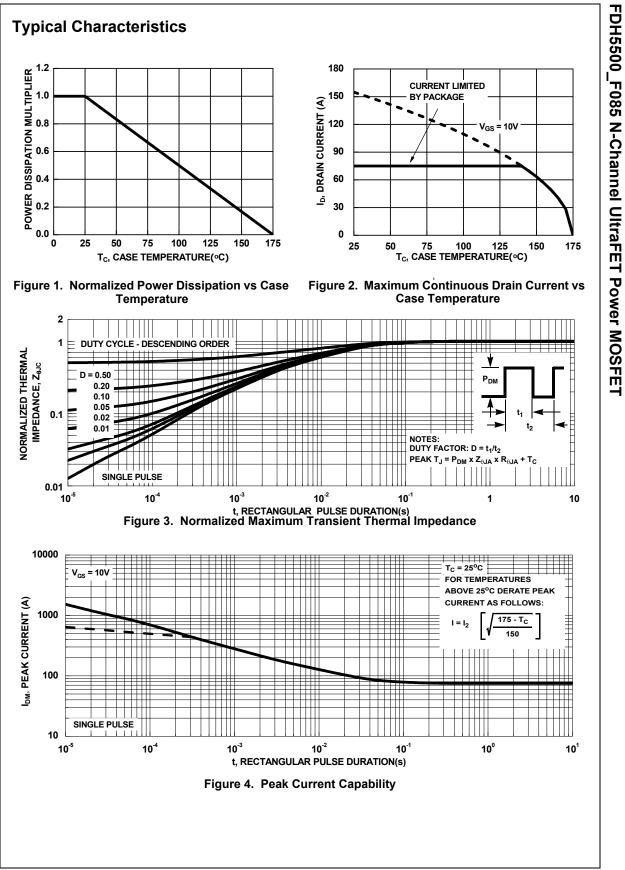
Ciss	Input Capacitance		$V_{DS} = 25V, V_{GS} = 0V,$ f = 1MHz		3565	-	pF
C _{oss}	Output Capacitance	── V _{DS} = 25V, V _{GS} = f = 1MHz			1310	-	pF
C _{rss}	Reverse Transfer Capacitance			-	395	-	pF
Q _{g(TOT)}	Total Gate Charge at 20V	V _{GS} = 0 to 20V		-	206	268	nC
Q _{g(10)}	Total Gate Charge at 10V	V _{GS} = 0 to 10V	$V_{DD} = 30V$	-	118	153	nC
Q _{g(TH)}	Threshold Gate Charge	V _{GS} = 0 to 2V	$I_D = 75A$ $R_1 = 0.4\Omega$	-	6.2	8.1	nC
Q _{gs}	Gate to Source Gate Charge		$I_0 = 1.0 \text{mA}$	-	17.8	-	nC
Q _{gd}	Gate to Drain "Miller" Charge		y -	-	51	-	nC

FDH5500_F085 Rev. A1

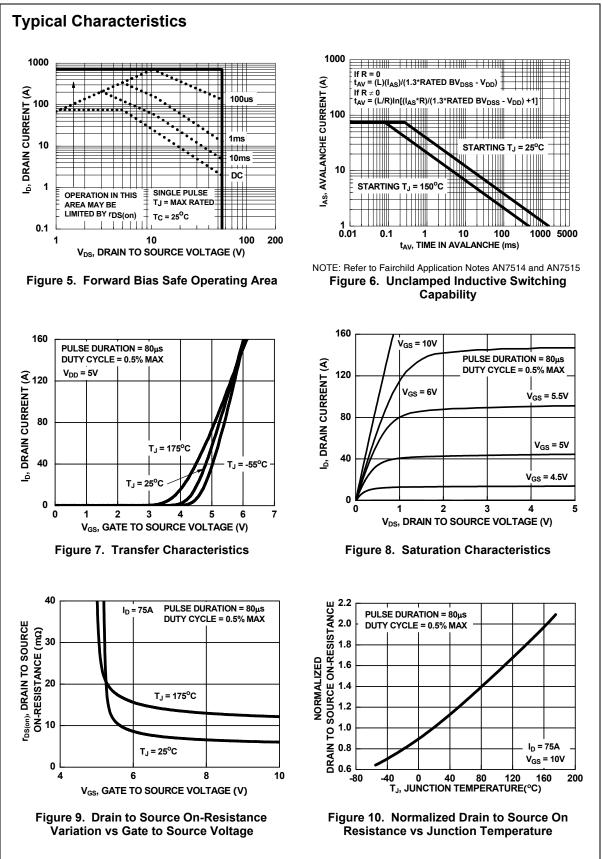
Symbol	Parameter	Test Conditions	Min	Тур	Max	Units
Switch	ing Characteristics					
t _{on}	Turn-On Time		-	-	185	ns
t _{d(on)}	Turn-On Delay Time		-	13.7	-	ns
t _r	Rise Time	$V_{DD} = 30V, I_D = 75A,$	-	102	-	ns
t _{d(off)}	Turn-Off Delay Time	$\frac{R_{L} = 0.4\Omega, V_{GS} = 10V,}{R_{GS} = 2.5\Omega}$	-	34	-	ns
t _f	Fall Time		-	22	-	ns
t _{off}	Turn-Off Time		-	-	91	ns
V _{SD}	OURCE DIODE Characteristics	I _{SD} = 75A	-	1	1.25	V
t _{rr}	Reverse Recovery Time	I _E = 75A, dI _{SD} /dt = 100A/μs	-	60	78	ns
Q _{rr}	Reverse Recovery Charge		-	77	100	nC

л Э D П П 2 . 4 D r MOSFET

This product has been designed to meet the extreme test conditions and environment demanded by the automotive industry. For a copy of the requirements, see AEC Q101 at: http://www.aecouncil.com/ All Fairchild Semiconductor products are manufactured, assembled and tested under ISO9000 and QS9000 quality systems certification.

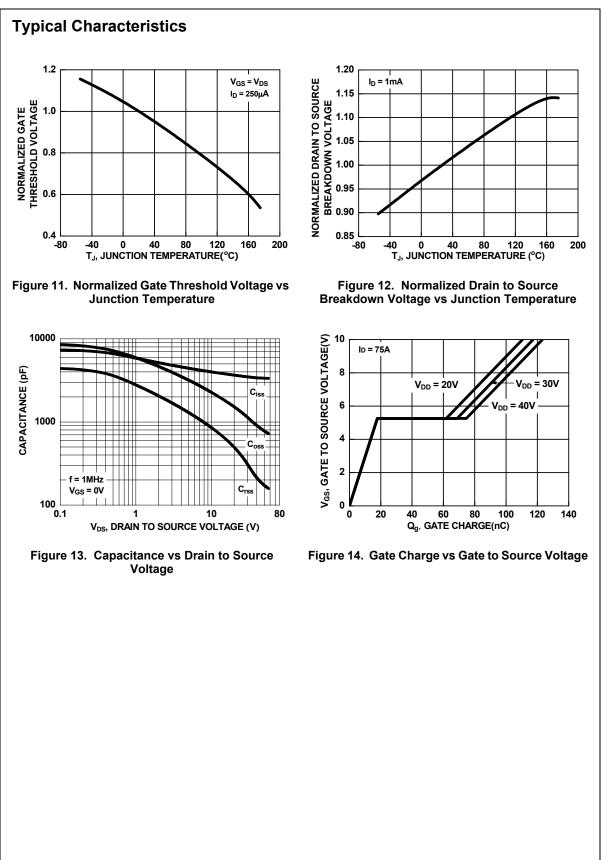


FDH5500_F085 Rev. A1



FDH5500_F085 N-Channel UltraFET Power MOSFET

FDH5500_F085 Rev. A1



FDH5500_F085 N-Channel UltraFET Power MOSFET



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Preliminary	First Production	Datasheet contains preliminary data; supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.
No Identification Needed	Full Production	Datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve the design.
Obsolete	Not In Production	Datasheet contains specifications on a product that is discontinued by Fairchild Semiconductor. The datasheet is for reference information only.
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