

MATERIAL NUMBER	# OF COLUMNS	# OF DIFF PAIR	DIM 'A' MAX	DIM 'B'	PTH ϕ
170510-18**	8	24	15.20	13.30	0.46+0.05
170510-11**	10	30	19.00	17.10	
170510-18**	8	24	15.20	13.30	0.39+0.05
170510-11**	10	30	19.00	17.10	

170510-1***

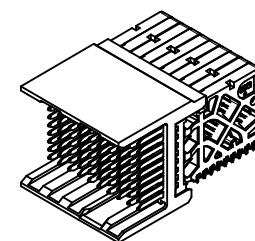
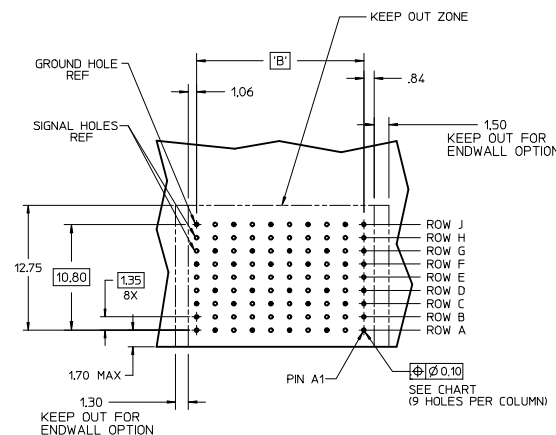
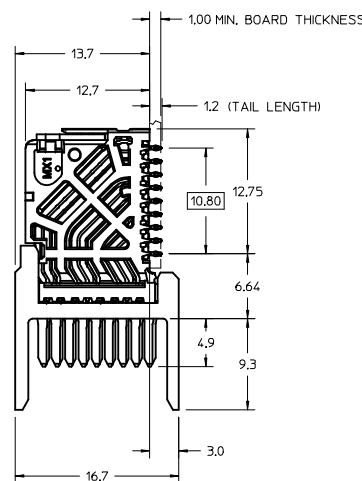
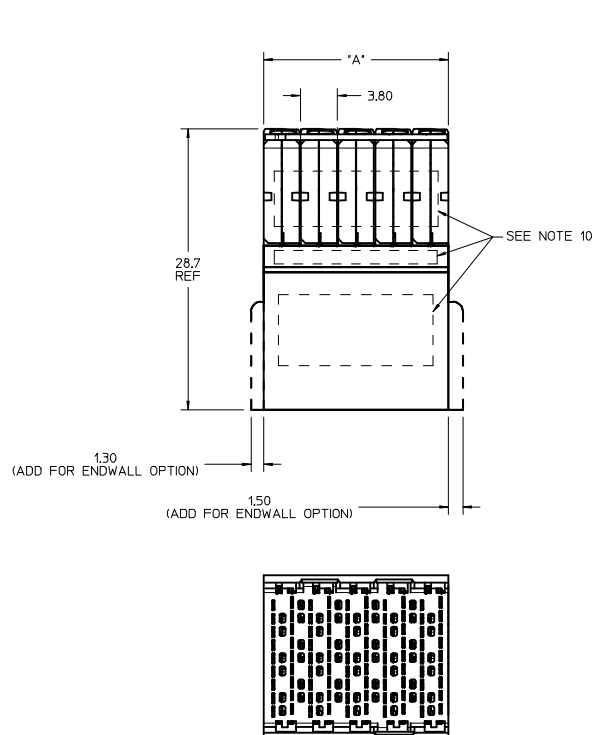
MODULE TYPE
1 = UNGUIDED, LEAD-FREE

OF COLUMNS
1 = 10 COLUMNS
8 = 8 COLUMNS

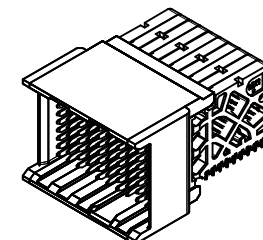
PIN LENGTH (P)
4 = 4.90 (0.46 PTH)
5 = 5.50 (0.46 PTH)
7 = 4.90 (0.39 PTH)
8 = 5.50 (0.39 PTH)

END WALL OPTIONS
0 = OPEN ENDS
1 = LEFT ENDWALL
2 = DUAL ENDWALL
3 = RIGHT ENDWALL

- NOTES:
1. MATERIALS: HOUSING - LIQUID CRYSTAL POLYMER (LCP), GLASS-FILLED, UL94V-0
TERMINALS - HIGH PERFORMANCE COPPER ALLOY
 2. FINISH: 304N SELECTIVE GOLD IN CONTACT AREA, SELECTIVE TIN ON PCB TAILS, NICKEL OVERALL.
 3. REFER TO MOLEX PRODUCT SPECIFICATION PS-170540-999 FOR PERFORMANCE SPECIFICATIONS AND ADDITIONAL PCB INFORMATION.
 4. EACH SIGNAL WAFER CONTAINS 2 COLUMNS OF TERMINALS.
 5. PRODUCT IS PACKAGED PER PK-70873-041.
 6. THIS PART CONFORMS TO CLASS B REQUIREMENTS OF COSMETIC SPEC PS-45499-002.
 7. REFER TO MOLEX SALES DRAWING SD-170340-001 FOR THE MATING RECEPTACLES.
 8. REFER TO MOLEX PCB ROUTING GUIDE AS-76060-990 FOR ANTIPAD AND ROUTING RECOMMENDATIONS.
 9. GROUND PATHS ARE NOT DISCRETE CIRCUITS.
 10. MARKING: LOCATED APPROXIMATELY AS SHOWN, PART NUMBER AND DATE CODE.

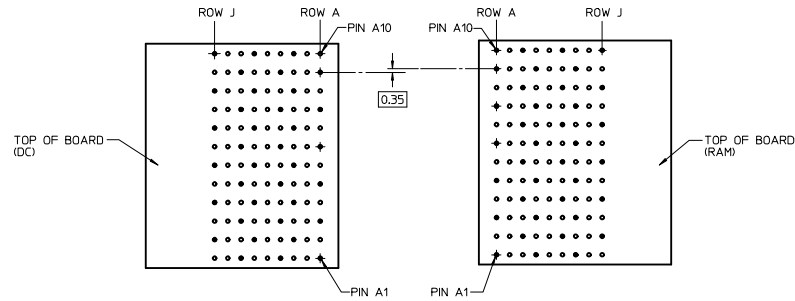


OPEN END OPTION



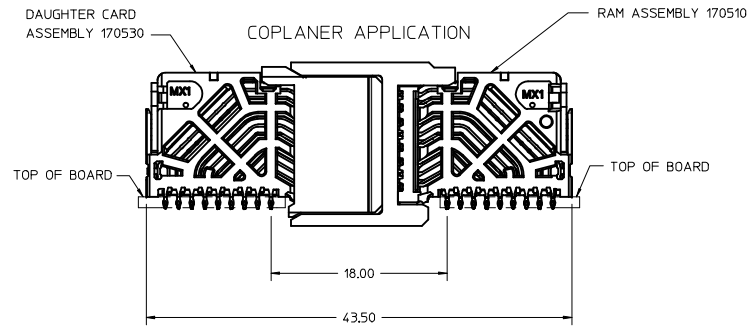
DUAL WALL OPTION

REV'D 12, 14, 16-COL. OPTIONS EC NO: UCP2017-2489 DRAWN: VARADARAJAN CHKD: TEL APPR: TEL DATE: 2017/05/17	QUALITY SYMBOLS ▽=0 ▽=0 ▽=0	GENERAL TOLERANCES (UNLESS SPECIFIED) mm INCH 4 PLACES ± .005 ± .005 3 PLACES ± .010 ± .010 2 PLACES ± .013 ± .013 1 PLACE ± .025 ± .025 0 PLACE ± .050 ± .050 ANGULAR ± 1/2° DRAFT WHERE APPLICABLE MUST REMAIN WITHIN DIMENSIONS	DIMENSION STYLE MM ONLY DATE MAY 2013/05/31 CHECKED BY TELO DATE 2016/02/22 APPROVED BY TELO DATE 2014/03/13	SCALE 4:1 DESIGN UNITS METRIC THIRD ANGLE PROJECTION	TITLE IMPACT 3P 85 OHM RAM SIGNAL MODULE ASSEMBLY UNGUIDED SALES DRAWING molex	MATERIAL NO. SEE CHART DOCUMENT NO. SD-170510-0002	SHEET NO. 1 OF 2



COPLANAR MATED SIGNAL PATHS

DC PIN A10-----RAM PIN A10
DC PIN A1-----RAM PIN A1



SEE SHEET 1 IEC NO: UCP2017-2489 DRAWN: VARARA CHKD: APPR: TELO DATE: 2017/05/17 SIZE: A3	QUALITY SYMBOLS	GENERAL TOLERANCES (UNLESS SPECIFIED)		DIMENSION STYLE MM ONLY		SCALE 4:1	DESIGN UNITS METRIC	THIRD ANGLE PROJECTION
	▽=0	mm	INCH	DRAWN BY MKRAUSE	DATE 2013/05/31	TITLE IMPACT 3P 85 OHM RAM SIGNAL MODULE ASSEMBLY UNGUIDED SALES DRAWING molex		
	▽=0	4 PLACES ±0	±---	CHECKED BY TELO	DATE 2016/02/22			
	▽=0	3 PLACES ±0	±---	APPROVED BY TELO	DATE 2014/03/13			
	▽=0	2 PLACES ±0.13	±---					
		1 PLACE ±0.25	±---					
		0 PLACE ±---	±---					
		ANGULAR ±1/2°		MATERIAL NO.		DOCUMENT NO.		SHEET NO.
		DRAFT WHERE APPLICABLE MUST REMAIN WITHIN DIMENSIONS		SEE CHART		SD-170510-0002		2 OF 2
				SIZE D THIS DRAWING CONTAINS INFORMATION THAT IS PROPRIETARY TO MOLEX INCORPORATED AND SHOULD NOT BE USED WITHOUT WRITTEN PERMISSION				