

Standard Recovery Diodes, (Stud Version), 400 A



PRIMARY CHARACTERISTICS			
I _{F(AV)} 400 A			
Package	DO-9 (DO-205AB)		
Circuit configuration	Single		

FEATURES

- Wide current range
- High voltage ratings up to 2400 V
- High surge current capabilities
- Stud cathode and stud anode version
- Standard JEDEC® types
- · Compression bonded encapsulations
- · Designed and qualified for industrial level
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

TYPICAL APPLICATIONS

- Converters
- Power supplies
- · Machine tool controls
- High power drives
- · Medium traction applications

MAJOR RATINGS AND CHARACTERISTICS				
PARAMETER	TEST CONDITIONS	VALUES	UNITS	
1		480	A	
I _{F(AV)}	T _C	120	°C	
I _{F(RMS)}		630		
I _{FSM}	50 Hz	8250	А	
	60 Hz	8640		
l ² t	50 Hz	340	kA ² s	
1-1	60 Hz	311		
V_{RRM}	Range	1600 to 2400	V	
TJ		-40 to +190	°C	

ELECTRICAL SPECIFICATIONS

VOLTAGE RATINGS						
TYPE NUMBER	VOLTAGE CODE	V _{RRM} , MAXIMUM REPETITIVE PEAK REVERSE VOLTAGE V	V _{RSM} , MAXIMUM NON-REPETITIVE PEAK REVERSE VOLTAGE V	$\begin{aligned} & I_{RRM} \text{ MAXIMUM} \\ \text{AT T}_{J} &= T_{J} \text{ MAXIMUM} \\ & \text{mA} \end{aligned}$		
	16	1600	1700			
VS-SD400N/R	20	2000	2100	15		
	24	2400	2500			



FORWARD CONDUCTION						
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS	
	I _{F(AV)}	190° conduction half size ways		400	Α	
Maximum average forward current				120	°C	
at case temperature		160 CONG	180° conduction, half sine wave		480	Α
					100	°C
Maximum RMS forward current	I _{F(RMS)}	DC at 110	°C case tempe	rature	630	
		t = 10 ms	No voltage	Sinusoidal half wave,	8250	
Maximum peak, one-cycle forward,		t = 8.3 ms	reapplied		8640	Α
non-repetitive surge current	I _{FSM}	t = 10 ms	100 % V _{RRM}		6940	
		t = 8.3 ms	reapplied		7270	
	l ² t	t = 10 ms	No voltage	T _J = T _J maximum	340	kA ² s
Maximum I ² t for fusing		t = 8.3 ms	reapplied		311	
waxiinum i-t ior rusing		t = 10 ms	100 % V _{RRM} reapplied		241	
		t = 8.3 ms		220		
Maximum $I^2\sqrt{t}$ for fusing	I ² √t	t = 0.1 to 10 ms, no voltage reapplied		3400	kA²√s	
Low level value of threshold voltage	V _{F(TO)1}	(16.7 % x π x $I_{F(AV)}$ < I < π x $I_{F(AV)}$), $I_J = I_J$ maximum		0.80	V	
High level value of threshold voltage	V _{F(TO)2}	$(I > \pi \times I_{F(AV)}), T_J = T_J \text{ maximum}$		0.85		
Low level value of forward slope resistance	r _{f1}	(16.7 % x π x $I_{F(AV)}$ < I < π x $I_{F(AV)}$), $T_J = T_J$ maximum		0.55	mW	
High level value of forward slope resistance	r _{f2}	$(I > \pi \times I_{F(AV)}), T_J = T_J \text{ maximum}$		0.51	IIIVV	
Maximum forward voltage drop	V _{FM}	$I_{pk} = 1500 \text{ A}, T_J = T_J \text{ maximum},$ $t_p = 10 \text{ ms sinusoidal wave}$		1.62	V	

THERMAL AND MECHANICAL SPECIFICATIONS					
PARAMETER	SYMBOL TEST CONDITIONS		VALUES	UNITS	
Maximum junction operating temperature range	ТЈ		-40 to +190	°C	
Maximum storage temperature range	T _{Stg}		-55 to +200		
Maximum thermal resistance, junction to case	R _{thJC}	DC operation	0.11	K/W	
Maximum thermal resistance, case to heatsink	R _{thCS}	Mounting surface, smooth, flat and greased	0.04	r\/ vv	
Maximum allowed mounting torque ± 10 %		Not-lubricated threads	27	Nm	
Approximate weight			250	g	
Case style		See dimensions (link at the end of datasheet		-205AB)	

△R _{thJC} CONDUCTION				
CONDUCTION ANGLE	SINUSOIDAL CONDUCTION	RECTANGULAR CONDUCTION	TEST CONDITIONS	UNITS
180°	0.020	0.013		
120°	0.023	0.023		
90°	0.029	0.031	$T_J = T_J$ maximum	K/W
60°	0.042	0.044		
30°	0.073	0.074		

Note

• The table above shows the increment of thermal resistance R_{thJC} when devices operate at different conduction angles than DC

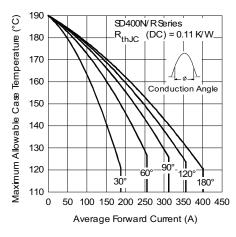


Fig. 1 - Current Ratings Characteristics

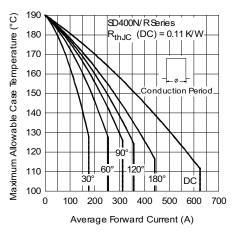


Fig. 2 - Current Ratings Characteristics

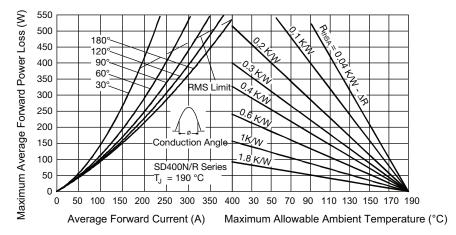


Fig. 3 - Forward Power Loss Characteristics

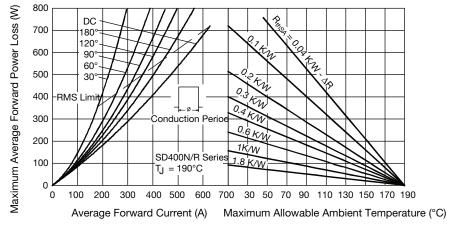


Fig. 4 - Forward Power Loss Characteristics

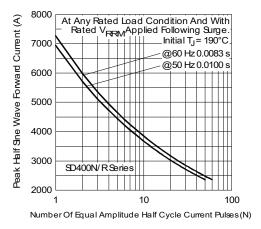


Fig. 5 - Maximum Non-Repetitive Surge Current

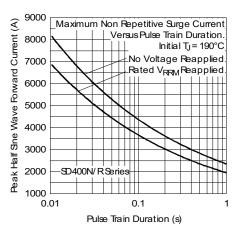


Fig. 6 - Maximum Non-Repetitive Surge Current

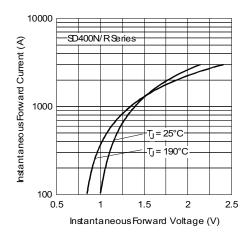


Fig. 7 - Forward Voltage Drop Characteristics

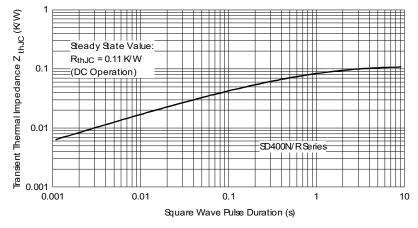
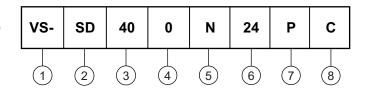


Fig. 8 - Thermal Impedance Z_{thJC} Characteristic



ORDERING INFORMATION TABLE

Device code

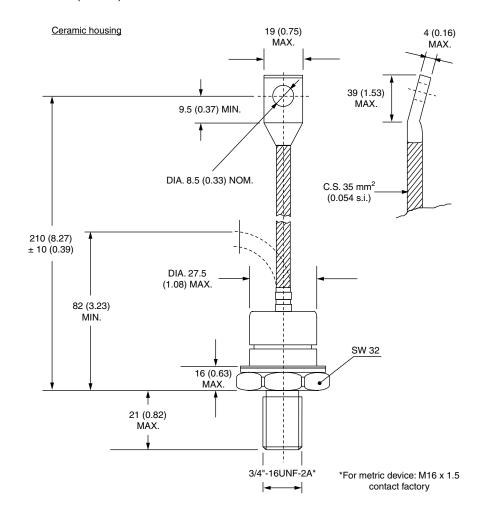


- 1 Vishay Semiconductors product
- 2 Diode
- 3 Essential part number
- 4 0 = standard recovery
- 5 • N = stud normal polarity (cathode to stud)
 - R = stud reverse polarity (anode to stud)
- 6 Voltage code x 100 = V_{RRM} (see Voltage Ratings table)
- 7 P = stud base DO-9 (DO-205AB) 3/4" 16UNF-2A
- 8 C = ceramic housing

LINKS TO RELATED DOCUMENTS				
Dimensions <u>www.vishay.com/doc?95301</u>				

DO-205AB (DO-9)

DIMENSIONS in millimeters (inches)





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